



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Sugon

SPECint®\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

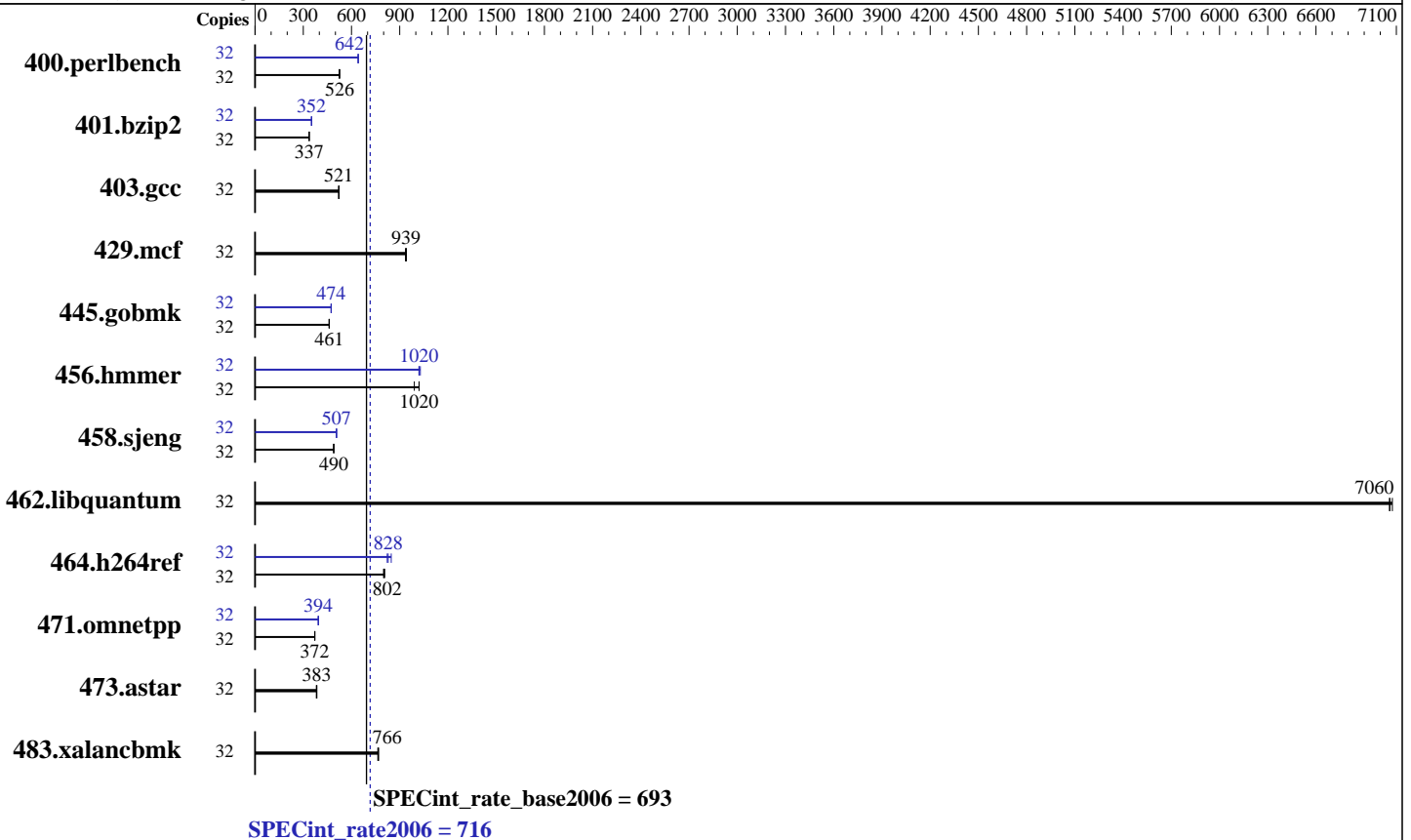
Test date: Nov-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2640 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 2.0 TB SATA 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Sugon

SPECint\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	594	526	598	523	<b>594</b>	<b>526</b>	32	487	642	<b>487</b>	<b>642</b>	487	641
401.bzip2	32	<b>917</b>	<b>337</b>	918	336	916	337	32	<b>878</b>	<b>352</b>	880	351	878	352
403.gcc	32	<b>494</b>	<b>521</b>	495	520	493	522	32	<b>494</b>	<b>521</b>	495	520	493	522
429.mcf	32	312	936	<b>311</b>	<b>939</b>	310	941	32	312	936	<b>311</b>	<b>939</b>	310	941
445.gobmk	32	<b>727</b>	<b>461</b>	728	461	727	461	32	<b>709</b>	<b>474</b>	708	474	710	473
456.hammer	32	<b>293</b>	<b>1020</b>	293	1020	301	991	32	293	1020	<b>292</b>	<b>1020</b>	290	1030
458.sjeng	32	793	489	790	490	<b>790</b>	<b>490</b>	32	<b>764</b>	<b>507</b>	763	507	766	506
462.libquantum	32	93.7	7080	94.0	7060	<b>93.9</b>	<b>7060</b>	32	93.7	7080	94.0	7060	<b>93.9</b>	<b>7060</b>
464.h264ref	32	878	807	886	800	<b>883</b>	<b>802</b>	32	861	822	836	847	<b>855</b>	<b>828</b>
471.omnetpp	32	539	371	<b>538</b>	<b>372</b>	538	372	32	509	393	507	394	<b>507</b>	<b>394</b>
473.astar	32	584	385	<b>587</b>	<b>383</b>	589	382	32	584	385	<b>587</b>	<b>383</b>	589	382
483.xalancbmk	32	<b>288</b>	<b>766</b>	289	764	287	769	32	<b>288</b>	<b>766</b>	289	764	287	769

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

Enforce POR set to Enable
Memory Frequency set to Auto
Early Snoop set to disabled
COD set to enable
Power Technology set to performance
Sysinfo program /home/cpu2006/config/sysinfo.rev6874
$Rev: 6874 $ $Date:: 2013-11-20 #$ 654bd3fcf53b06faef0efe54ed011998
running on localhost Sun Nov 9 07:15:18 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
2 "physical id"s (chips)
32 "processors"

```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Sugon

SPECint\_rate2006 = 716

### Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046  
Test sponsor: Sugon  
Tested by: Sugon

Test date: Nov-2014  
Hardware Availability: Sep-2014  
Software Availability: Nov-2013

### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264479460 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 7 10:38
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  1.8T  753G  970G  44% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 068 08/15/2014

Memory:  
16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1866 MHz  
8x NO DIMM NO DIMM

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1 > /proc/sys/vm/drop\_caches  
 runspec command invoked through numactl i.e.:  
 numactl --interleave=all runspec <etc>

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
 Submitted: Wed Nov 12 00:56:20 EST 2014  
 Submission: cpu2006-20141112-32778.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
 Submitted: Sun Nov 23 21:50:03 EST 2014  
 Submission: cpu2006-20141112-32778.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
 Submitted: Wed Nov 26 05:11:52 EST 2014  
 Submission: cpu2006-20141112-32778.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>  
 Submitted: Thu Nov 27 00:57:34 EST 2014  
 Submission: cpu2006-20141112-32778.sub

## Base Compiler Invocation

C benchmarks:  
 icc -m32

C++ benchmarks:  
 icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -opt-mem-layout-trans=3

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

Test date: Nov-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Nov-2013

## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

Test date: Nov-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Nov-2013

## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 716

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECint\_rate\_base2006 = 693

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Dec 3 10:29:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 December 2014.