



# SPEC® CFP2006 Result

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## Sugon

SPECfp®2006 = 100

Sugon I620-G20 (Intel Xeon E5-2685 v3)

SPECfp\_base2006 = 95.6

CPU2006 license: 9046

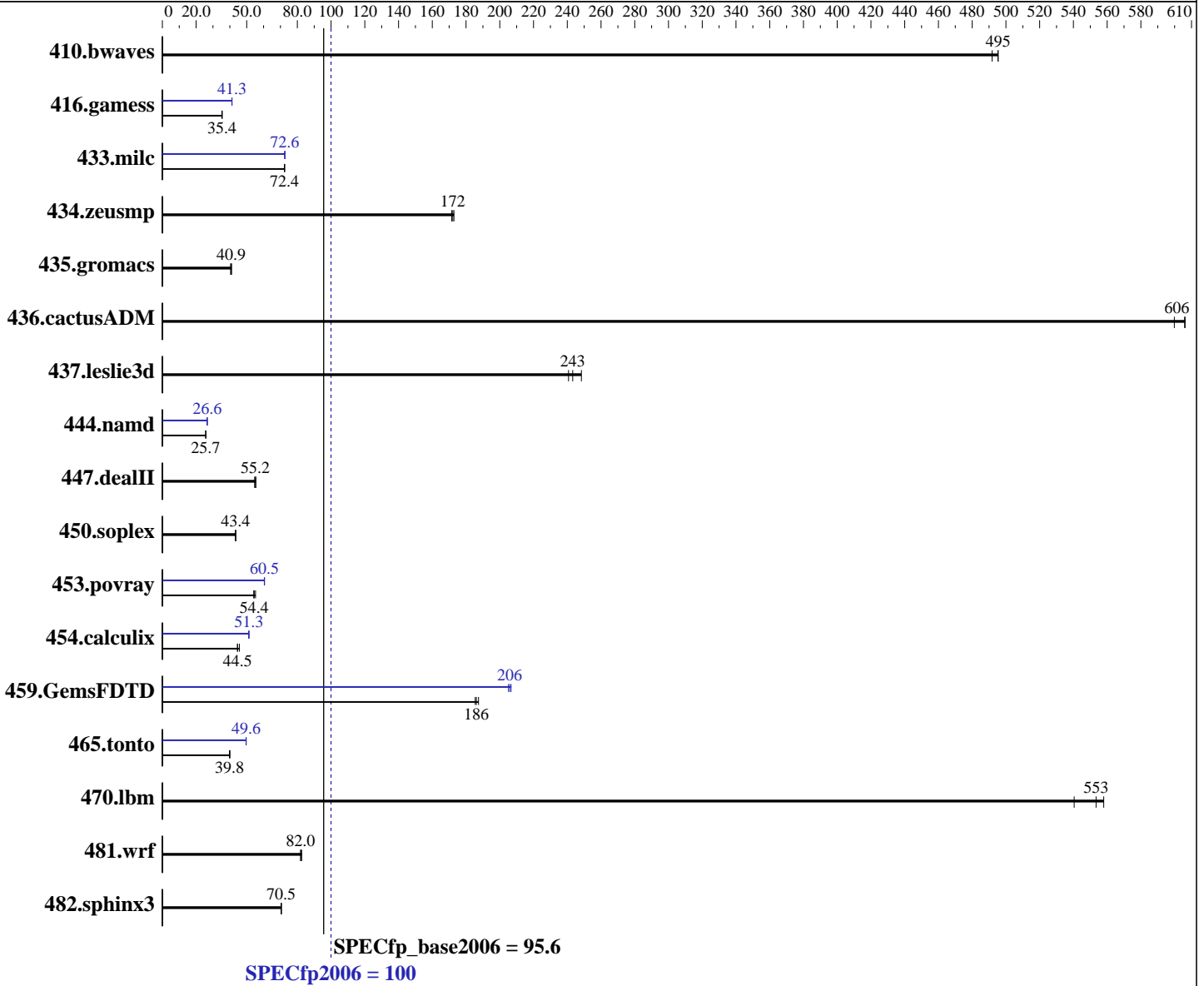
Test date: Nov-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Nov-2013



**Hardware**

CPU Name: Intel Xeon E5-2685 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 2.0 TB SATA 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b><u>27.4</u></b>	<b><u>495</u></b>	27.6	492	27.4	495	<b><u>27.4</u></b>	<b><u>495</u></b>	27.6	492	27.4	495
416.gamess	553	35.4	554	35.4	<b><u>554</u></b>	<b><u>35.4</u></b>	475	41.3	475	41.2	<b><u>475</u></b>	<b><u>41.3</u></b>
433.milc	127	72.3	127	72.5	<b><u>127</u></b>	<b><u>72.4</u></b>	127	72.6	126	72.6	<b><u>126</u></b>	<b><u>72.6</u></b>
434.zeusmp	52.7	173	<b><u>52.9</u></b>	<b><u>172</u></b>	53.1	172	52.7	173	<b><u>52.9</u></b>	<b><u>172</u></b>	53.1	172
435.gromacs	177	40.4	174	41.0	<b><u>175</u></b>	<b><u>40.9</u></b>	177	40.4	174	41.0	<b><u>175</u></b>	<b><u>40.9</u></b>
436.cactusADM	<b><u>19.7</u></b>	<b><u>606</u></b>	19.9	600	19.7	606	<b><u>19.7</u></b>	<b><u>606</u></b>	19.9	600	19.7	606
437.leslie3d	<b><u>38.6</u></b>	<b><u>243</u></b>	39.0	241	37.8	248	<b><u>38.6</u></b>	<b><u>243</u></b>	39.0	241	37.8	248
444.namd	312	25.7	<b><u>312</u></b>	<b><u>25.7</u></b>	311	25.8	302	26.6	302	26.6	<b><u>302</u></b>	<b><u>26.6</u></b>
447.dealII	<b><u>207</u></b>	<b><u>55.2</u></b>	206	55.4	209	54.7	<b><u>207</u></b>	<b><u>55.2</u></b>	206	55.4	209	54.7
450.soplex	<b><u>192</u></b>	<b><u>43.4</u></b>	192	43.4	192	43.4	<b><u>192</u></b>	<b><u>43.4</u></b>	192	43.4	192	43.4
453.povray	98.2	54.1	96.3	55.2	<b><u>97.7</u></b>	<b><u>54.4</u></b>	88.0	60.4	87.9	60.5	<b><u>87.9</u></b>	<b><u>60.5</u></b>
454.calculix	<b><u>185</u></b>	<b><u>44.5</u></b>	181	45.7	185	44.5	161	51.3	<b><u>161</u></b>	<b><u>51.3</u></b>	161	51.3
459.GemsFDTD	<b><u>57.0</u></b>	<b><u>186</u></b>	56.6	187	57.2	185	51.3	207	51.7	205	<b><u>51.5</u></b>	<b><u>206</u></b>
465.tonto	247	39.8	<b><u>247</u></b>	<b><u>39.8</u></b>	246	40.0	<b><u>198</u></b>	<b><u>49.6</u></b>	198	49.6	198	49.7
470.lbm	<b><u>24.8</u></b>	<b><u>553</u></b>	24.6	558	25.4	540	<b><u>24.8</u></b>	<b><u>553</u></b>	24.6	558	25.4	540
481.wrf	135	82.6	136	82.0	<b><u>136</u></b>	<b><u>82.0</u></b>	135	82.6	136	82.0	<b><u>136</u></b>	<b><u>82.0</u></b>
482.sphinx3	276	70.5	<b><u>277</u></b>	<b><u>70.5</u></b>	277	70.4	276	70.5	<b><u>277</u></b>	<b><u>70.5</u></b>	277	70.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:  
 Enforce POR set to disabled  
 DDR Speed set to 2133  
 Early Snoop set to disabled  
 COD set to disable  
 Power Technology set to performance  
 Sysinfo program /home/cpu2006/config/sysinfo.rev6818  
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
 running on localhost Fri Nov 14 10:32:02 2014

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### Sugon I620-G20 (Intel Xeon E5-2685 v3)

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**CPU2006 license:** 9046

**Test sponsor:** Sugon

**Tested by:** Sugon

**Test date:** Nov-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Nov-2013

## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2685 v3 @ 2.60GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 12
  siblings     : 12
  physical 0:  cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1:  cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 30720 KB

```

```

From /proc/meminfo
MemTotal:      264480248 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 14 05:50

```

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  1.8T   19G  1.7T   2% /

```

```

Additional information from dmidecode:
BIOS American Megatrends Inc. 067 08/13/2014
Memory:
 16x   16 GB
 8x NO DIMM NO DIMM
 16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Submitted\_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Mon Nov 24 19:24:05 EST 2014

Submission: cpu2006-20141116-32835.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Wed Nov 26 05:11:52 EST 2014

Submission: cpu2006-20141116-32835.sub

Submitted\_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Thu Nov 27 00:57:34 EST 2014

Submission: cpu2006-20141116-32835.sub

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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