



SPEC® CINT2006 Result

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NEC Corporation

SPECint®2006 = 34.9

Express5800/R120f-1M (Intel Xeon E5-2609 v3)

SPECint_base2006 = 33.7

CPU2006 license: 9006

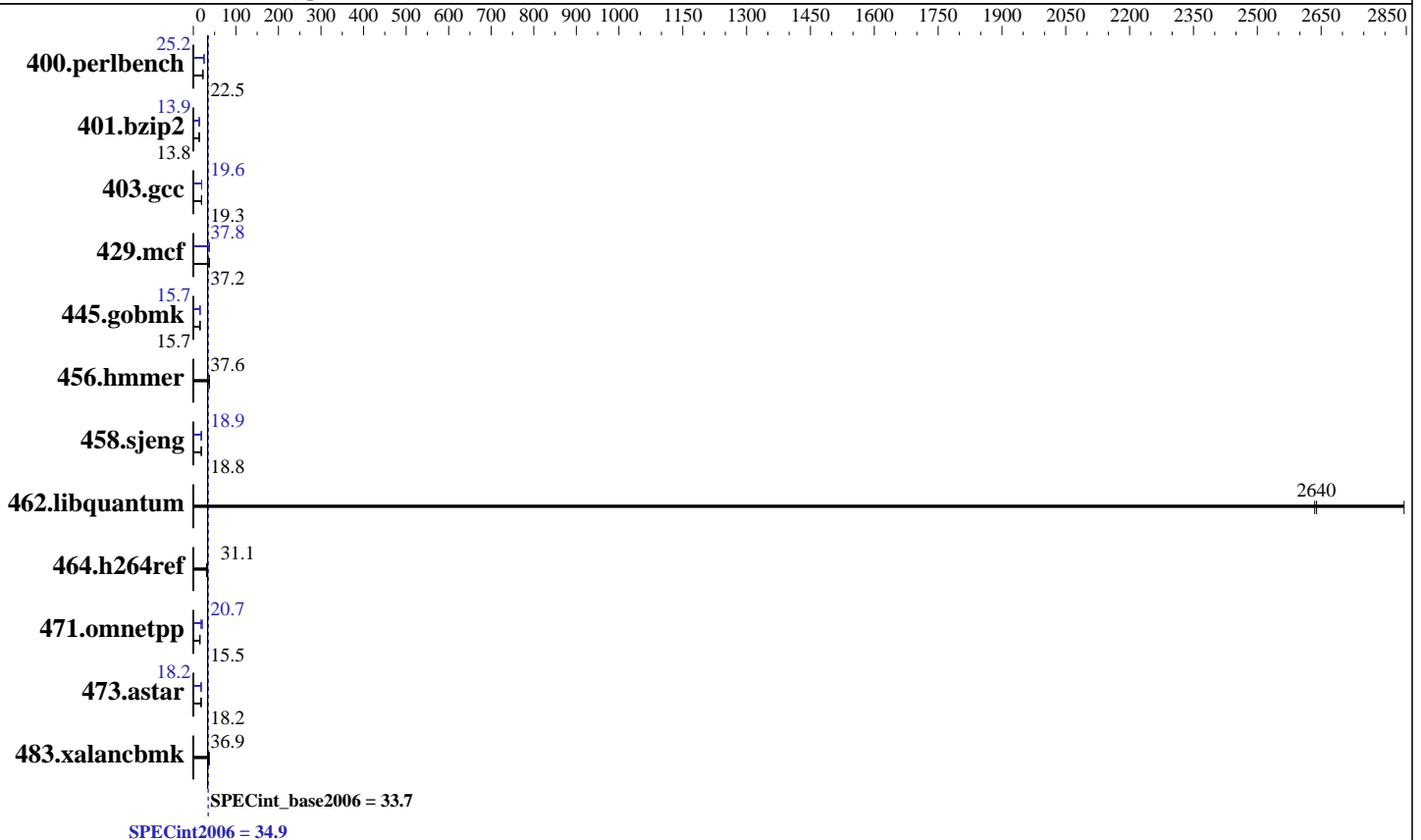
Test date: Nov-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2609 v3
 CPU Characteristics: 1900
 CPU MHz: Integrated
 FPU: 12 cores, 2 chips, 6 cores/chip
 CPU(s) enabled: 1,2 chips
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core
 Primary Cache: 256 KB I+D on chip per core
 Secondary Cache: 15 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.17.1.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	433	22.6	<u>434</u>	<u>22.5</u>	437	22.4	389	25.1	388	25.2	<u>388</u>	<u>25.2</u>
401.bzip2	704	13.7	<u>699</u>	<u>13.8</u>	699	13.8	<u>692</u>	<u>13.9</u>	692	13.9	694	13.9
403.gcc	416	19.4	416	19.3	<u>416</u>	<u>19.3</u>	<u>411</u>	<u>19.6</u>	411	19.6	411	19.6
429.mcf	<u>245</u>	<u>37.2</u>	241	37.8	247	36.9	<u>241</u>	<u>37.8</u>	240	38.0	243	37.5
445.gobmk	<u>669</u>	<u>15.7</u>	669	15.7	668	15.7	<u>666</u>	<u>15.7</u>	666	15.8	666	15.7
456.hammer	249	37.5	248	37.6	<u>248</u>	<u>37.6</u>	249	37.5	248	37.6	<u>248</u>	<u>37.6</u>
458.sjeng	<u>644</u>	<u>18.8</u>	644	18.8	644	18.8	640	18.9	663	18.2	<u>641</u>	<u>18.9</u>
462.libquantum	<u>7.85</u>	<u>2640</u>	7.86	2630	7.28	2840	<u>7.85</u>	<u>2640</u>	7.86	2630	7.28	2840
464.h264ref	712	31.1	<u>712</u>	<u>31.1</u>	713	31.1	712	31.1	<u>712</u>	<u>31.1</u>	713	31.1
471.omnetpp	<u>404</u>	<u>15.5</u>	404	15.5	411	15.2	<u>302</u>	<u>20.7</u>	351	17.8	302	20.7
473.astar	<u>386</u>	<u>18.2</u>	386	18.2	383	18.3	385	18.2	<u>386</u>	<u>18.2</u>	388	18.1
483.xalancbmk	185	37.2	<u>187</u>	<u>36.9</u>	192	35.9	185	37.2	<u>187</u>	<u>36.9</u>	192	35.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance
Patrol Scrub: Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "12"

The Express5800/R120f-1M (Intel Xeon E5-2609 v3) and the Express5800/R120f-2M (Intel Xeon E5-2609 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2609 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

Continued on next page



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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
 401.bzip2: `-DSPEC_CPU_LP64`
 403.gcc: `-DSPEC_CPU_LP64`
 429.mcf: `-DSPEC_CPU_LP64`
 456.hmmer: `-DSPEC_CPU_LP64`
 458.sjeng: `-DSPEC_CPU_LP64`
 462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
 464.h264ref: `-DSPEC_CPU_LP64`
 473.astar: `-DSPEC_CPU_LP64`
 483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `basepeak = yes`

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Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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