SPEC® CFP2006 Result
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Lenovo Group Limited

Lenovo ThinkServer TD350 (Intel Xeon E5-2660 v3, 2.60 GHz)

$SPECfp®\_rate2006 = 696$

$SPECfp\_rate\_base2006 = 679$

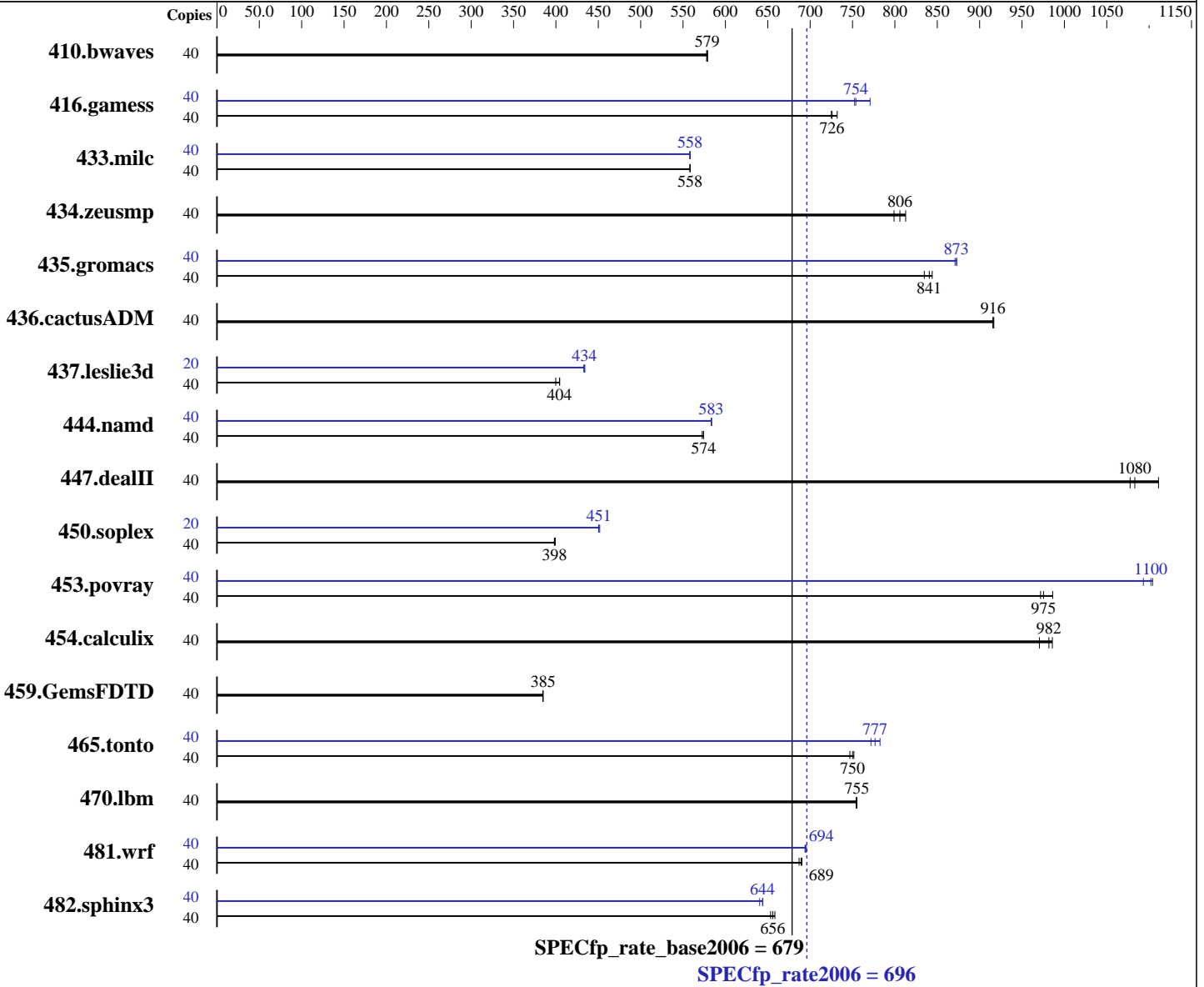|	
CPU2006 license: 9017	**Test date:** Nov-2014
Test sponsor: Lenovo Group Limited	**Hardware Availability:** Sep-2014
Tested by: Lenovo Group Limited	**Software Availability:** Jan-2014

Copies

Benchmark	Copies	Value
410.bwaves	40	579
416.gamess	40	754
416.gamess	40	726
433.milc	40	558
433.milc	40	558
434.zeusmp	40	806
435.gromacs	40	873
435.gromacs	40	841
436.cactusADM	40	916
437.leslie3d	20	434
437.leslie3d	40	404
444.namd	40	583
444.namd	40	574
447.dealII	40	1080
450.soplex	20	451
450.soplex	40	398
453.povray	40	1100
453.povray	40	975
454.calculix	40	982
459.GemsFDTD	40	385
465.tonto	40	777
465.tonto	40	750
470.lbm	40	755
481.wrf	40	694
481.wrf	40	689
482.sphinx3	40	644
482.sphinx3	40	656

SPECfp_rate_base2006 = 679

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Hardware

CPU Name:	Intel Xeon E5-2660 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago)
	2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
	Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	**940**	**579**	939	579	941	578	40	**940**	**579**	939	579	941	578
416.gamess	40	1070	732	1080	725	**1079**	**726**	40	1016	771	**1039**	**754**	1041	752
433.milc	40	658	558	**658**	**558**	658	558	40	658	558	658	558	**658**	**558**
434.zeusmp	40	448	813	456	799	**452**	**806**	40	448	813	456	799	**452**	**806**
435.gromacs	40	342	835	338	844	**340**	**841**	40	328	871	**327**	**873**	327	873
436.cactusADM	40	522	916	**522**	**916**	521	917	40	522	916	**522**	**916**	521	917
437.leslie3d	40	930	404	**930**	**404**	941	400	20	434	433	433	434	**434**	**434**
444.namd	40	560	573	**559**	**574**	559	574	40	549	584	550	583	**550**	**583**
447.dealII	40	425	1080	412	1110	**422**	**1080**	40	425	1080	412	1110	**422**	**1080**
450.soplex	40	836	399	838	398	**837**	**398**	20	370	450	**370**	**451**	369	452
453.povray	40	216	986	**218**	**975**	219	972	40	193	1100	195	1090	**193**	**1100**
454.calculix	40	335	986	340	971	**336**	**982**	40	335	986	340	971	**336**	**982**
459.GemsFDTD	40	**1103**	**385**	1103	385	1103	385	40	**1103**	**385**	1103	385	1103	385
465.tonto	40	527	747	524	752	**525**	**750**	40	**507**	**777**	510	772	503	782
470.lbm	40	728	755	**728**	**755**	729	754	40	728	755	**728**	**755**	729	754
481.wrf	40	647	690	**648**	**689**	650	687	40	**643**	**694**	642	696	644	694
482.sphinx3	40	1193	653	**1188**	**656**	1184	658	40	1210	644	**1211**	**644**	1217	640

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Cluster On Die set to Enabled
Early Snoop set to Disabled

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Platform Notes (Continued)

```
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to Max Performance
Memory Power Savings set to Disabled
 Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on TD350 Mon Nov 10 23:22:12 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
       2 "physical id"s (chips)
       40 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 10
       siblings  : 20
       physical 0: cores 0 1 2 3 4 8 9 10 11 12
       physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 12800 KB

 From /proc/meminfo
    MemTotal:       264412072 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)

 From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
    Linux TD350 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
    x86_64 x86_64 GNU/Linux

 run-level 3 Nov 10 23:21

 SPEC is set to: /usr/cpu2006
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2       ext4  730G   12G  682G   2% /

 Additional information from dmidecode:
    BIOS LENOVO TB5TS110 10/06/2014
```
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Platform Notes (Continued)

```
    Memory:
     16x   16 GB
     2x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 2133 MHz 2 rank
     14x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank

  (End of data from sysinfo program)
 TD350 support 4 channels and 8 DIMMs per processor, total 8 channels and
 16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
```

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Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014

Base Portability Flags (Continued)

```
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

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Hardware Availability: Sep-2014
Software Availability: Jan-2014

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
   433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
  444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
   481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
          -auto-ilp32

 470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
          -unroll2
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
          -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
          -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2)
          -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
          -ansi-alias
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

    410.bwaves: basepeak = yes

    416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
        -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
        -inline-level=0 -scalar-rep-

    434.zeusmp: basepeak = yes

    437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

  459.GemsFDTD: basepeak = yes

      465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
        -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
        -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

    435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
        -O3(pass 2) -no-prec-div(pass 2)
        -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
        -opt-prefetch -auto-ilp32

  436.cactusADM: basepeak = yes

    454.calculix: basepeak = yes

      481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TD350-revA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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