



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>\_rate2006 = 669

Huawei CH220 V3 (Intel Xeon E5-2667 v3)

SPECfp\_rate\_base2006 = 652

CPU2006 license: 3175

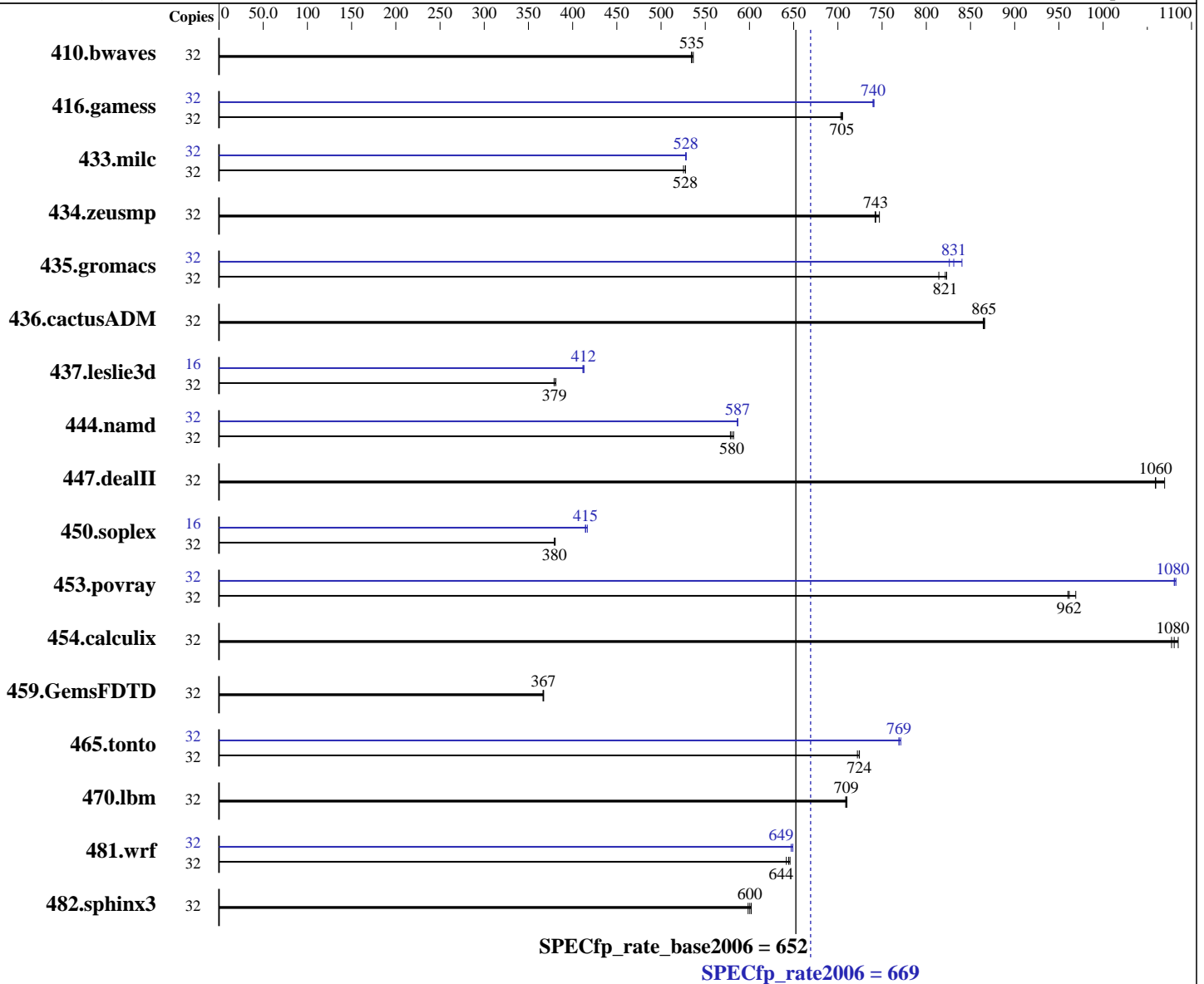
Test sponsor: Huawei

Tested by: Huawei

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2667 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	<u>813</u>	<u>535</u>	814	534	811	536	32	<u>813</u>	<u>535</u>	814	534	811	536
416.gamess	32	888	705	<u>889</u>	<u>705</u>	891	703	32	846	741	847	740	<u>847</u>	<u>740</u>
433.milc	32	<u>557</u>	<u>528</u>	559	525	557	528	32	<u>556</u>	<u>528</u>	556	528	556	529
434.zeusmp	32	<u>392</u>	<u>743</u>	392	742	390	747	32	<u>392</u>	<u>743</u>	392	742	390	747
435.gromacs	32	<u>278</u>	<u>821</u>	278	823	281	814	32	<u>275</u>	<u>831</u>	277	826	272	840
436.cactusADM	32	<u>442</u>	<u>865</u>	442	866	442	864	32	<u>442</u>	<u>865</u>	442	866	442	864
437.leslie3d	32	789	381	<u>793</u>	<u>379</u>	794	379	16	<u>365</u>	<u>412</u>	364	413	365	412
444.namd	32	441	582	444	578	<u>442</u>	<u>580</u>	32	437	587	438	586	<u>438</u>	<u>587</u>
447.dealII	32	<u>346</u>	<u>1060</u>	346	1060	342	1070	32	<u>346</u>	<u>1060</u>	346	1060	342	1070
450.soplex	32	702	380	<u>703</u>	<u>380</u>	703	379	16	<u>322</u>	<u>415</u>	322	415	320	417
453.povray	32	<u>177</u>	<u>962</u>	177	960	176	969	32	158	1080	157	1080	<u>157</u>	<u>1080</u>
454.calculix	32	245	1080	243	1080	<u>244</u>	<u>1080</u>	32	245	1080	243	1080	<u>244</u>	<u>1080</u>
459.GemsFDTD	32	<u>925</u>	<u>367</u>	926	366	925	367	32	<u>925</u>	<u>367</u>	926	366	925	367
465.tonto	32	435	724	<u>435</u>	<u>724</u>	436	722	32	<u>409</u>	<u>769</u>	409	769	408	771
470.lbm	32	<u>620</u>	<u>709</u>	619	710	620	709	32	<u>620</u>	<u>709</u>	619	710	620	709
481.wrf	32	553	646	<u>555</u>	<u>644</u>	557	642	32	551	649	552	647	<u>551</u>	<u>649</u>
482.sphinx3	32	<u>1039</u>	<u>600</u>	1036	602	1042	598	32	<u>1039</u>	<u>600</u>	1036	602	1042	598

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Snoop Mode to ES

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### Platform Notes (Continued)

```

Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Jan 29 19:14:38 2015

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 v3 @ 3.20GHz
 2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      263577520 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 29 09:42

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   445G  136G  309G  31% /

```

Additional information from dmidecode:

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.23 11/19/2014

Memory:

4x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz

4x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

4x Samsung M393A2G40DB0-CPB 16 GB 1 rank 2133 MHz

4x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

### C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

### Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

### Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

### C benchmarks:

```

icc -m64

```

### C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

```

### Fortran benchmarks:

```

ifort -m64

```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

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## Peak Optimization Flags (Continued)

450.soplex (continued):

-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4

-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2

-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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