



SPEC® CINT2006 Result

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Supermicro

SuperServer 2048U-RTR4
(X10QRH+ , Intel Xeon E5-4640 v3)

SPECint®_rate2006 = 1610

SPECint_rate_base2006 = 1550

CPU2006 license: 001176

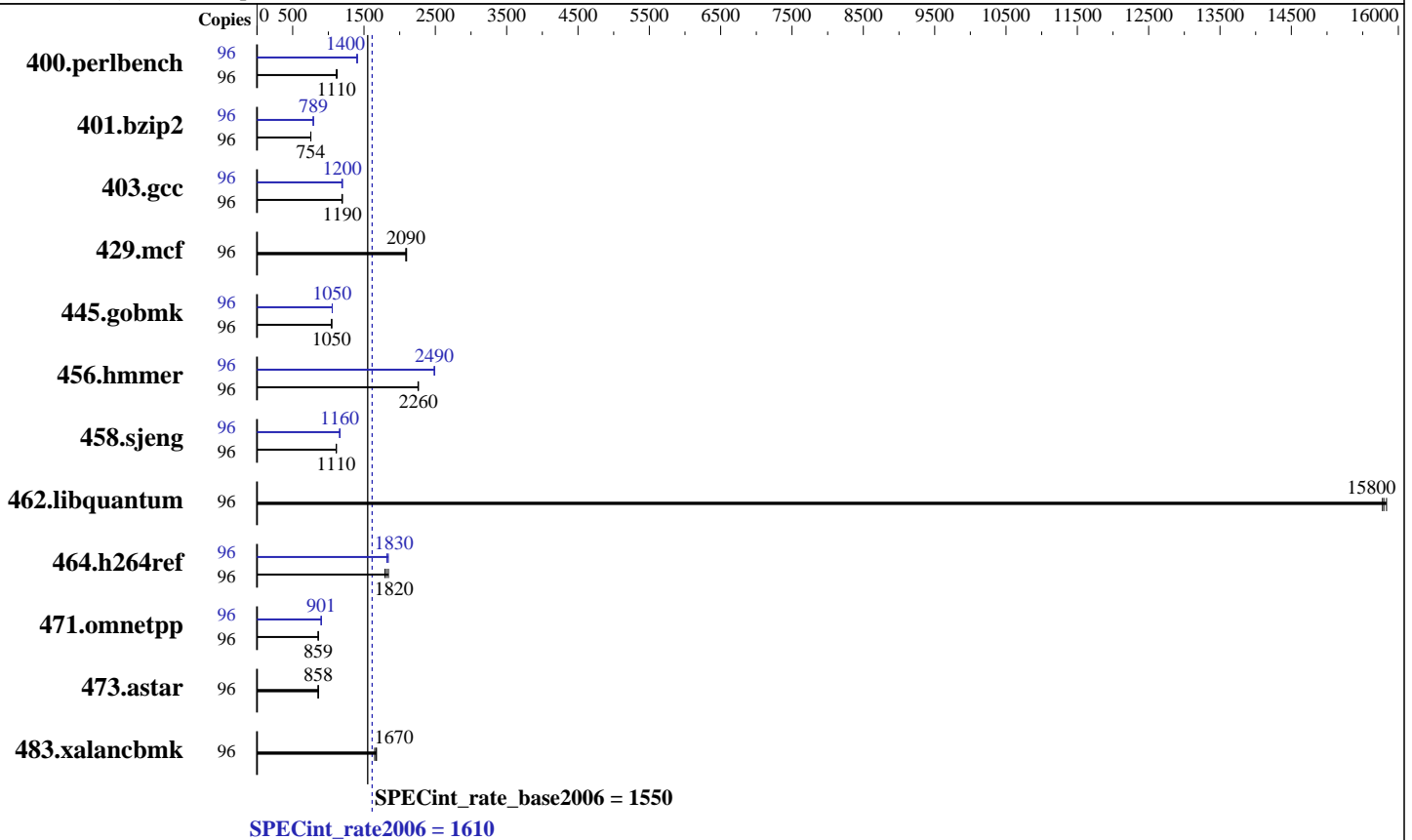
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-4640 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz
 CPU MHz: 1900
 FPU: Integrated
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 600 GB SATA II, SSD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12, Kernel 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	96	834	1120	<u>842</u>	<u>1110</u>	842	1110	96	669	1400	666	1410	<u>669</u>	<u>1400</u>
401.bzip2	96	1230	753	<u>1228</u>	<u>754</u>	1227	755	96	<u>1174</u>	<u>789</u>	1173	790	1174	789
403.gcc	96	646	1200	649	1190	<u>647</u>	<u>1190</u>	96	645	1200	<u>647</u>	<u>1200</u>	650	1190
429.mcf	96	417	2100	<u>419</u>	<u>2090</u>	420	2090	96	417	2100	<u>419</u>	<u>2090</u>	420	2090
445.gobmk	96	963	1050	<u>962</u>	<u>1050</u>	962	1050	96	955	1050	955	1060	<u>955</u>	<u>1050</u>
456.hammer	96	397	2260	396	2260	<u>396</u>	<u>2260</u>	96	360	2480	<u>360</u>	<u>2490</u>	360	2490
458.sjeng	96	1044	1110	<u>1045</u>	<u>1110</u>	1045	1110	96	1001	1160	<u>1001</u>	<u>1160</u>	1002	1160
462.libquantum	96	<u>126</u>	<u>15800</u>	126	15800	126	15800	96	<u>126</u>	<u>15800</u>	126	15800	126	15800
464.h264ref	96	1153	1840	<u>1170</u>	<u>1820</u>	1184	1790	96	<u>1162</u>	<u>1830</u>	1167	1820	1154	1840
471.omnetpp	96	<u>699</u>	<u>859</u>	700	857	698	859	96	<u>666</u>	<u>901</u>	666	901	667	900
473.astar	96	782	862	<u>786</u>	<u>858</u>	787	856	96	782	862	<u>786</u>	<u>858</u>	787	856
483.xalancbmk	96	401	1650	395	1680	<u>397</u>	<u>1670</u>	96	401	1650	395	1680	<u>397</u>	<u>1670</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
COD Enable = Enable
Early Snoop = Disable
Enforce POR = Disabled
Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on 18-216 Fri Jun 19 11:57:30 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4640 v3 @ 1.90GHz
4 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      529330024 kB
HugePages_Total:      0
Hugepagesize:   2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux 18-216 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Jun 19 11:48
```

```
SPEC is set to: /home/SPEC2K6/SPEC2006-V12
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  529G  7.5G  520G   2% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.00 05/28/2015
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Platform Notes (Continued)

Memory:

16x NO DIMM NO DIMM
7x Samsung(data:13/51) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz
3x Samsung(data:14/16) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz
8x Samsung(data:14/17) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz
6x Samsung(data:14/25) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz
8x Samsung(data:14/26) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzips2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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