



SPEC[®] CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp[®]_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

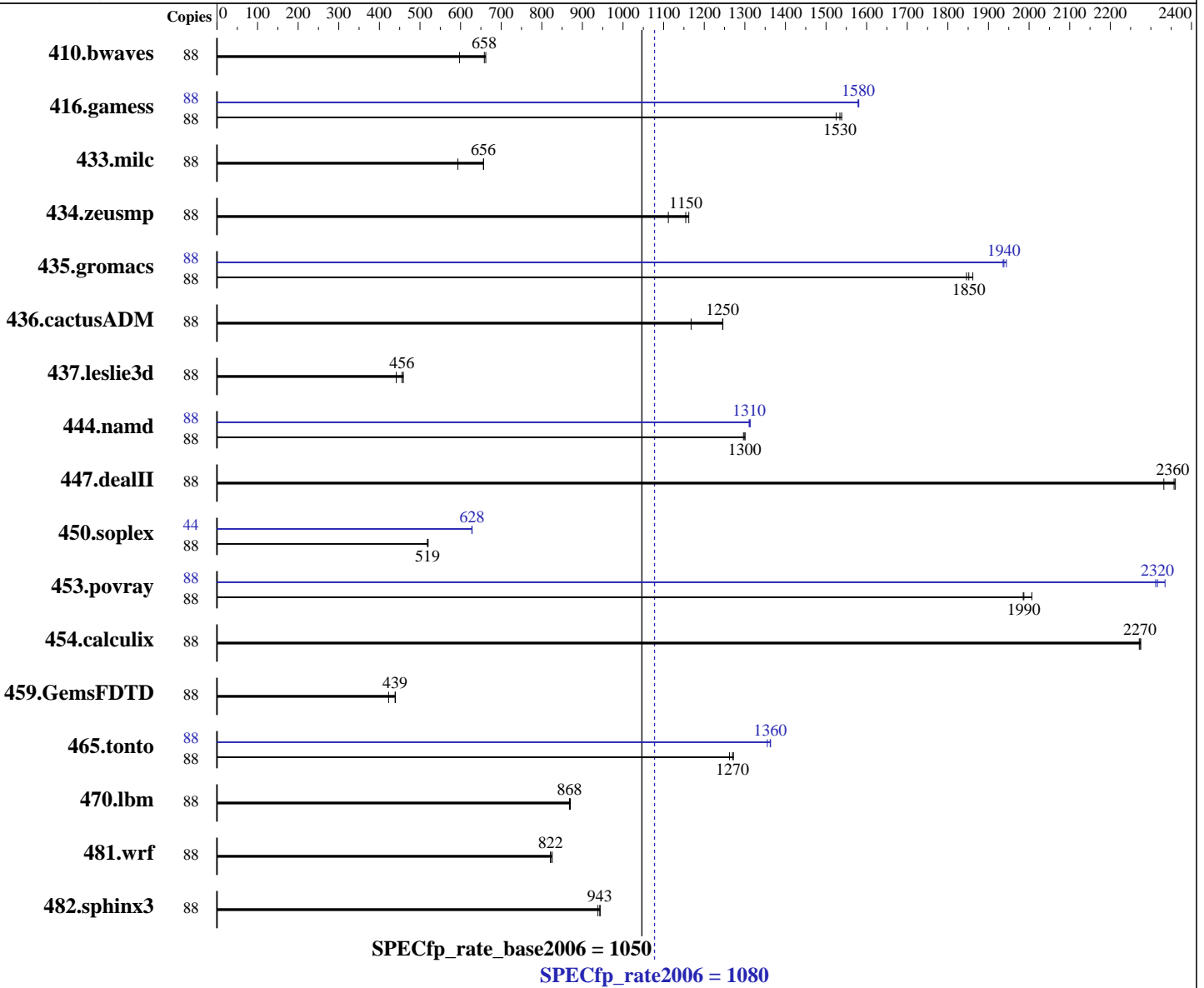
Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-2699 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
 3.10.0-229.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

L3 Cache: 55 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	88	2001	598	<u>1816</u>	<u>658</u>	1807	662	88	2001	598	<u>1816</u>	<u>658</u>	1807	662
416.gamess	88	1130	1530	1120	1540	<u>1123</u>	<u>1530</u>	88	1091	1580	1090	1580	<u>1091</u>	<u>1580</u>
433.milc	88	1361	593	1230	657	<u>1231</u>	<u>656</u>	88	1361	593	1230	657	<u>1231</u>	<u>656</u>
434.zeusmp	88	720	1110	689	1160	<u>694</u>	<u>1150</u>	88	720	1110	689	1160	<u>694</u>	<u>1150</u>
435.gromacs	88	338	1860	<u>339</u>	<u>1850</u>	340	1850	88	325	1940	323	1940	<u>324</u>	<u>1940</u>
436.cactusADM	88	901	1170	844	1250	<u>844</u>	<u>1250</u>	88	901	1170	844	1250	<u>844</u>	<u>1250</u>
437.leslie3d	88	1874	442	<u>1815</u>	<u>456</u>	1802	459	88	1874	442	<u>1815</u>	<u>456</u>	1802	459
444.namd	88	543	1300	<u>543</u>	<u>1300</u>	544	1300	88	538	1310	<u>538</u>	<u>1310</u>	537	1310
447.dealII	88	432	2330	<u>427</u>	<u>2360</u>	427	2360	88	432	2330	<u>427</u>	<u>2360</u>	427	2360
450.soplex	88	1416	518	<u>1414</u>	<u>519</u>	1411	520	44	585	628	<u>584</u>	<u>628</u>	584	628
453.povray	88	236	1990	<u>236</u>	<u>1990</u>	233	2010	88	203	2310	201	2330	<u>202</u>	<u>2320</u>
454.calculix	88	<u>319</u>	<u>2270</u>	319	2280	320	2270	88	<u>319</u>	<u>2270</u>	319	2280	320	2270
459.GemsFDTD	88	2209	423	2125	439	<u>2128</u>	<u>439</u>	88	2209	423	2125	439	<u>2128</u>	<u>439</u>
465.tonto	88	681	1270	686	1260	<u>682</u>	<u>1270</u>	88	635	1360	639	1360	<u>636</u>	<u>1360</u>
470.lbm	88	<u>1392</u>	<u>868</u>	1389	870	1393	868	88	<u>1392</u>	<u>868</u>	1389	870	1393	868
481.wrf	88	<u>1196</u>	<u>822</u>	1190	826	1197	821	88	<u>1196</u>	<u>822</u>	1190	826	1197	821
482.sphinx3	88	<u>1819</u>	<u>943</u>	1828	938	1817	944	88	<u>1819</u>	<u>943</u>	1828	938	1817	944

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Snoop Mode set to Cluster on Die
Virtualization Technology disabled

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Platform Notes (Continued)

```

System Profile set to Performance
Memory Patrol Scrub disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Sat Dec 5 21:49:22 2015

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
 2 "physical id"s (chips)
 88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 22
siblings       : 44
physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size     : 28160 KB

```

```

From /proc/meminfo
MemTotal:      528282496 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 5 09:09

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Platform Notes (Continued)

/dev/sda2 xfs 235G 7.9G 227G 4% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.7.10 11/22/2015

Memory:

16x 00CE00B300CE M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz
8x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Peak Optimization Flags (Continued)

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 1080

PowerEdge T630 (Intel Xeon E5-2699 v4, 2.20 GHz)

SPECfp_rate_base2006 = 1050

CPU2006 license: 55

Test date: Dec-2015

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Apr 5 14:54:37 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 April 2016.