



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C, Intel Xeon E5-2695 v4)

SPECfp®2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176

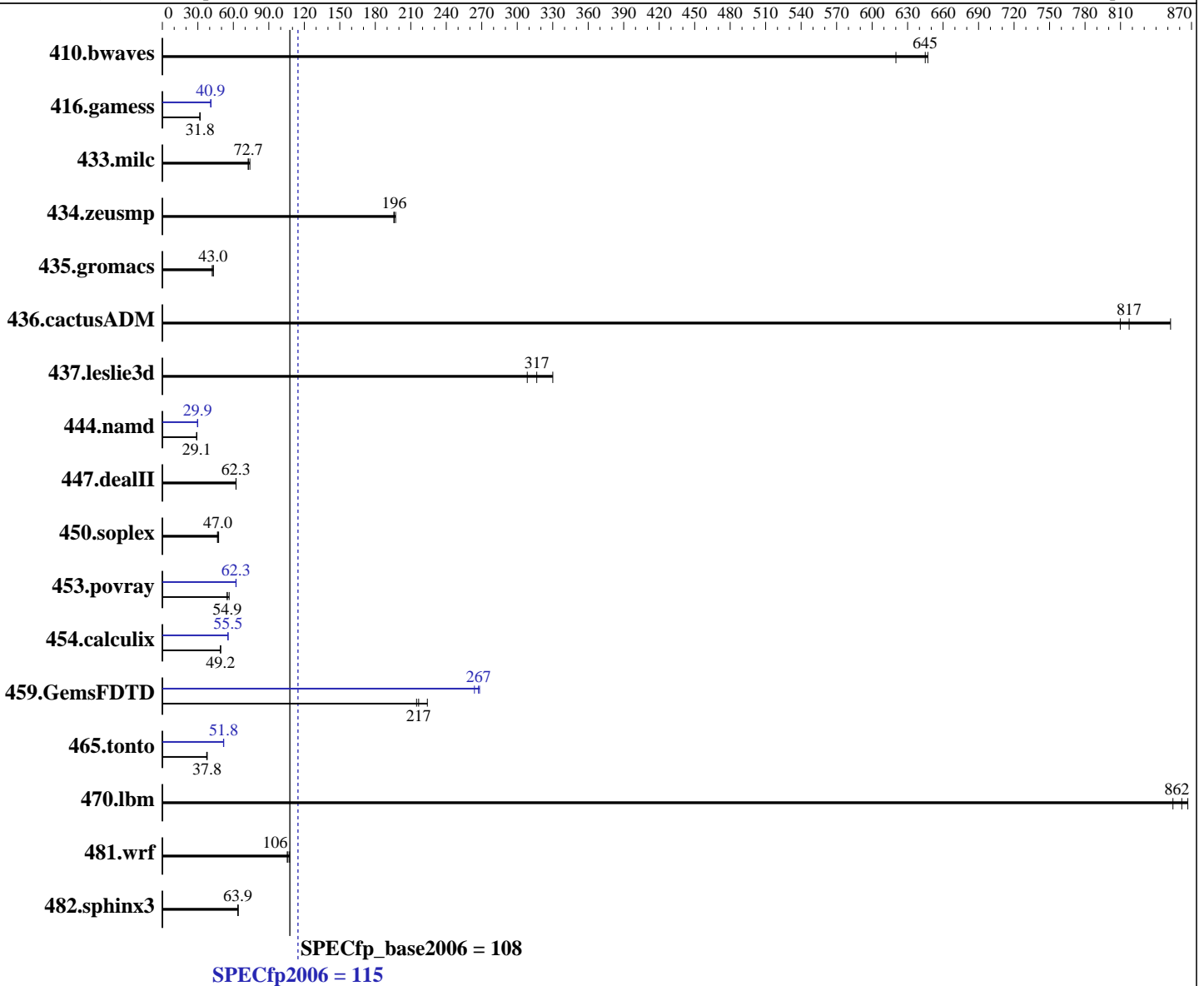
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E5-2695 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C, Intel Xeon E5-2695 v4)

SPECfp2006 = **115**

SPECfp_base2006 = **108**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015

L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 2 x 100 GB SAS SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	21.9	620	<u>21.1</u>	<u>645</u>	21.0	647	21.9	620	<u>21.1</u>	<u>645</u>	21.0	647
416.gamess	<u>615</u>	<u>31.8</u>	617	31.7	614	31.9	<u>479</u>	<u>40.9</u>	479	40.9	475	41.2
433.milc	<u>126</u>	<u>72.7</u>	124	74.0	126	72.6	<u>126</u>	<u>72.7</u>	124	74.0	126	72.6
434.zeusmp	46.2	197	<u>46.4</u>	<u>196</u>	46.5	196	46.2	197	<u>46.4</u>	<u>196</u>	46.5	196
435.gromacs	<u>166</u>	<u>43.0</u>	166	43.1	170	42.1	<u>166</u>	<u>43.0</u>	166	43.1	170	42.1
436.cactusADM	14.0	852	14.8	810	<u>14.6</u>	<u>817</u>	14.0	852	14.8	810	<u>14.6</u>	<u>817</u>
437.leslie3d	30.5	308	28.5	330	<u>29.7</u>	<u>317</u>	30.5	308	28.5	330	<u>29.7</u>	<u>317</u>
444.namd	<u>276</u>	<u>29.1</u>	276	29.1	276	29.0	271	29.6	268	29.9	<u>268</u>	<u>29.9</u>
447.dealII	183	62.3	<u>184</u>	<u>62.3</u>	184	62.2	183	62.3	<u>184</u>	<u>62.3</u>	184	62.2
450.soplex	179	46.6	<u>177</u>	<u>47.0</u>	175	47.6	179	46.6	<u>177</u>	<u>47.0</u>	175	47.6
453.povray	<u>96.9</u>	<u>54.9</u>	97.2	54.7	94.2	56.5	85.6	62.2	85.2	62.4	<u>85.4</u>	<u>62.3</u>
454.calculix	167	49.3	168	49.1	<u>168</u>	<u>49.2</u>	<u>149</u>	<u>55.5</u>	149	55.5	149	55.5
459.GemsFDTD	47.4	224	<u>49.0</u>	<u>217</u>	49.4	215	39.6	268	<u>39.7</u>	<u>267</u>	40.2	264
465.tonto	<u>260</u>	<u>37.8</u>	260	37.8	263	37.5	<u>190</u>	<u>51.8</u>	190	51.7	190	51.9
470.lbm	<u>15.9</u>	<u>862</u>	16.1	854	15.8	867	<u>15.9</u>	<u>862</u>	16.1	854	15.8	867
481.wrf	104	108	<u>105</u>	<u>106</u>	106	106	104	108	<u>105</u>	<u>106</u>	106	106
482.sphinx3	<u>305</u>	<u>63.9</u>	304	64.1	305	63.9	<u>305</u>	<u>63.9</u>	304	64.1	305	63.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Enforce POR = Disabled
Memory Frequency = 2400
Early Snoop = Disable
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on X10DRFF-01 Thu Mar 3 17:38:59 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C, Intel Xeon E5-2695 v4)

SPECfp2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
 2 "physical id"s (chips)
 72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 18
  siblings  : 36
  physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB

```

```

From /proc/meminfo
MemTotal:      131914388 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux X10DRFF-01 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 3 17:36

```

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   181G  6.5G 175G   4% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.0 01/11/2016
Memory:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C, Intel Xeon E5-2695 v4)

SPECfp2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

Platform Notes (Continued)

8x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "36"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C , Intel Xeon E5-2695 v4)

SPECfp2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C , Intel Xeon E5-2695 v4)

SPECfp2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015

Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-alloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer F618R2-FC0
(X10DRFF-C , Intel Xeon E5-2695 v4)

SPECfp2006 = 115

SPECfp_base2006 = 108

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jun 30 12:43:31 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 April 2016.