SPEC® CINT2006 Result
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EXO S.A.
SERVER EXO SPRINT X385

$SPECint^®\_rate2006 =$ Not Run

$SPECint\_rate\_base2006 =$ 160

Benchmark	Copies	Result
400.perlbench	4	143
401.bzip2	4	81.2
403.gcc	4	125
429.mcf	4	201
445.gobmk	4	111
456.hmmer	4	255
458.sjeng	4	127
462.libquantum	4	1520
464.h264ref	4	233
471.omnetpp	4	66.7
473.astar	4	76.2
483.xalancbmk	4	143

SPECint_rate_base2006 = 160

Hardware
CPU Name: Intel Xeon E3-1231 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 1Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 2x1TB SATA 7200 RPM Raid 1
Other Hardware: None

Software
Operating System: Ubuntu 12.04 LTS
3.2.0-23-generic
Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None

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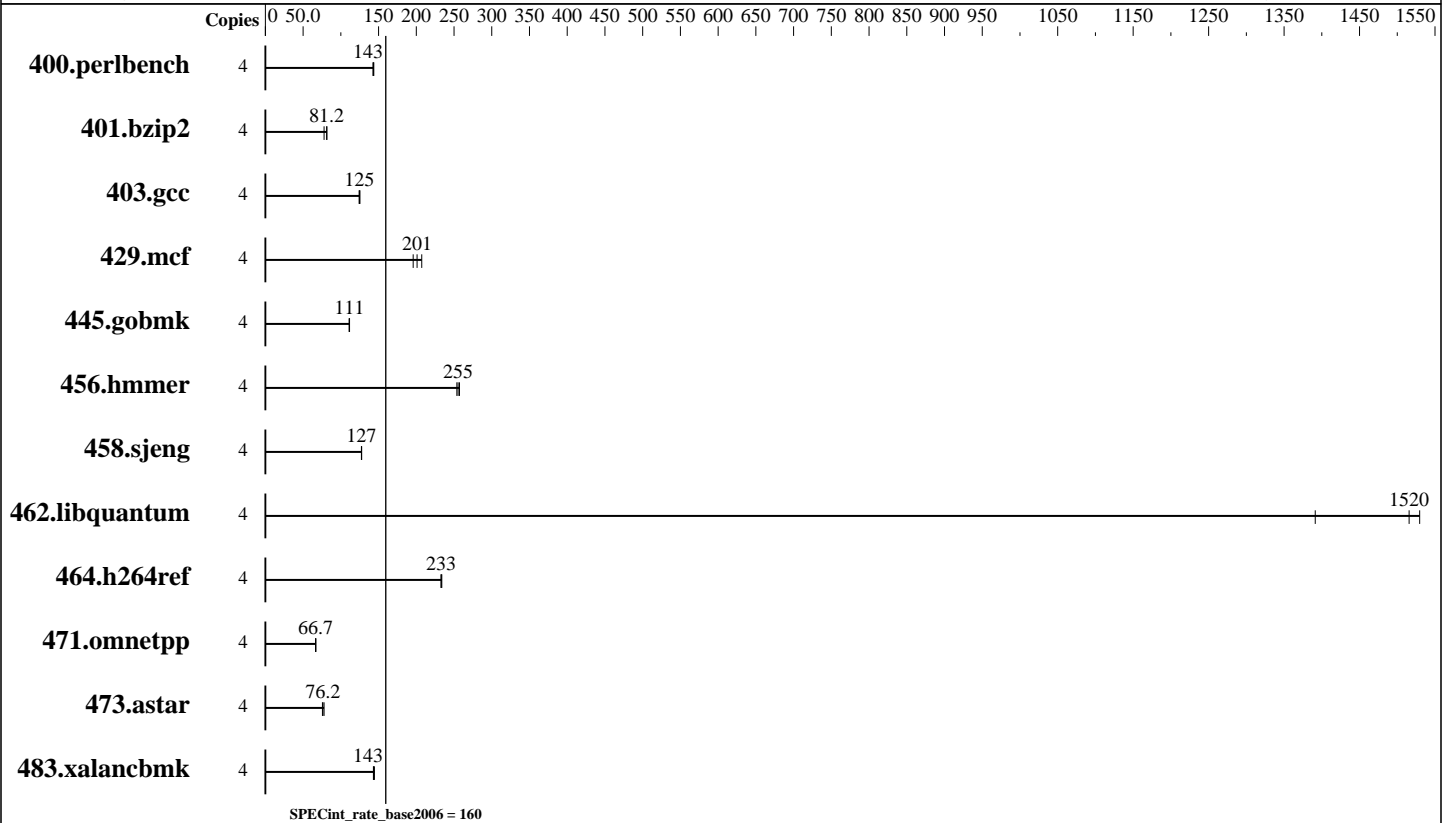SPECint_rate_base2006 = 160

CPU2006 license: 4691
Test sponsor: EXO S.A.
Tested by: EXO S.A.

Test date: Feb-2016
Hardware Availability: Jun-2015
Software Availability: Nov-2013

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	272	144	274	143	**273**	**143**							
401.bzip2	4	473	81.6	496	77.8	**475**	**81.2**							
403.gcc	4	**258**	**125**	257	125	259	124							
429.mcf	4	176	207	**181**	**201**	186	196							
445.gobmk	4	378	111	376	112	**377**	**111**							
456.hmmer	4	145	257	**146**	**255**	147	254							
458.sjeng	4	**380**	**127**	379	128	380	127							
462.libquantum	4	54.2	1530	**54.7**	**1520**	59.6	1390							
464.h264ref	4	380	233	**380**	**233**	379	234							
471.omnetpp	4	**375**	**66.7**	376	66.4	374	66.8							
473.astar	4	361	77.8	371	75.6	**368**	**76.2**							
483.xalancbmk	4	193	143	**192**	**143**	191	145							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
    BIOS Settings:
      Hyper-Threading Disabled.

    Sysinfo program /usr/cpu2006/Docs/sysinfo
    $Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
    running on ubuntu Tue Feb 23 09:02:07 2016

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E3-1231 v3 @ 3.40GHz
          1 "physical id"s (chips)
          4 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 4
          siblings  : 4
          physical 0: cores 0 1 2 3
       cache size : 8192 KB

    From /proc/meminfo
       MemTotal:        8113744 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB
```

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Platform Notes (Continued)

```
/usr/bin/lsb_release -d
   Ubuntu 12.04 LTS

From /etc/*release* /etc/*version*
   debian_version: wheezy/sid

uname -a:
   Linux ubuntu 3.2.0-23-generic #36-Ubuntu SMP Tue Apr 10 20:39:51 UTC 2012
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 23 00:13 last=2

SPEC is set to: /usr/cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  899G   35G  820G   5% /

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
```

Base Compiler Invocation

C benchmarks:
```
   icc  -m32
```

C++ benchmarks:
```
   icpc -m32
```

Base Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_IA32
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
   -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
   -opt-mem-layout-trans=3
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/EXO-platform-flags-HSW-v1.1.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/EXO-platform-flags-HSW-v1.1.xml

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For other inquiries, please contact webmaster@spec.org.

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