



# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp®\_rate2006 = 346

SPECfp\_rate\_base2006 = 340

CPU2006 license: 9017

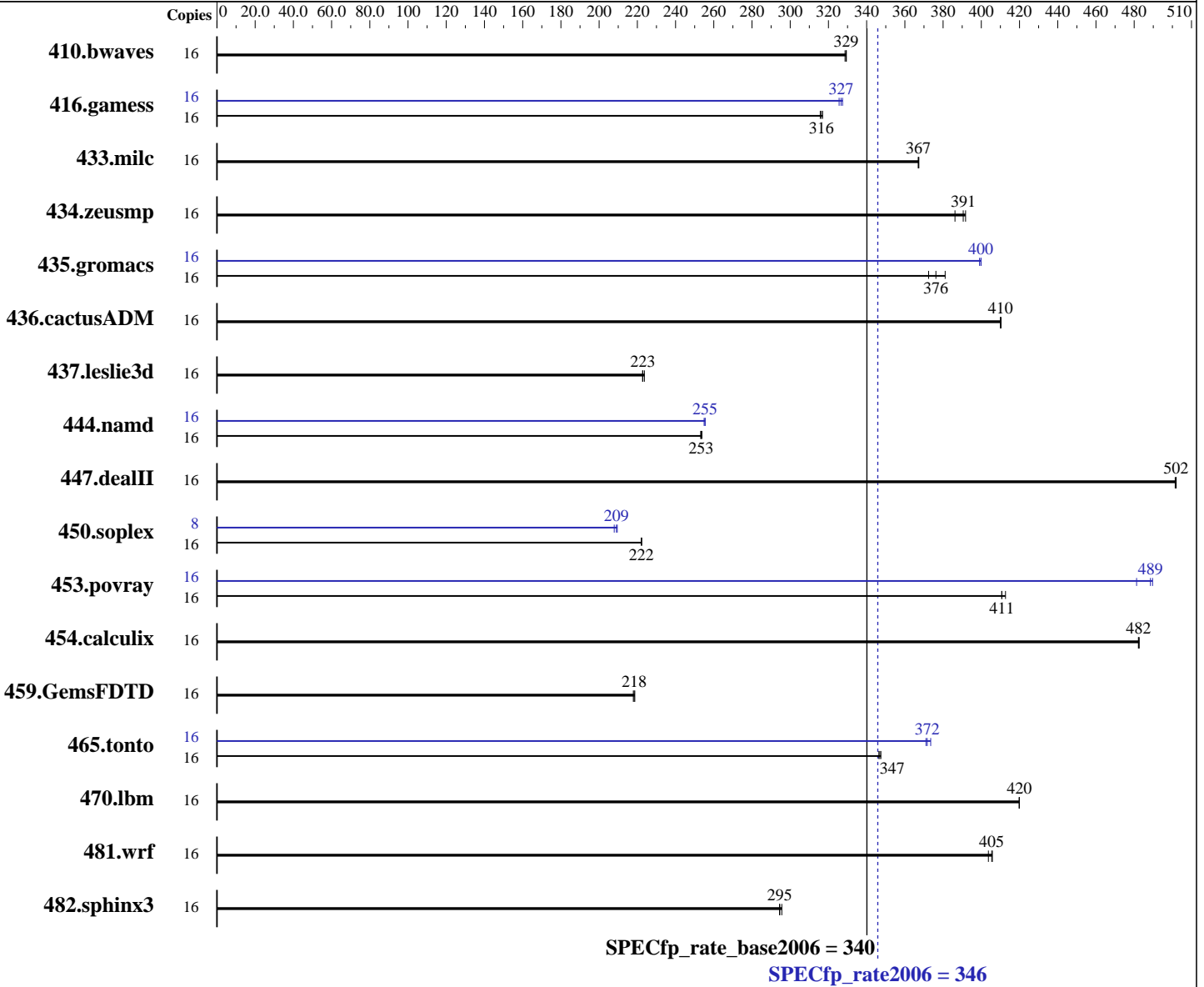
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2623 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 346

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp\_rate\_base2006 = 340

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	16	<b>660</b>	<b>329</b>	662	329	660	329	16	<b>660</b>	<b>329</b>	662	329	660	329		
416.gamess	16	<b>990</b>	<b>316</b>	988	317	992	316	16	962	326	<b>959</b>	<b>327</b>	957	327		
433.milc	16	400	367	<b>400</b>	<b>367</b>	400	367	16	400	367	<b>400</b>	<b>367</b>	400	367		
434.zeusmp	16	377	386	372	392	<b>373</b>	<b>391</b>	16	377	386	372	392	<b>373</b>	<b>391</b>		
435.gromacs	16	<b>304</b>	<b>376</b>	300	381	307	372	16	<b>286</b>	<b>400</b>	286	399	286	400		
436.cactusADM	16	466	410	466	410	<b>466</b>	<b>410</b>	16	466	410	466	410	<b>466</b>	<b>410</b>		
437.leslie3d	16	672	224	675	223	<b>675</b>	<b>223</b>	16	672	224	675	223	<b>675</b>	<b>223</b>		
444.namd	16	506	254	507	253	<b>506</b>	<b>253</b>	16	<b>502</b>	<b>255</b>	503	255	502	255		
447.dealII	16	365	501	365	502	<b>365</b>	<b>502</b>	16	365	501	365	502	<b>365</b>	<b>502</b>		
450.soplex	16	601	222	601	222	<b>601</b>	<b>222</b>	8	<b>319</b>	<b>209</b>	321	208	318	209		
453.povray	16	<b>207</b>	<b>411</b>	206	413	207	411	16	177	481	174	490	<b>174</b>	<b>489</b>		
454.calculix	16	<b>274</b>	<b>482</b>	274	482	273	483	16	<b>274</b>	<b>482</b>	274	482	273	483		
459.GemsFDTD	16	779	218	<b>778</b>	<b>218</b>	776	219	16	779	218	<b>778</b>	<b>218</b>	776	219		
465.tonto	16	453	347	<b>454</b>	<b>347</b>	455	346	16	424	371	<b>424</b>	<b>372</b>	422	374		
470.lbm	16	<b>524</b>	<b>420</b>	523	420	524	420	16	<b>524</b>	<b>420</b>	523	420	524	420		
481.wrf	16	443	404	<b>441</b>	<b>405</b>	440	406	16	443	404	<b>441</b>	<b>405</b>	440	406		
482.sphinx3	16	1055	296	<b>1059</b>	<b>295</b>	1059	294	16	1055	296	<b>1059</b>	<b>295</b>	1059	294		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 346

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp\_rate\_base2006 = 340

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Platform Notes

### BIOS Configuration:

Cluster On Die set to Disabled  
Early Snoop set to Disabled  
Performance Profile set to Custom  
ClE Support set to Disabled  
Core C3 set to Disabled  
Core C6 set to Disabled  
Thermal Profile set to High Fan Speed  
Memory Power Savings set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on rd550-mlk-rackA02 Sat May 7 03:07:22 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2623 v4@ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores      : 4
siblings       : 8
physical 0:    cores 0 1 2 3
physical 1:    cores 0 1 2 3
cache size     : 10240 KB
```

### From /proc/meminfo

```
MemTotal:      264558044 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

### From /etc/\*release\* /etc/\*version\*

#### SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

#### os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp\_rate2006 = 346

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp\_rate\_base2006 = 340

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

### Platform Notes (Continued)

```
uname -a:
Linux rd550-mlk-rackA02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 6 16:37
```

```
SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   690G  7.8G  683G   2% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB1TS362 03/24/2016

Memory:

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz  
8x NO DIMM NO DIMM

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

### Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 346**

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECfp\_rate\_base2006 = 340**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 346**

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECfp\_rate\_base2006 = 340**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp\_rate2006 = 346**

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECfp\_rate\_base2006 = 340**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD550  
(2.60 GHz, Intel Xeon E5-2623 v4)

**SPECfp\_rate2006 = 346**

**SPECfp\_rate\_base2006 = 340**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jun 30 14:05:36 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 June 2016.