



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

SPECint®\_rate2006 = 832

SPECint\_rate\_base2006 = 792

CPU2006 license: 001176

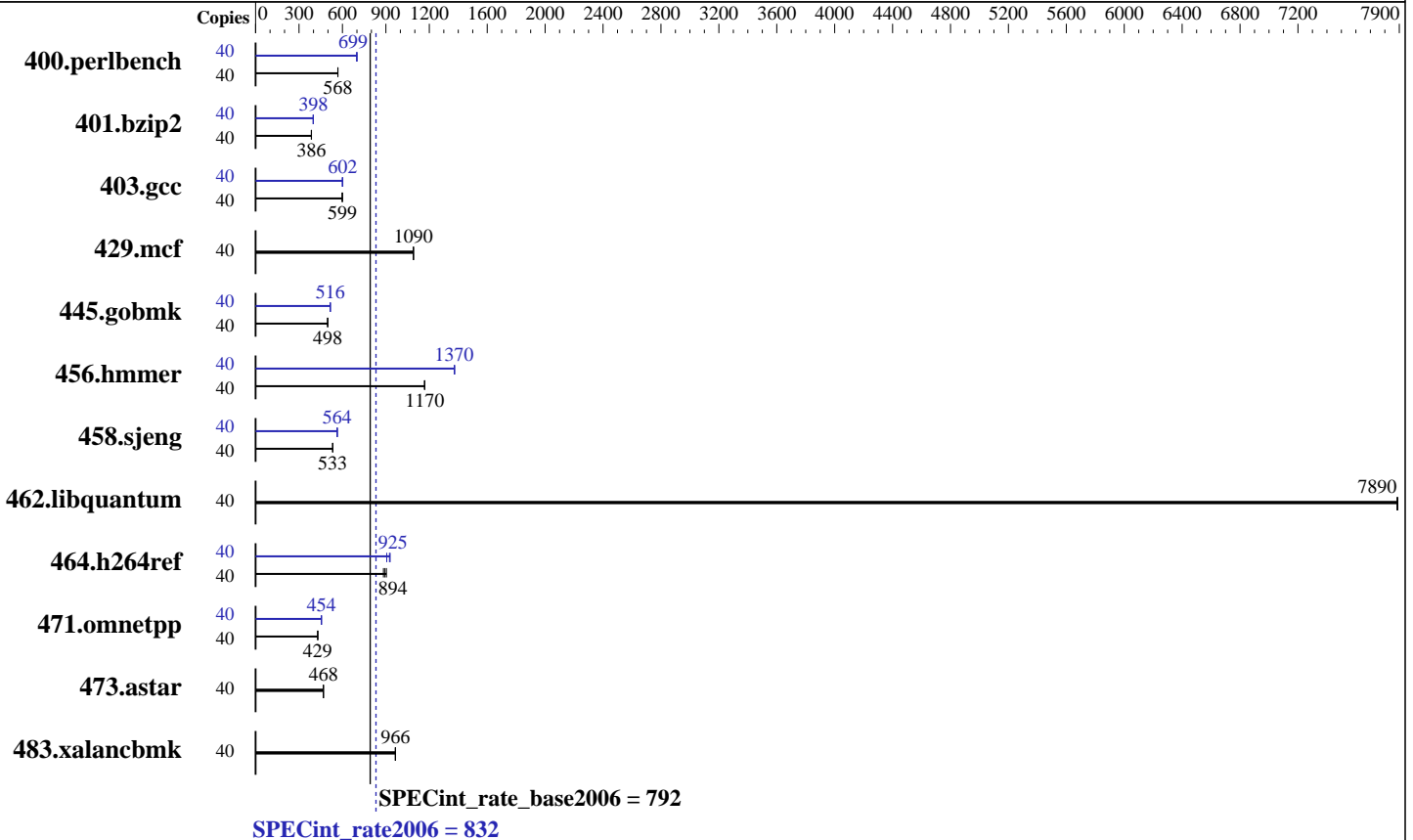
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E5-2618L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 400 GB SATA III SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

SPECint\_rate2006 = 832

SPECint\_rate\_base2006 = 792

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Sep-2015

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	687	569	<b>687</b>	<b>568</b>	688	568	40	558	701	<b>559</b>	<b>699</b>	560	698
401.bzip2	40	1000	386	1003	385	<b>1001</b>	<b>386</b>	40	<b>969</b>	<b>398</b>	968	399	970	398
403.gcc	40	<b>537</b>	<b>599</b>	537	600	539	597	40	538	598	<b>535</b>	<b>602</b>	535	602
429.mcf	40	334	1090	335	1090	<b>335</b>	<b>1090</b>	40	334	1090	335	1090	<b>335</b>	<b>1090</b>
445.gobmk	40	<b>842</b>	<b>498</b>	842	498	843	498	40	<b>813</b>	<b>516</b>	813	516	813	516
456.hammer	40	320	1170	319	1170	<b>319</b>	<b>1170</b>	40	271	1380	<b>272</b>	<b>1370</b>	272	1370
458.sjeng	40	907	534	910	532	<b>909</b>	<b>533</b>	40	860	563	<b>859</b>	<b>564</b>	856	565
462.libquantum	40	105	7890	<b>105</b>	<b>7890</b>	105	7880	40	105	7890	<b>105</b>	<b>7890</b>	105	7880
464.h264ref	40	<b>991</b>	<b>894</b>	1002	883	980	904	40	952	929	<b>957</b>	<b>925</b>	977	906
471.omnetpp	40	583	429	<b>582</b>	<b>429</b>	582	429	40	549	455	550	454	<b>550</b>	<b>454</b>
473.astar	40	601	467	<b>600</b>	<b>468</b>	598	469	40	601	467	<b>600</b>	<b>468</b>	598	469
483.xalancbmk	40	286	967	<b>286</b>	<b>966</b>	286	965	40	286	967	<b>286</b>	<b>966</b>	286	965

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Early Snoop = Disable

Sysinfo program /home/cpu2006\_ic16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Fri May 27 08:29:46 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2618L v4 @ 2.20GHz

2 "physical id"s (chips)

40 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

**SPECint\_rate2006 = 832**

**SPECint\_rate\_base2006 = 792**

**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

**Test date:** May-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Sep-2015

### Platform Notes (Continued)

```
cpu cores : 10
siblings  : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      263860080 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 27 08:29
```

```
SPEC is set to: /home/cpu2006_ic16
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      216G      4.4G  212G   3% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 2.0 02/26/2016
Memory:
8x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz, configured at 2133 MHz
```

(End of data from sysinfo program)



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

SPECint\_rate2006 = 832

SPECint\_rate\_base2006 = 792

**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

**Test date:** May-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Sep-2015

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006\_ic16/libs/32:/home/cpu2006\_ic16/libs/64:/home/cpu2006\_ic16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin  
C++ benchmarks:  
icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarheap



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

SPECint\_rate2006 = 832

SPECint\_rate\_base2006 = 792

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Sep-2015

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

SPECint\_rate2006 = 832

SPECint\_rate\_base2006 = 792

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015

## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6018R-TDTP  
(X10DRD-LTP , Intel Xeon E5-2618L v4)

**SPECint\_rate2006 = 832**

**SPECint\_rate\_base2006 = 792**

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Sep-2015

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jun 30 14:07:19 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 June 2016.