



SPEC[®] CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp[®]_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

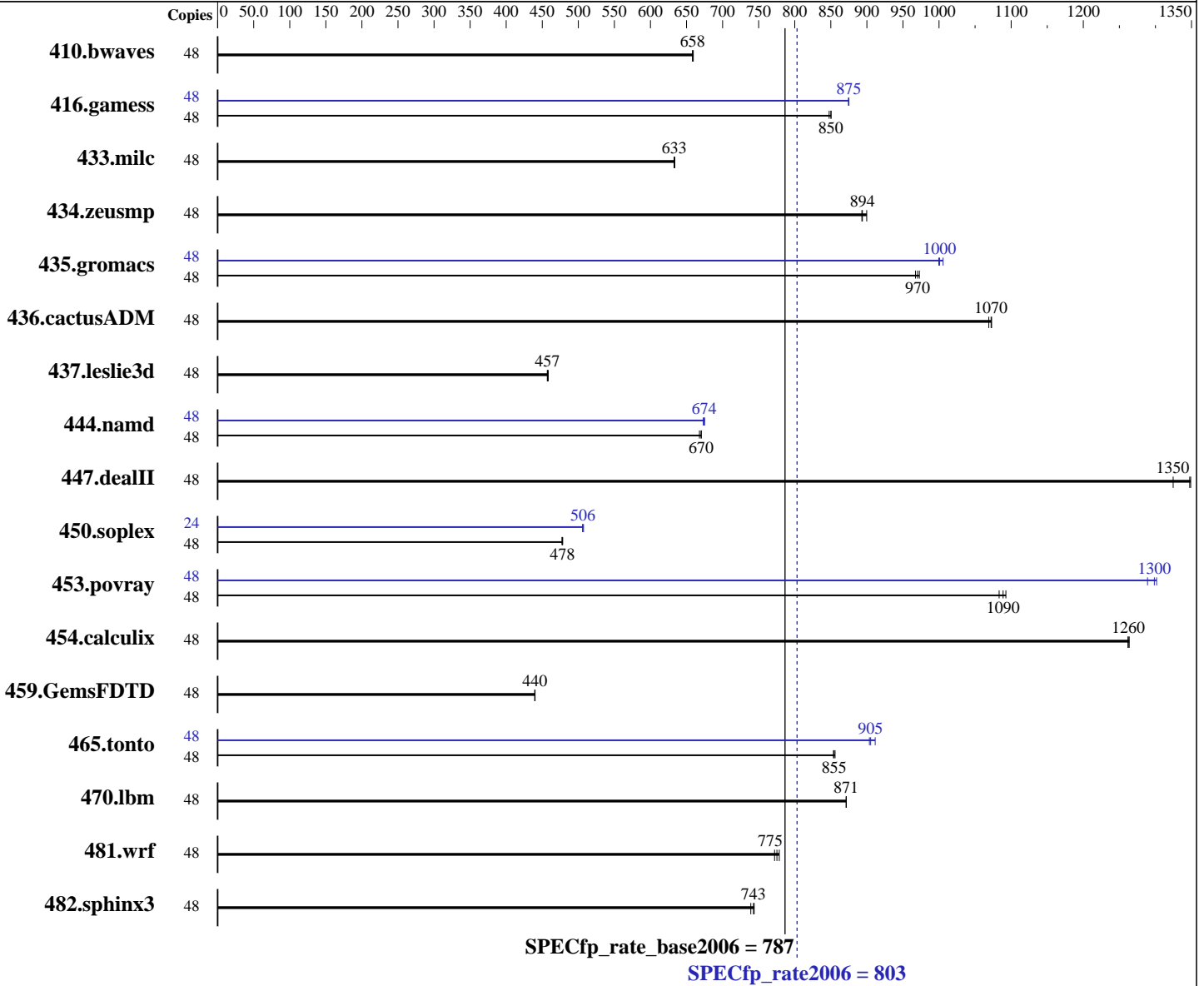
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-2650 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = **803**

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = **787**

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 2 x 300 GB SAS, 15000 RPM, RAID1
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	<u>991</u>	<u>658</u>	991	658	989	659	48	<u>991</u>	<u>658</u>	991	658	989	659
416.gamess	48	<u>1105</u>	<u>850</u>	1105	850	1109	848	48	<u>1074</u>	<u>875</u>	1074	875	1075	874
433.milc	48	696	633	<u>696</u>	<u>633</u>	695	634	48	696	633	<u>696</u>	<u>633</u>	695	634
434.zeusmp	48	<u>489</u>	<u>894</u>	485	900	489	893	48	<u>489</u>	<u>894</u>	485	900	489	893
435.gromacs	48	354	967	<u>353</u>	<u>970</u>	352	973	48	341	1010	<u>342</u>	<u>1000</u>	343	1000
436.cactusADM	48	535	1070	<u>535</u>	<u>1070</u>	537	1070	48	535	1070	<u>535</u>	<u>1070</u>	537	1070
437.leslie3d	48	985	458	987	457	<u>987</u>	<u>457</u>	48	985	458	987	457	<u>987</u>	<u>457</u>
444.namd	48	574	670	<u>574</u>	<u>670</u>	576	668	48	570	675	<u>571</u>	<u>674</u>	572	673
447.dealII	48	407	1350	415	1320	<u>408</u>	<u>1350</u>	48	407	1350	415	1320	<u>408</u>	<u>1350</u>
450.soplex	48	837	478	839	477	<u>837</u>	<u>478</u>	24	395	507	396	506	<u>395</u>	<u>506</u>
453.povray	48	236	1080	234	1090	<u>235</u>	<u>1090</u>	48	198	1290	196	1300	<u>197</u>	<u>1300</u>
454.calculix	48	313	1260	314	1260	<u>314</u>	<u>1260</u>	48	313	1260	314	1260	<u>314</u>	<u>1260</u>
459.GemsFDTD	48	1157	440	<u>1158</u>	<u>440</u>	1159	439	48	1157	440	<u>1158</u>	<u>440</u>	1159	439
465.tonto	48	553	854	<u>553</u>	<u>855</u>	552	856	48	518	912	<u>522</u>	<u>905</u>	523	904
470.lbm	48	757	872	<u>757</u>	<u>871</u>	757	871	48	757	872	<u>757</u>	<u>871</u>	757	871
481.wrf	48	689	779	694	772	<u>692</u>	<u>775</u>	48	689	779	694	772	<u>692</u>	<u>775</u>
482.sphinx3	48	1266	739	<u>1260</u>	<u>743</u>	1258	744	48	1266	739	<u>1260</u>	<u>743</u>	1258	744

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Patrol Scrub = Disable
Per Core P-state = Disable

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

Platform Notes (Continued)

COD Preference = Enable

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on rhel722 Sat Jun 11 05:18:27 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
2 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB

From /proc/meminfo
MemTotal: 527319964 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux rhel722 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 10 17:07

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 225G 24G 201G 11% /home

Additional information from dmidecode:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HITACHI 10-00 01/29/2016

Memory:

8x NO DIMM Unknown

16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 are electronically equivalent.

The results have been measured on a Hitachi Compute Blade 520H.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

Peak Optimization Flags (Continued)

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml>



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 803

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECfp_rate_base2006 = 787

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Mar-2016

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 28 17:31:08 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 June 2016.