



SPEC® CFP2006 Result

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ASUSTeK Computer Inc.
(Test Sponsor: Intel Corporation)

SPECfp®_rate2006 = 151

ASUS Q170M-C motherboard (Intel Core i5-6500T)

SPECfp_rate_base2006 = 148

CPU2006 license: 13

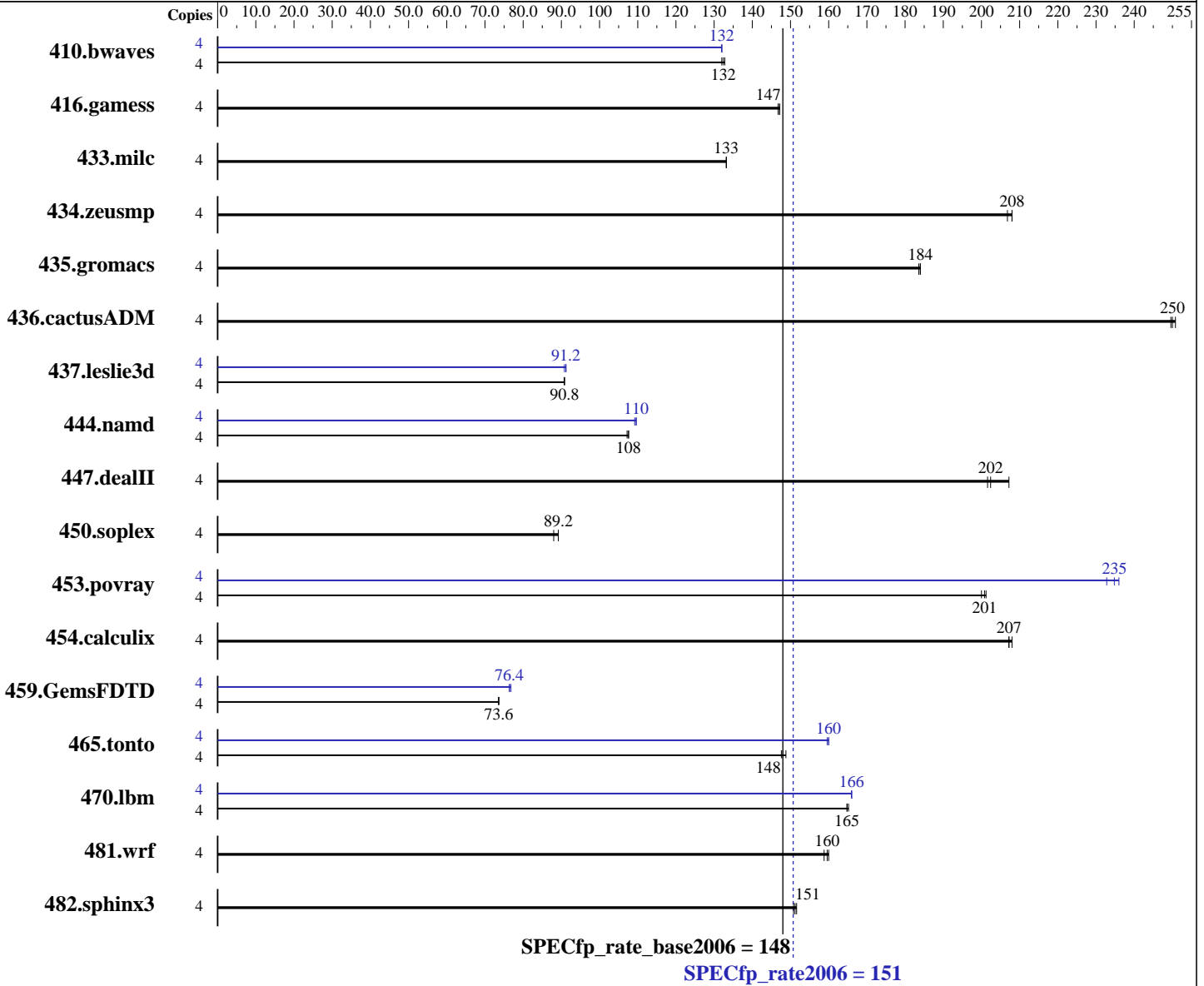
Test date: May-2016

Test sponsor: Intel Corporation

Hardware Availability: Sep-2015

Tested by: Intel Corporation

Software Availability: Aug-2015



Hardware

CPU Name: Intel Core i5-6500T
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Microsoft Windows 7 Professional
 6.1.7601 Service Pack 1 Build 7601
 Compiler: C/C++: Version 16.0.0.110 of Intel C++ Studio XE
 for Windows;
 Fortran: Version 16.0.0.110 of Intel Fortran
 Studio XE for Windows;
 Libraries: Version 18.00.30723 of Microsoft
 Visual Studio 2013
 Auto Parallel: No

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx4 PC4-2133P-U)
Disk Subsystem: 1 TB Seagate Barracuda HDD, 7200 RPM
Other Hardware: None

File System: NTFS
System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 11.0 from <http://www.microquill.com/>

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	409	133	<u>411</u>	<u>132</u>	411	132	4	412	132	411	132	<u>412</u>	<u>132</u>
416.gamess	4	<u>533</u>	<u>147</u>	533	147	534	147	4	<u>533</u>	<u>147</u>	533	147	534	147
433.milc	4	276	133	<u>275</u>	<u>133</u>	275	133	4	276	133	<u>275</u>	<u>133</u>	275	133
434.zeusmp	4	175	208	176	207	<u>175</u>	<u>208</u>	4	175	208	176	207	<u>175</u>	<u>208</u>
435.gromacs	4	156	184	155	184	<u>155</u>	<u>184</u>	4	156	184	155	184	<u>155</u>	<u>184</u>
436.cactusADM	4	191	251	192	250	<u>191</u>	<u>250</u>	4	191	251	192	250	<u>191</u>	<u>250</u>
437.leslie3d	4	<u>414</u>	<u>90.8</u>	413	90.8	415	90.8	4	413	90.8	412	91.2	<u>413</u>	<u>91.2</u>
444.namd	4	300	107	299	108	<u>299</u>	<u>108</u>	4	294	109	293	110	<u>293</u>	<u>110</u>
447.dealII	4	221	207	227	202	<u>226</u>	<u>202</u>	4	221	207	227	202	<u>226</u>	<u>202</u>
450.soplex	4	379	88.0	<u>374</u>	<u>89.2</u>	373	89.2	4	379	88.0	<u>374</u>	<u>89.2</u>	373	89.2
453.povray	4	106	201	107	200	<u>106</u>	<u>201</u>	4	91.4	233	<u>90.7</u>	<u>235</u>	90.2	236
454.calculix	4	<u>159</u>	<u>207</u>	159	208	159	207	4	<u>159</u>	<u>207</u>	159	208	159	207
459.GemsFDTD	4	577	73.6	<u>578</u>	<u>73.6</u>	578	73.6	4	554	76.8	555	76.4	<u>554</u>	<u>76.4</u>
465.tonto	4	<u>266</u>	<u>148</u>	264	149	267	148	4	247	160	246	160	<u>246</u>	<u>160</u>
470.lbm	4	333	165	333	165	<u>333</u>	<u>165</u>	4	331	166	331	166	<u>331</u>	<u>166</u>
481.wrf	4	281	159	279	160	<u>280</u>	<u>160</u>	4	281	159	279	160	<u>280</u>	<u>160</u>
482.sphinx3	4	515	152	518	151	<u>516</u>	<u>151</u>	4	515	152	518	151	<u>516</u>	<u>151</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

To compile these binaries, the Intel Compiler 16.0 was set up to generate 64-bit binaries with the command:
"psxevars.bat intel64" (shortcut provided in the Intel(r) Parallel Studio XE 2016 program folder)

Platform Notes

Sysinfo program C:\SPEC16.0\Docs\sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ \8787f7622badcf24e01c368b1db4377c
running on CltF832E4885693 Wed May 4 01:49:12 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Trying 'systeminfo'

```
OS Name       : Microsoft Windows 7 Professional
OS Version    : 6.1.7601 Service Pack 1 Build 7601
System Manufacturer: System manufacturer
System Model   : System Product Name
Processor(s)  : 1 Processor(s) Installed.
               [01]: Intel64 Family 6 Model 94 Stepping 3 GenuineIntel ~2501 Mhz
BIOS Version  : American Megatrends Inc. 0704, 1/12/2016
Total Physical Memory: 8,070 MB
```

Trying 'wmic cpu get /value'

```
DeviceID      : CPU0
L2CacheSize   : 1024
L3CacheSize   : 6144
MaxClockSpeed : 2501
Name          : Intel(R) Core(TM) i5-6500T CPU @ 2.50GHz
NumberOfCores : 4
NumberOfLogicalProcessors: 4
```

(End of data from sysinfo program)

Component Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

General Notes

```
450.soplex (base): "getline_test" src.alt was used.
447.dealII (base): "max_prototype" src.alt was used.
447.dealII (base): "cxx11_make_pair" src.alt was used.
450.soplex (base): "getline_test" src.alt was used.
447.dealII (base): "max_prototype" src.alt was used.
447.dealII (base): "cxx11_make_pair" src.alt was used.
```

Binaries compiled on a system with 1x Intel Xeon E5-2699 v3 CPU
+ 64GB memory using Windows 8.1 Enterprise 64-bit

Base Compiler Invocation

C benchmarks:
icl -Qvc12 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc12

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc12 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DSPEC_CPU_BOOST_CONFIG_MSC_VER -DSPEC_NEED_ALGORITHM
450.soplex: -DSPEC_CPU_P64 -DSPEC_GETLINE_TEST
453.povray: -DSPEC_CPU_P64
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:

icl -Qvc12 -Qstd=c99

C++ benchmarks:

icl -Qvc12

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc12 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -QxCORE-AVX2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

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Peak Optimization Flags (Continued)

```
453.povray: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: -QxCORE-AVX2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE
```

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

```
465.tonto: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-windows.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-windows.xml>

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