



# SPEC® CFP2006 Result

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Huawei

**SPECfp®\_rate2006 = 4480**

Huawei RH8100 V3 (Intel Xeon E7-8880 v4)

**SPECfp\_rate\_base2006 = 4360**

CPU2006 license: 3175

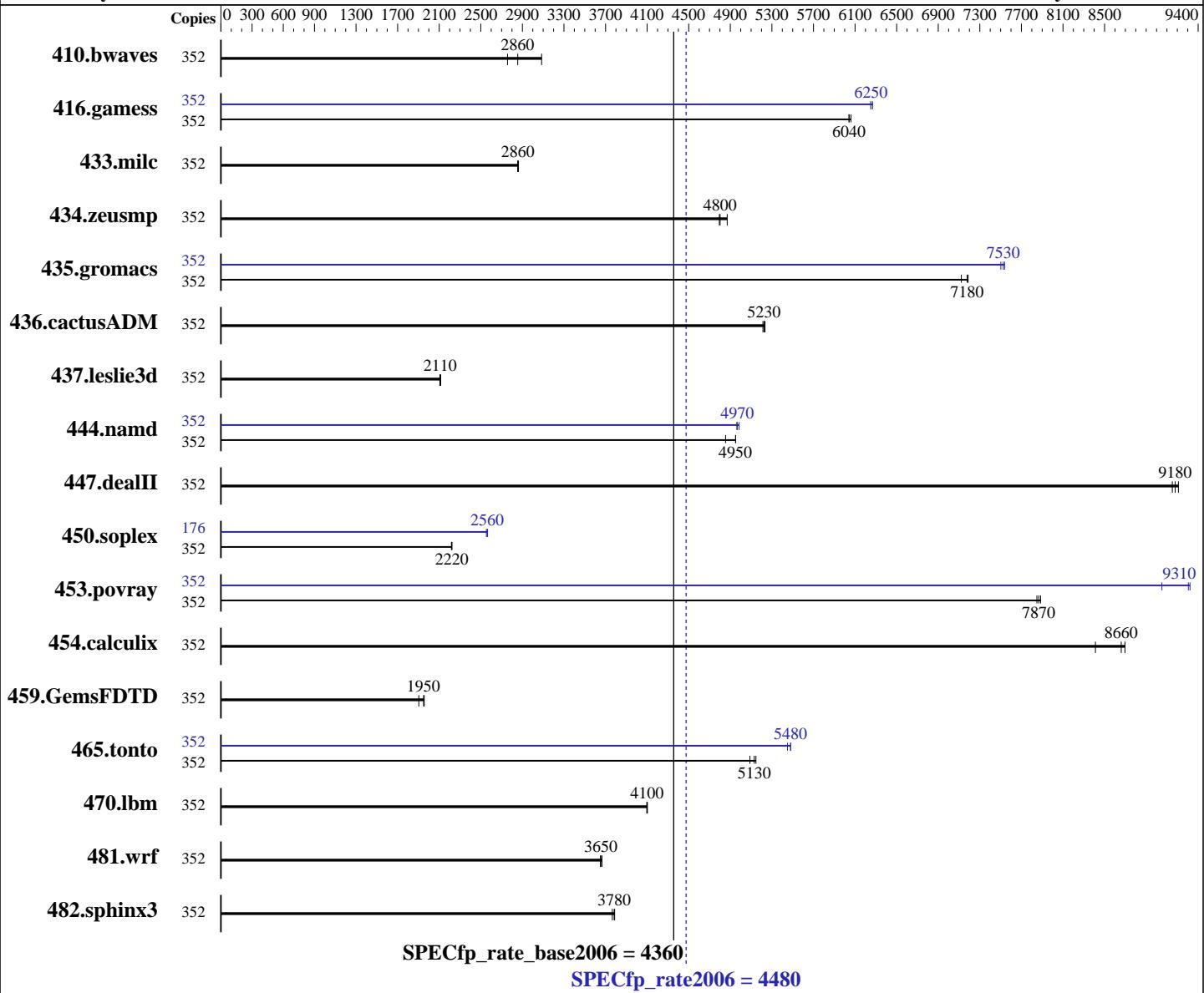
Test date: Jul-2016

Test sponsor: Huawei

Hardware Availability: Jun-2016

Tested by: Huawei

Software Availability: Oct-2015



## Hardware

CPU Name: Intel Xeon E7-8880 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 176 cores, 8 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 4,6,8 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Compiler: 3.10.0-327.el7.x86\_64  
 C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R,  
 running at 1600 MHz)  
 Disk Subsystem: 2 x 600GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	352	1551	3080	1735	2760	<b><u>1675</u></b>	<b><u>2860</u></b>	352	1551	3080	1735	2760	<b><u>1675</u></b>	<b><u>2860</u></b>
416.gamess	352	1137	6060	<b><u>1141</u></b>	<b><u>6040</u></b>	1141	6040	352	1102	6250	<b><u>1102</u></b>	<b><u>6250</u></b>	1099	6270
433.milc	352	1129	2860	1132	2860	<b><u>1131</u></b>	<b><u>2860</u></b>	352	1129	2860	1132	2860	<b><u>1131</u></b>	<b><u>2860</u></b>
434.zeusmp	352	658	4870	668	4790	<b><u>667</u></b>	<b><u>4800</u></b>	352	658	4870	668	4790	<b><u>667</u></b>	<b><u>4800</u></b>
435.gromacs	352	<b><u>350</u></b>	<b><u>7180</u></b>	353	7120	350	7190	352	<b><u>334</u></b>	<b><u>7530</u></b>	335	7500	333	7540
436.cactusADM	352	807	5210	<b><u>805</u></b>	<b><u>5230</u></b>	804	5230	352	807	5210	<b><u>805</u></b>	<b><u>5230</u></b>	804	5230
437.leslie3d	352	1571	2110	1565	2110	<b><u>1568</u></b>	<b><u>2110</u></b>	352	1571	2110	1565	2110	<b><u>1568</u></b>	<b><u>2110</u></b>
444.namd	352	<b><u>570</u></b>	<b><u>4950</u></b>	570	4950	582	4850	352	<b><u>568</u></b>	<b><u>4970</u></b>	566	4980	<b><u>569</u></b>	4960
447.dealII	352	440	9150	<b><u>439</u></b>	<b><u>9180</u></b>	437	9210	352	440	9150	<b><u>439</u></b>	<b><u>9180</u></b>	437	9210
450.soplex	352	1321	2220	<b><u>1322</u></b>	<b><u>2220</u></b>	1324	2220	176	<b><u>573</u></b>	<b><u>2560</u></b>	575	2550	572	2570
453.povray	352	<b><u>238</u></b>	<b><u>7870</u></b>	238	7880	239	7850	352	207	9050	<b><u>201</u></b>	<b><u>9310</u></b>	201	9320
454.calculix	352	334	8700	345	8410	<b><u>335</u></b>	<b><u>8660</u></b>	352	334	8700	345	8410	<b><u>335</u></b>	<b><u>8660</u></b>
459.GemsFDTD	352	<b><u>1916</u></b>	<b><u>1950</u></b>	1909	1960	1962	1900	352	<b><u>1916</u></b>	<b><u>1950</u></b>	1909	1960	1962	1900
465.tonto	352	<b><u>675</u></b>	<b><u>5130</u></b>	681	5090	673	5150	352	<b><u>632</u></b>	<b><u>5480</u></b>	632	5480	636	5450
470.lbm	352	1181	4100	1179	4100	<b><u>1180</u></b>	<b><u>4100</u></b>	352	1181	4100	1179	4100	<b><u>1180</u></b>	<b><u>4100</u></b>
481.wrf	352	1077	3650	<b><u>1076</u></b>	<b><u>3650</u></b>	1073	3670	352	<b><u>1077</u></b>	<b><u>3650</u></b>	<b><u>1076</u></b>	<b><u>3650</u></b>	1073	3670
482.sphinx3	352	1811	3790	<b><u>1814</u></b>	<b><u>3780</u></b>	1822	3760	352	<b><u>1811</u></b>	<b><u>3790</u></b>	<b><u>1814</u></b>	<b><u>3780</u></b>	1822	3760

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Turbo mode set with:  
 cpupower -c all frequency-set -g performance



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## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Lock\_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Set C-State to C0/C1

Sysinfo program /home/spec2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Sun Jul 31 07:00:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
        8 "physical id"s (chips)
        352 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 22
    siblings   : 44
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
    physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size : 56320 KB
```

```
From /proc/meminfo
MemTotal:      1056480360 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
```

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## Platform Notes (Continued)

```
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 30 18:33
```

```
SPEC is set to: /home/spec2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   1.0T  300G  725G  30% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 5.11 02/05/2016
Memory:
64x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz
128x NO DIMM NO DIMM
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1024 GB and the dmidecode description should have two lines reading as:

```
64x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz
128x NO DIMM NO DIMM
```

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/spec2006/libs/32:/home/spec2006/libs/64:/home/spec2006/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.xml>

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