



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = 107

Huawei 5288 V3 (Intel Xeon E5-2695 v4)

SPECfp_base2006 = 102

CPU2006 license: 3175

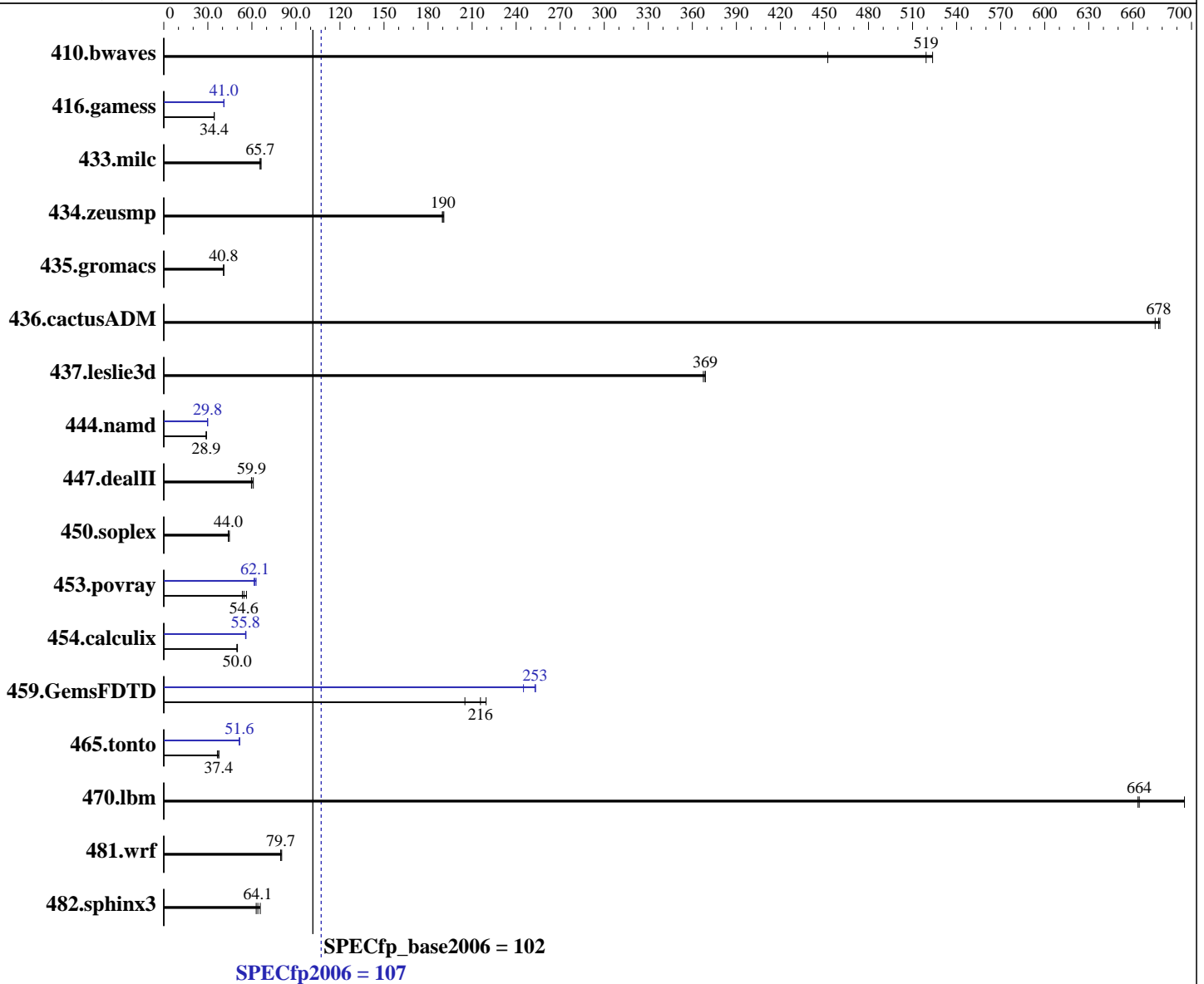
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2695 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.0	524	<u>26.2</u>	<u>519</u>	30.0	452	26.0	524	<u>26.2</u>	<u>519</u>	30.0	452
416.gamess	<u>570</u>	<u>34.4</u>	571	34.3	570	34.4	478	41.0	<u>478</u>	<u>41.0</u>	478	40.9
433.milc	<u>140</u>	<u>65.7</u>	138	66.3	140	65.6	<u>140</u>	<u>65.7</u>	138	66.3	140	65.6
434.zeusmp	<u>47.9</u>	<u>190</u>	47.7	191	48.0	190	<u>47.9</u>	<u>190</u>	47.7	191	48.0	190
435.gromacs	<u>175</u>	<u>40.8</u>	175	40.7	175	40.9	<u>175</u>	<u>40.8</u>	175	40.7	175	40.9
436.cactusADM	<u>17.6</u>	<u>678</u>	17.7	675	17.6	679	<u>17.6</u>	<u>678</u>	17.7	675	17.6	679
437.leslie3d	<u>25.5</u>	<u>369</u>	25.6	368	25.5	369	<u>25.5</u>	<u>369</u>	25.6	368	25.5	369
444.namd	277	28.9	277	29.0	<u>277</u>	<u>28.9</u>	<u>269</u>	<u>29.8</u>	269	29.8	269	29.8
447.dealII	<u>191</u>	<u>59.9</u>	188	61.0	192	59.7	<u>191</u>	<u>59.9</u>	188	61.0	192	59.7
450.soplex	190	44.0	<u>190</u>	<u>44.0</u>	187	44.7	190	44.0	<u>190</u>	<u>44.0</u>	187	44.7
453.povray	99.4	53.5	94.5	56.3	<u>97.5</u>	<u>54.6</u>	84.6	62.9	86.5	61.5	<u>85.7</u>	<u>62.1</u>
454.calculix	166	49.8	<u>165</u>	<u>50.0</u>	164	50.2	148	55.7	147	56.0	<u>148</u>	<u>55.8</u>
459.GemsFDTD	48.3	220	<u>49.2</u>	<u>216</u>	51.7	205	43.3	245	41.9	253	<u>42.0</u>	<u>253</u>
465.tonto	270	36.5	<u>263</u>	<u>37.4</u>	263	37.4	191	51.6	<u>191</u>	<u>51.6</u>	191	51.4
470.lbm	19.8	695	<u>20.7</u>	<u>664</u>	20.7	664	19.8	695	<u>20.7</u>	<u>664</u>	20.7	664
481.wrf	<u>140</u>	<u>79.7</u>	139	80.3	140	79.6	<u>140</u>	<u>79.7</u>	139	80.3	140	79.6
482.sphinx3	310	62.9	<u>304</u>	<u>64.1</u>	297	65.7	310	62.9	<u>304</u>	<u>64.1</u>	297	65.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS mode
 Set Patrol Scrub to Disable
 Set Hyper-Threading to Disable
 Sysinfo program /spec/spec16/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-c3qu Mon Nov 28 06:29:10 2016

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
  2 "physical id"s (chips)
  36 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 18
    siblings  : 18
    physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  cache size : 46080 KB

```

```

From /proc/meminfo
MemTotal:      264059648 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-c3qu 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 28 06:27

```

SPEC is set to: /spec/spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   641G  7.6G  634G   2% /spec
Additional information from dmidecode:

```

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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Samsung M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec/spec16/libs/32:/spec/spec16/libs/64:/spec/spec16/sh"

OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

```

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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