



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 107

Huawei CH242 V3 (Intel Xeon E7-8890 v4)

SPECfp_base2006 = 101

CPU2006 license: 3175

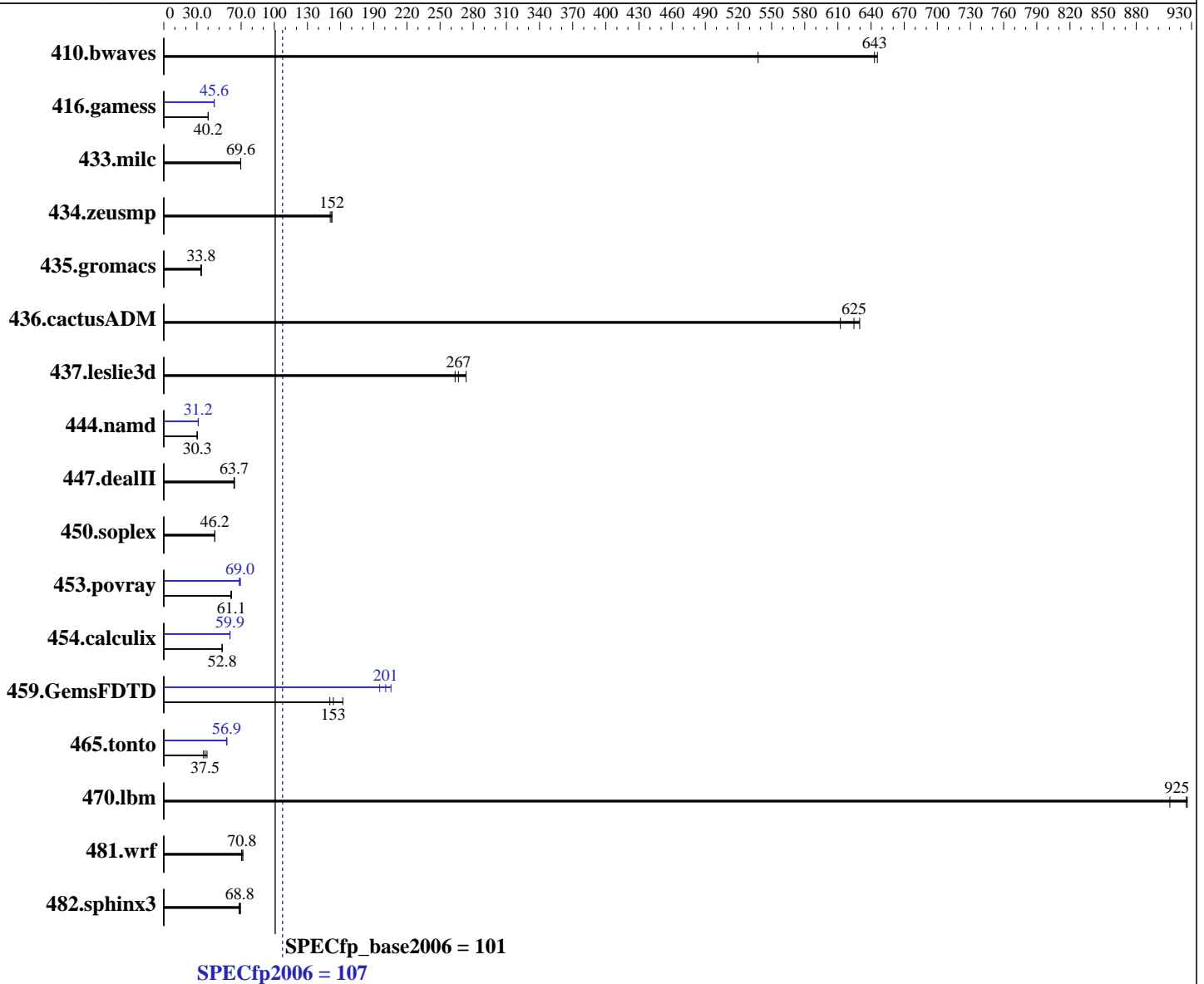
Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Jun-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E7-8890 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 96 cores, 4 chips, 24 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit

Continued on next page



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L3 Cache: 60 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R, running at 1600 MHz)
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>21.1</u>	<u>643</u>	21.0	646	25.3	538	<u>21.1</u>	<u>643</u>	21.0	646	25.3	538
416.gamess	<u>487</u>	<u>40.2</u>	487	40.2	488	40.1	430	45.6	<u>429</u>	<u>45.6</u>	429	45.6
433.milc	<u>132</u>	<u>69.6</u>	132	69.7	132	69.6	<u>132</u>	<u>69.6</u>	132	69.7	132	69.6
434.zeusmp	59.8	152	<u>59.9</u>	<u>152</u>	60.4	151	59.8	152	<u>59.9</u>	<u>152</u>	60.4	151
435.gromacs	<u>211</u>	<u>33.8</u>	209	34.1	213	33.6	<u>211</u>	<u>33.8</u>	209	34.1	213	33.6
436.cactusADM	<u>19.1</u>	<u>625</u>	19.0	630	19.5	612	<u>19.1</u>	<u>625</u>	19.0	630	19.5	612
437.leslie3d	34.4	274	<u>35.3</u>	<u>267</u>	35.7	264	34.4	274	<u>35.3</u>	<u>267</u>	35.7	264
444.namd	265	30.2	<u>265</u>	<u>30.3</u>	265	30.3	257	31.2	258	31.1	<u>257</u>	<u>31.2</u>
447.dealII	<u>180</u>	<u>63.7</u>	179	64.0	180	63.5	<u>180</u>	<u>63.7</u>	179	64.0	180	63.5
450.soplex	<u>180</u>	<u>46.2</u>	181	46.1	180	46.3	<u>180</u>	<u>46.2</u>	181	46.1	180	46.3
453.povray	<u>87.0</u>	<u>61.1</u>	87.6	60.8	87.0	61.1	<u>78.0</u>	<u>68.2</u>	<u>77.1</u>	<u>69.0</u>	76.8	69.3
454.calculix	<u>156</u>	<u>52.8</u>	156	52.8	156	52.7	138	60.0	<u>138</u>	<u>59.9</u>	138	59.8
459.GemsFDTD	65.5	162	<u>69.1</u>	<u>153</u>	70.6	150	51.6	206	54.3	195	<u>52.8</u>	<u>201</u>
465.tonto	<u>263</u>	<u>37.5</u>	251	39.1	274	35.9	174	56.7	172	57.1	<u>173</u>	<u>56.9</u>
470.lbm	15.1	910	14.8	926	<u>14.9</u>	<u>925</u>	15.1	910	14.8	926	<u>14.9</u>	<u>925</u>
481.wrf	159	70.4	<u>158</u>	<u>70.8</u>	156	71.6	159	70.4	<u>158</u>	<u>70.8</u>	156	71.6
482.sphinx3	<u>283</u>	<u>68.8</u>	281	69.4	286	68.2	<u>283</u>	<u>68.8</u>	281	69.4	286	68.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Hyper-Threading to Disabled
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100
Sysinfo program /spec16/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Dec 5 22:06:29 2016

Continued on next page



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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E7-8890 v4 @ 2.20GHz
 4 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 24
siblings       : 24
physical 0:    : cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
                27 28 29
physical 1:    : cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
                27 28 29
physical 2:    : cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
                27 28 29
physical 3:    : cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
                27 28 29
cache size     : 61440 KB

```

```

From /proc/meminfo
MemTotal:      528074584 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 5 17:10

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  867G  343G  480G  42% /

```

Additional information from dmidecode:

Continued on next page



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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV788 11/07/2016

Memory:

32x Samsung M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP_NUM_THREADS = "96"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

Continued on next page

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Page 4



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Tested by: Huawei

Software Availability: Nov-2015

Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

Continued on next page



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Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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Page 7