



SPEC® CINT2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(3.00 GHz, Intel Xeon E5-2687W v4)

SPECint®2006 =

71.7

SPECint_base2006 =

68.1

CPU2006 license: 3

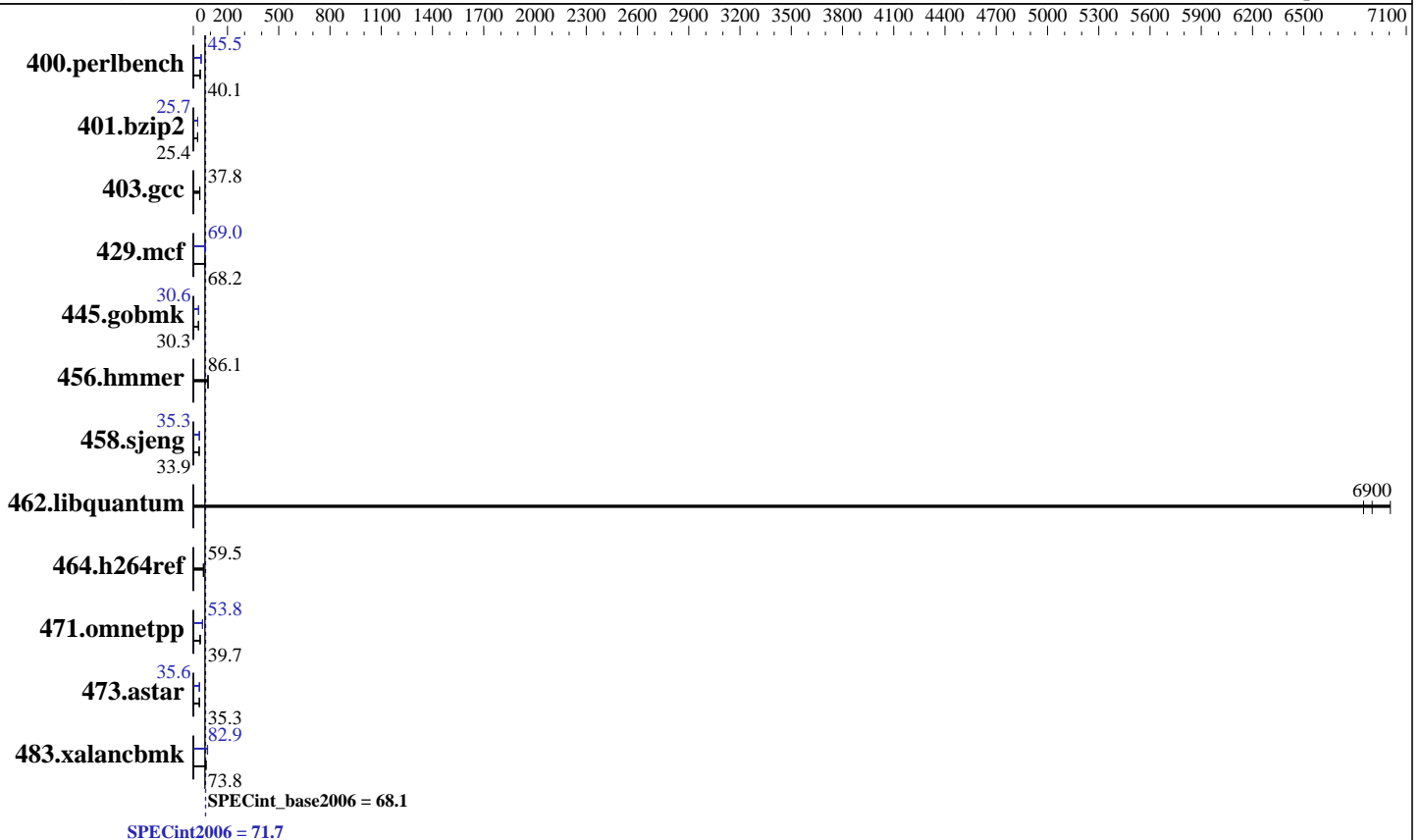
Test sponsor: HPE

Tested by: HPE

Test date: Jun-2017

Hardware Availability: Feb-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon E5-2687W v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
CPU(s) orderable: 1,2 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 400 SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Kernel 4.4.21-69-default
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	244	40.0	<u>243</u>	<u>40.1</u>	243	40.2	215	45.4	<u>215</u>	<u>45.5</u>	214	45.6
401.bzip2	381	25.3	<u>380</u>	<u>25.4</u>	380	25.4	<u>375</u>	<u>25.7</u>	375	25.8	375	25.7
403.gcc	213	37.8	213	37.8	<u>213</u>	<u>37.8</u>	213	37.8	213	37.8	<u>213</u>	<u>37.8</u>
429.mcf	136	67.2	<u>134</u>	<u>68.2</u>	131	69.4	<u>132</u>	<u>69.0</u>	132	69.1	134	68.0
445.gobmk	347	30.3	346	30.3	<u>346</u>	<u>30.3</u>	342	30.7	342	30.6	<u>342</u>	<u>30.6</u>
456.hammer	108	86.2	<u>108</u>	<u>86.1</u>	109	85.9	108	86.2	<u>108</u>	<u>86.1</u>	109	85.9
458.sjeng	357	33.9	357	33.9	<u>357</u>	<u>33.9</u>	<u>343</u>	<u>35.3</u>	343	35.3	343	35.3
462.libquantum	<u>3.00</u>	<u>6900</u>	3.02	6850	2.96	7010	<u>3.00</u>	<u>6900</u>	3.02	6850	2.96	7010
464.h264ref	<u>372</u>	<u>59.5</u>	373	59.3	371	59.6	<u>372</u>	<u>59.5</u>	373	59.3	371	59.6
471.omnetpp	155	40.2	<u>157</u>	<u>39.7</u>	158	39.6	116	53.9	<u>116</u>	<u>53.8</u>	116	53.7
473.astar	200	35.1	<u>199</u>	<u>35.3</u>	197	35.6	198	35.5	197	35.7	<u>197</u>	<u>35.6</u>
483.xalancbmk	92.8	74.4	93.6	73.7	<u>93.5</u>	<u>73.8</u>	<u>83.2</u>	<u>82.9</u>	83.4	82.7	82.2	83.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Platform Notes

BIOS Configuration:
Intel Hyperthreading set to Disabled
Power Profile set to Custom
Minimum Processor Idle Power Core C-State set to C3 State
Minimum Processor Idle Power Package C-State set to Package C6 (retention) State
QPI Snoop Configuration set to Home Snoop
Collaborative Power Control set to Disabled
Energy/Performance Bias set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
NUMA Group Size Optimization set to Flat

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl360-g9 Tue Jun 6 14:19:05 2017

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2687W v4@ 3.00GHz
 2 "physical id"s (chips)
 24 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores      : 12
siblings       : 12
physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 30720 KB
```

From /proc/meminfo

```
MemTotal:      263824484 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

From /etc/*release* /etc/*version*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
```

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

```
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

uname -a:

```
Linux dl360-g9 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 6 14:16

SPEC is set to: /home/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda4        xfs       331G  4.1G  327G   2% /home
```

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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 02/17/2017

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.aster: -DSPEC_CPU_LP64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
 -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2)

456.hmmcr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-ra-region-strategy=block
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.aster: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
 -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jul 12 12:13:03 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 July 2017.