



# SPEC® CFP2006 Result

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## Lenovo Global Technology

ThinkSystem SR650  
(2.40 GHz, Intel Xeon Gold 5115)

SPECfp®2006 = 127

SPECfp\_base2006 = 122

CPU2006 license: 9017

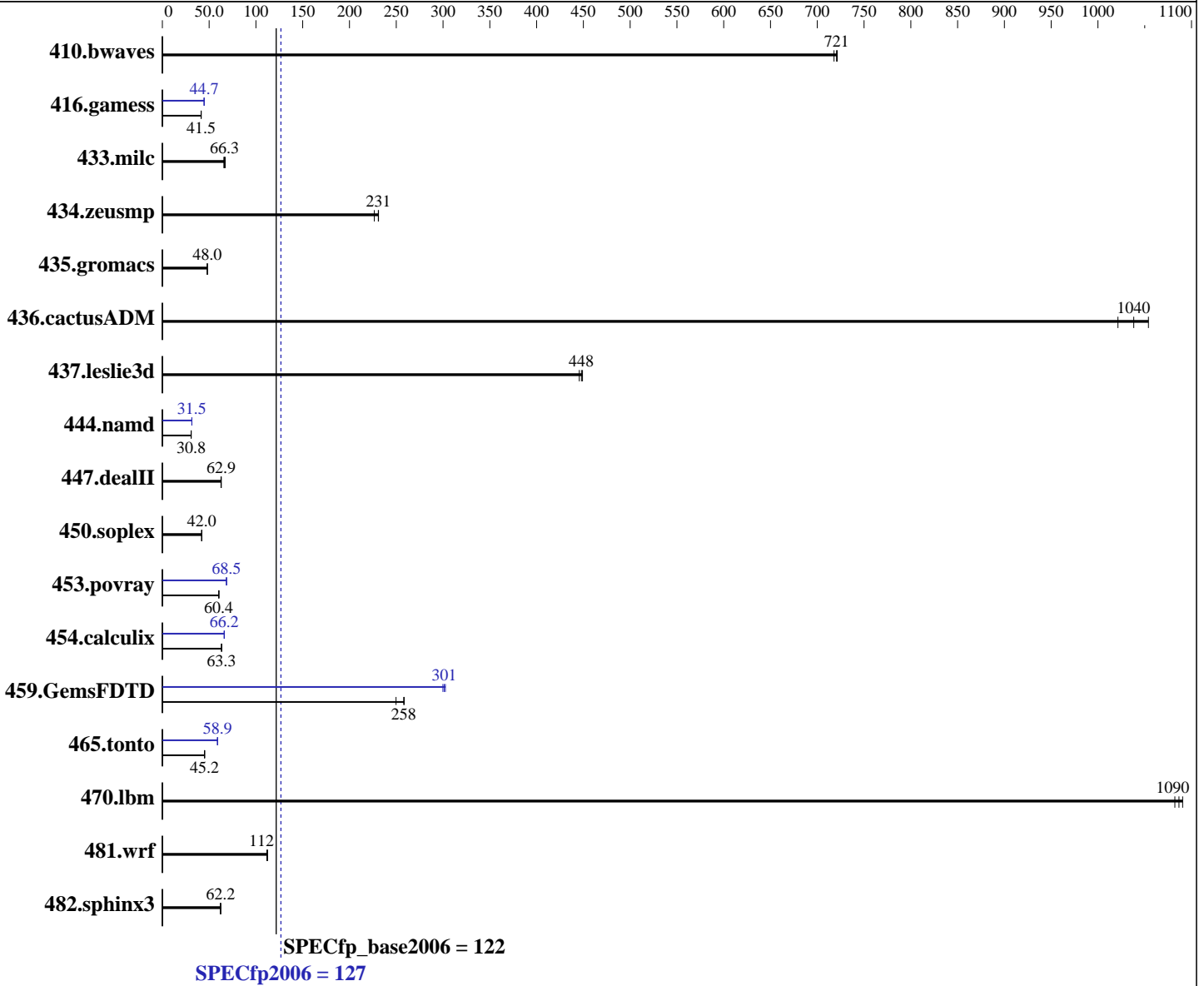
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Aug-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 5115  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 13.75 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400 MHz)  
Disk Subsystem: 1 x 800 GB SAS SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	18.8	721	18.9	718	<b>18.9</b>	<b>721</b>	18.8	721	18.9	718	<b>18.9</b>	<b>721</b>
416.gamess	472	41.4	<b>472</b>	<b>41.5</b>	472	41.5	438	44.7	<b>438</b>	<b>44.7</b>	438	44.7
433.milc	<b>138</b>	<b>66.3</b>	137	67.2	140	65.7	<b>138</b>	<b>66.3</b>	137	67.2	140	65.7
434.zeusmp	39.4	231	<b>39.4</b>	<b>231</b>	40.2	227	39.4	231	<b>39.4</b>	<b>231</b>	40.2	227
435.gromacs	149	48.0	<b>149</b>	<b>48.0</b>	148	48.1	149	48.0	<b>149</b>	<b>48.0</b>	148	48.1
436.cactusADM	11.7	1020	11.3	1050	<b>11.5</b>	<b>1040</b>	11.7	1020	11.3	1050	<b>11.5</b>	<b>1040</b>
437.leslie3d	21.1	446	20.9	449	<b>21.0</b>	<b>448</b>	21.1	446	20.9	449	<b>21.0</b>	<b>448</b>
444.namd	<b>261</b>	<b>30.8</b>	261	30.8	260	30.8	254	31.5	254	31.6	<b>254</b>	<b>31.5</b>
447.dealII	182	62.9	<b>182</b>	<b>62.9</b>	183	62.7	182	62.9	<b>182</b>	<b>62.9</b>	183	62.7
450.soplex	198	42.0	198	42.1	<b>198</b>	<b>42.0</b>	198	42.0	198	42.1	<b>198</b>	<b>42.0</b>
453.povray	<b>88.1</b>	<b>60.4</b>	88.2	60.3	88.0	60.4	77.6	68.5	77.6	68.6	<b>77.6</b>	<b>68.5</b>
454.calculix	130	63.4	<b>130</b>	<b>63.3</b>	131	63.2	<b>125</b>	<b>66.2</b>	124	66.3	125	66.0
459.GemsFDTD	42.5	250	41.0	259	<b>41.1</b>	<b>258</b>	35.1	303	35.4	300	<b>35.2</b>	<b>301</b>
465.tonto	218	45.2	219	45.0	<b>218</b>	<b>45.2</b>	167	58.9	167	59.0	<b>167</b>	<b>58.9</b>
470.lbm	12.6	1090	<b>12.6</b>	<b>1090</b>	12.7	1080	12.6	1090	<b>12.6</b>	<b>1090</b>	12.7	1080
481.wrf	<b>99.5</b>	<b>112</b>	100	112	99.4	112	<b>99.5</b>	<b>112</b>	100	112	99.4	112
482.sphinx3	<b>313</b>	<b>62.2</b>	312	62.6	315	61.9	<b>313</b>	<b>62.2</b>	312	62.6	315	61.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance

Hyper-Threading set to Disabled

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on Cyborg-SUT3 Thu Aug 24 18:10:03 2017

This section contains SUT (System Under Test) info as seen by

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### Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
  2 "physical id"s (chips)
  20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 10
    siblings  : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 14080 KB

```

```

From /proc/meminfo
MemTotal:      395893692 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux Cyborg-SUT3 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 24 18:09

```

SPEC is set to: /home/cpu2006-1.2-ic17.0u3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb3       xfs   445G  11G  435G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE111C-1.00]- 07/17/2017

Memory:

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.lelie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.xml>

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