



# SPEC® CINT2006 Result

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## Huawei

## SPECint®\_rate2006 = Not Run

## Kunlun 9008 (Intel Xeon E7-8855 v4)

## SPECint\_rate\_base2006 = 4060

CPU2006 license: 3175

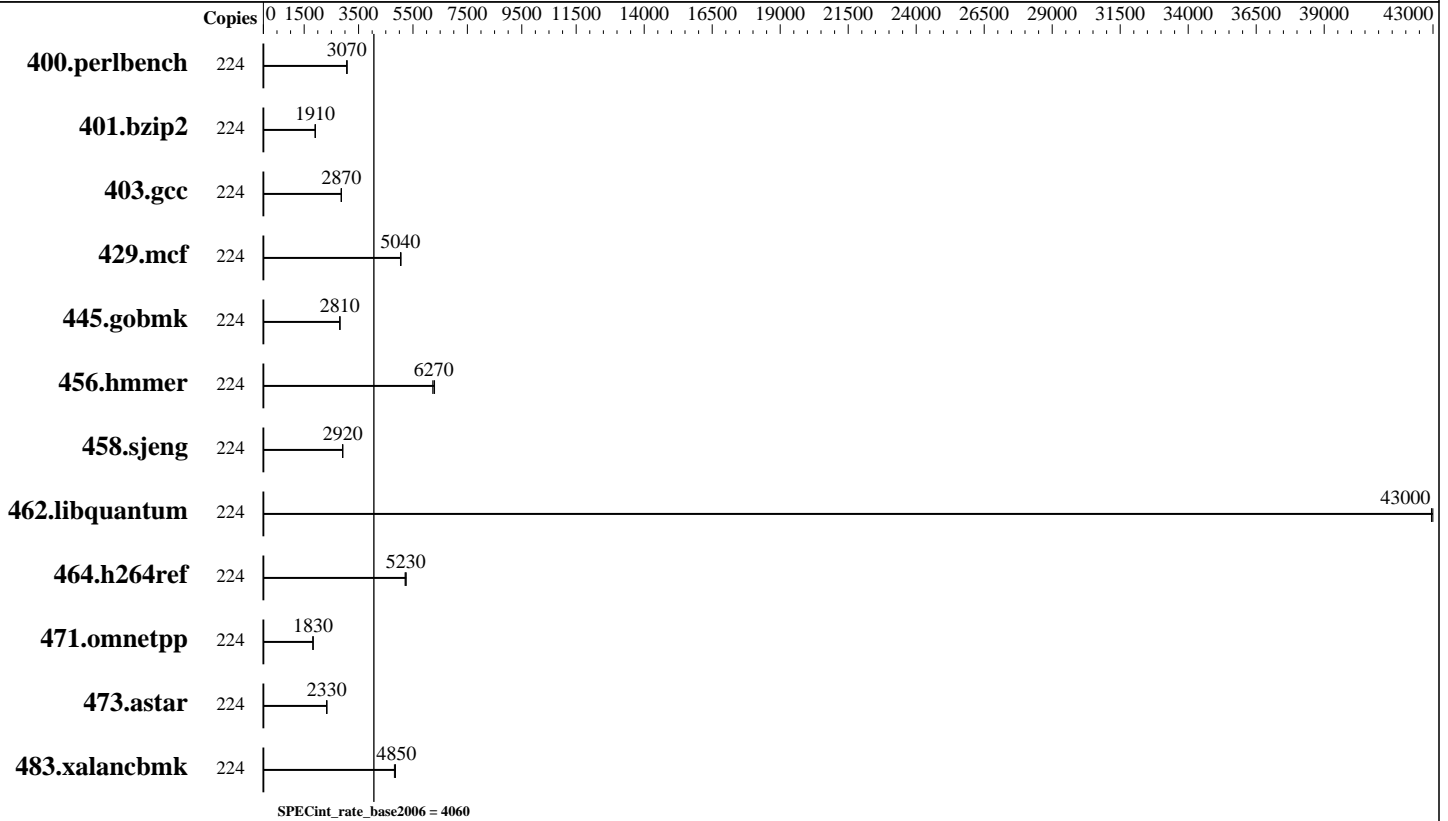
Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2017

Hardware Availability: Jan-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E7-8855 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 112 cores, 8 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 4,8 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 35 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)  
 Disk Subsystem: 2 x 600 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1  
 Kernel 3.12.49-11-default  
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	224	<b>712</b>	<b>3070</b>	712	3070	714	3070							
401.bzip2	224	<b>1133</b>	<b>1910</b>	1135	1900	1131	1910							
403.gcc	224	<b>629</b>	<b>2870</b>	630	2860	628	2870							
429.mcf	224	<b>405</b>	<b>5040</b>	405	5040	404	5060							
445.gobmk	224	835	2810	<b>836</b>	<b>2810</b>	836	2810							
456.hammer	224	332	6290	336	6230	<b>333</b>	<b>6270</b>							
458.sjeng	224	930	2920	<b>930</b>	<b>2920</b>	930	2920							
462.libquantum	224	108	42900	<b>108</b>	<b>43000</b>	108	43000							
464.h264ref	224	951	5210	944	5250	<b>948</b>	<b>5230</b>							
471.omnetpp	224	766	1830	<b>767</b>	<b>1830</b>	768	1820							
473.astar	224	675	2330	675	2330	<b>675</b>	<b>2330</b>							
483.xalancbmk	224	319	4850	321	4820	<b>319</b>	<b>4850</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Turbo mode set with:  
cpupower -c all frequency-set -g performance

### Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-ew80 Sat Apr 1 00:34:20 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E7-8855 v4 @ 2.10GHz  
8 "physical id"s (chips)  
224 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 14
siblings  : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 4: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 5: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 6: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 7: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      1058049388 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-ew80 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Apr 1 00:32
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  1.1T   27G  1.1T   3% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
Continued on next page



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## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 5.11 02/21/2017

Memory:

64x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1333 MHz  
128x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -D\_FILE\_OFFSET\_BITS=64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmr: -D\_FILE\_OFFSET\_BITS=64

458.sjeng: -D\_FILE\_OFFSET\_BITS=64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

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## Base Portability Flags (Continued)

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.20170404.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.20170404.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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