



SPEC[®] CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen10
(2.60 GHz, Intel Xeon Silver 4112)

SPECfp[®]_rate2006 = Not Run

SPECfp_rate_base2006 = 420

CPU2006 license: 3

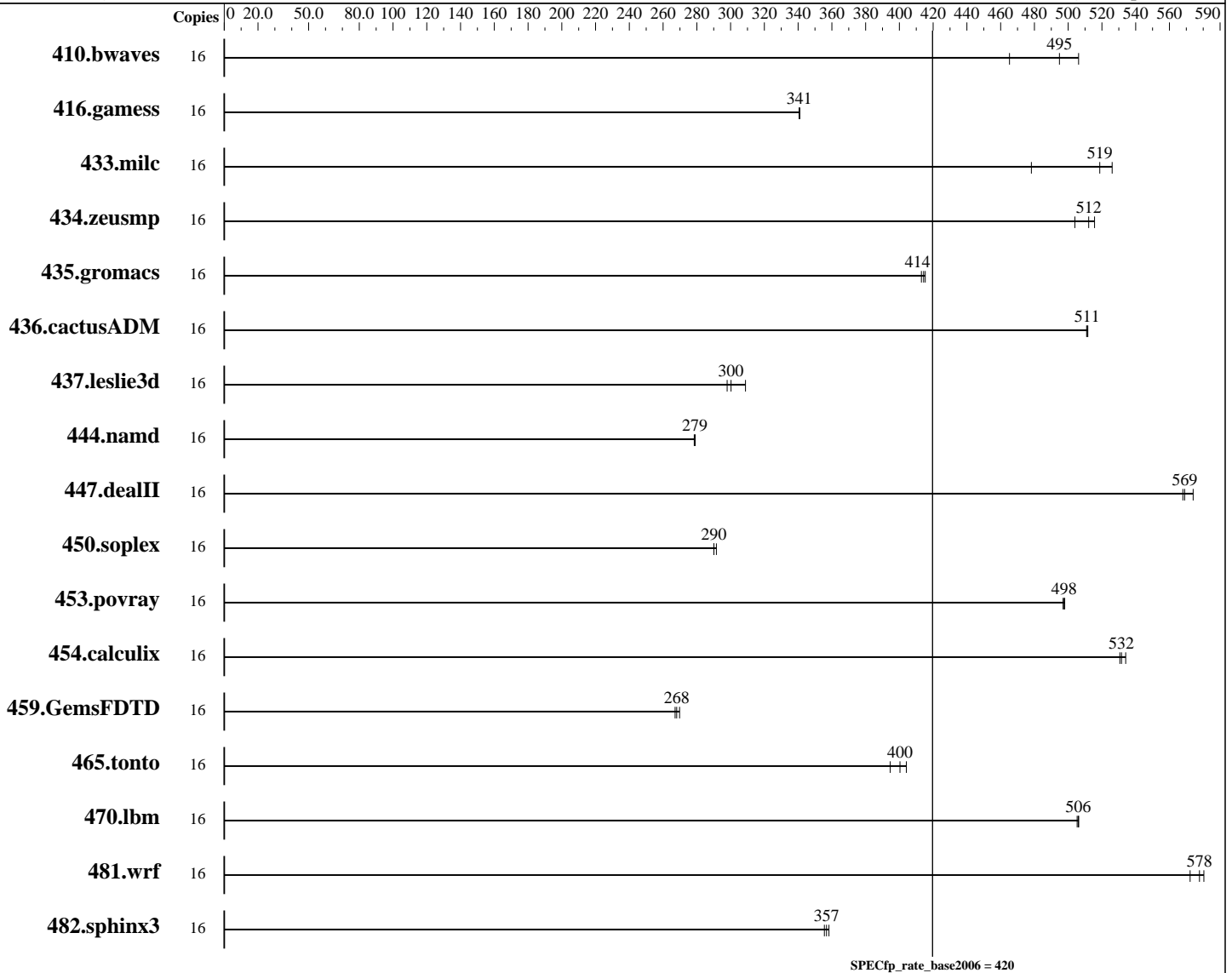
Test sponsor: HPE

Tested by: HPE

Test date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Silver 4112
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Kernel 4.4.21-69-default
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: No
 File System: btrfs
 System State: Run level 3 (multi-user)

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L3 Cache: 8.25 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R, running at 2400)
Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	467	465	<u>439</u>	<u>495</u>	430	506							
416.gamess	16	920	341	<u>920</u>	<u>341</u>	919	341							
433.milc	16	307	478	<u>283</u>	<u>519</u>	279	526							
434.zeusmp	16	289	504	<u>284</u>	<u>512</u>	282	516							
435.gromacs	16	275	415	277	413	<u>276</u>	<u>414</u>							
436.cactusADM	16	374	511	374	512	<u>374</u>	<u>511</u>							
437.leslie3d	16	<u>501</u>	<u>300</u>	505	298	487	309							
444.namd	16	461	278	<u>460</u>	<u>279</u>	460	279							
447.dealII	16	319	574	322	568	<u>322</u>	<u>569</u>							
450.soplex	16	<u>460</u>	<u>290</u>	458	292	460	290							
453.povray	16	<u>171</u>	<u>498</u>	171	498	171	497							
454.calculix	16	247	534	<u>248</u>	<u>532</u>	249	531							
459.GemsFDTD	16	636	267	<u>633</u>	<u>268</u>	629	270							
465.tonto	16	399	395	<u>393</u>	<u>400</u>	390	404							
470.lbm	16	<u>434</u>	<u>506</u>	435	505	434	506							
481.wrf	16	312	572	<u>309</u>	<u>578</u>	308	580							
482.sphinx3	16	877	356	<u>874</u>	<u>357</u>	870	358							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"

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Operating System Notes (Continued)

tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetcher set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E
Sysinfo program /cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on sys1-dl380-lic-srvr Wed Oct 18 09:53:57 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 4
  siblings      : 8
  physical 0    : cores 1 2 4 5
  physical 1    : cores 0 2 3 4
cache size     : 8448 KB
```

From /proc/meminfo

```
MemTotal:      197558352 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
```

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Platform Notes (Continued)

```

NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux sys1-dl380-lic-srvr 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC
2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 17 17:38

SPEC is set to: /cpu2006

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        btrfs    489G   45G  445G  10% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006/lib/ia32:/cpu2006/lib/intel64:/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Nov 15 10:59:14 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 November 2017.