SPEC® CINT2006 Result
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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL360 Gen10
(1.70 GHz, Intel Xeon Bronze 3104)

$SPECint®2006 =$ **Not Run**

$SPECint\_base2006 =$ **35.9**

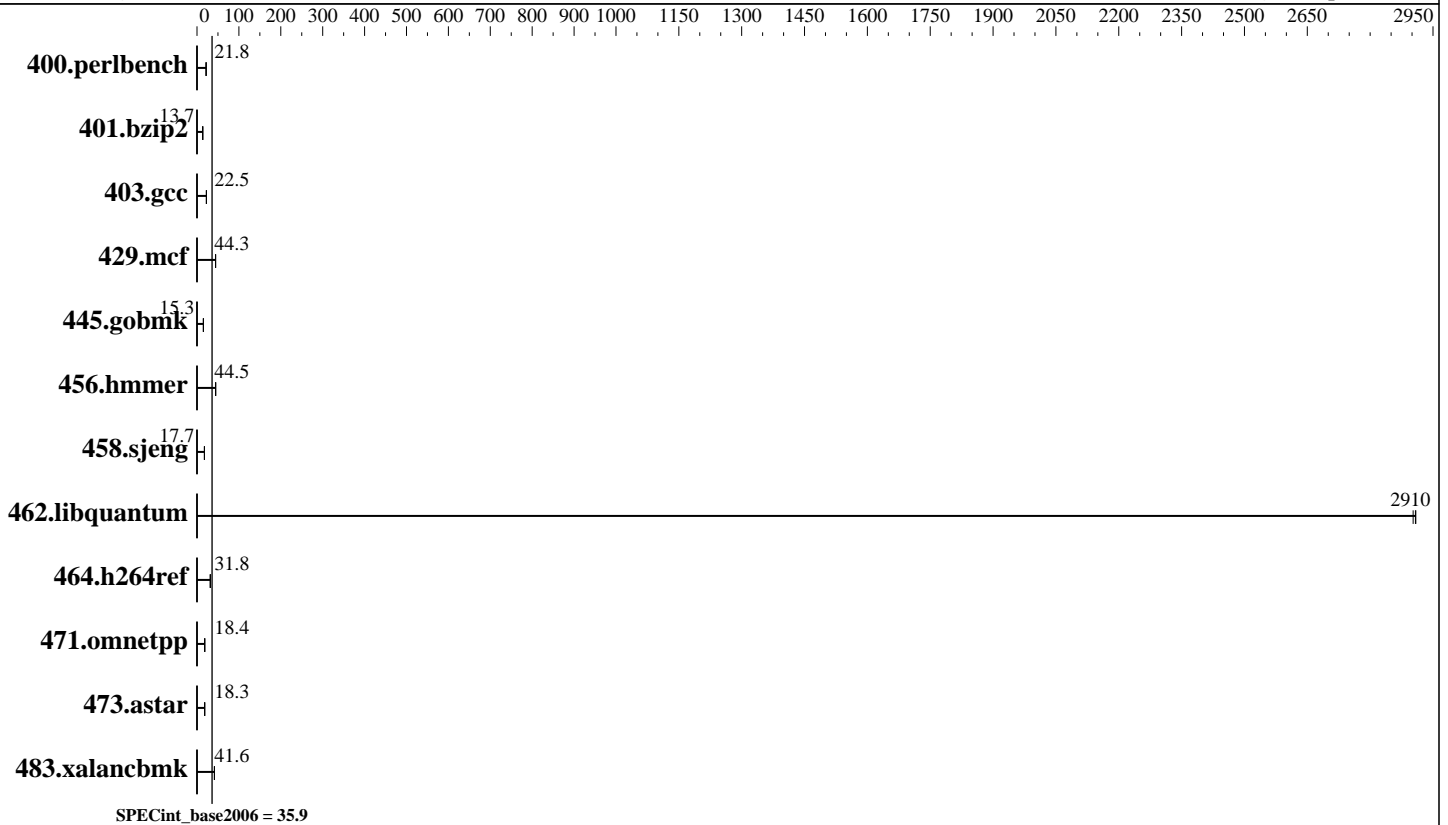|	
CPU2006 license: 3	**Test date:** Nov-2017
Test sponsor: HPE	**Hardware Availability:** Oct-2017
Tested by: HPE	**Software Availability:** Apr-2017

```
          0  100 200 300 400 500 600 700 800 900 1000    1150    1300    1450    1600    1750    1900    2050    2200    2350   2500    2650        2950

400.perlbench   21.8

401.bzip2    13.7

403.gcc      22.5

429.mcf      44.3

445.gobmk    15.3

456.hmmer    44.5

458.sjeng    17.7

462.libquantum                                                                                                                              2910

464.h264ref   31.8

471.omnetpp   18.4

473.astar     18.3

483.xalancbmk  41.6
```

SPECint_base2006 = 35.9

Hardware

CPU Name:	Intel Xeon Bronze 3104
CPU Characteristics:	
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1, 2 chip(s)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8.25 MB I+D on chip per chip
Other Cache:	None
Memory:	192 GB (24 x 8 GB 2Rx8 PC4-2666V-R, running at 2133)
Disk Subsystem:	1 x 600 GB SATA SSD, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.3 (Maipo) Kernel 3.10.0-514.el7.x86_64
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	21.9	450	21.7	**448**	**21.8**						
401.bzip2	**705**	**13.7**	706	13.7	704	13.7						
403.gcc	**358**	**22.5**	358	22.5	359	22.5						
429.mcf	206	44.3	**206**	**44.3**	205	44.4						
445.gobmk	687	15.3	**687**	**15.3**	687	15.3						
456.hmmer	210	44.5	**210**	**44.5**	210	44.5						
458.sjeng	682	17.7	682	17.7	**682**	**17.7**						
462.libquantum	7.12	2910	7.14	2900	**7.13**	**2910**						
464.h264ref	696	31.8	**696**	**31.8**	696	31.8						
471.omnetpp	339	18.4	**339**	**18.4**	339	18.5						
473.astar	**383**	**18.3**	383	18.3	383	18.3						
483.xalancbmk	166	41.6	**166**	**41.6**	166	41.6						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
IRQ balance service was stop using "service irqbalance stop"
Tuned-adm profile was set to Throughtput-Performance
```

Platform Notes

```
BIOS Configuration:
 Intel Hyperthreading set to Disabled
 Thermal Configuration set to Maximum Cooling
 LLC Prefetch set to Enabled
 LLC Dead Line Allocation set to Disabled
 Memory Patrol Scrubbing set to Disabled
 Workload Profile set to General Peak Frequency Compute
  Energy/Performance Bias set to Maximum Performance
 Workload Profile set to Custom
  NUMA Group Size Optimization set to Flat
 Sysinfo program /home/cpu2006/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on DL360G10 Fri Nov 17 03:30:11 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 6
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 8448 KB

From /proc/meminfo
   MemTotal:       197752788 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.3 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.3"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:
   Linux DL360G10 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 17 03:19

SPEC is set to: /home/cpu2006
   Filesystem                     Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel_dl360g10-home xfs   504G   37G  467G   8% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HPE U32 09/29/2017
   Memory:
   24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz, configured at 2133 MHz
```

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Platform Notes (Continued)

```
    (End of data from sysinfo program)
```

General Notes

```
    Environment variables set by runspec before the start of the run:
    KMP_AFFINITY = "granularity=fine,compact"
    LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"
    OMP_NUM_THREADS = "12"

     Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
     memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
```
    icc -m64
```

C++ benchmarks:
```
    icpc -m64
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
       473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
    -auto-p32
```

C++ benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

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Base Other Flags

C benchmarks:

      403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 12 17:06:39 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 December 2017.