



# SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Sugon

**SPECint®2006 = 76.0**

### TC4600E(CX50-G30, Intel Gold 6132)

**SPECint\_base2006 = 72.8**

CPU2006 license: 9046

Test date: Dec-2017

Test sponsor: Sugon

Hardware Availability: Dec-2017

Tested by: Sugon

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 6132  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 19.25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC4-2667V-R)  
 Disk Subsystem: 1 x 1.0 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (Multi User)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Sugon

SPECint2006 = **76.0**

## TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = **72.8**

CPU2006 license: 9046  
Test sponsor: Sugon  
Tested by: Sugon

Test date: Dec-2017  
Hardware Availability: Dec-2017  
Software Availability: Apr-2017

### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	214	45.7	215	45.4	<b><u>215</u></b>	<b><u>45.5</u></b>	188	52.0	<b><u>187</u></b>	<b><u>52.2</u></b>	187	52.3
401.bzip2	346	27.9	<b><u>346</u></b>	<b><u>27.9</u></b>	346	27.9	<b><u>345</u></b>	<b><u>28.0</u></b>	344	28.0	345	28.0
403.gcc	<b><u>188</u></b>	<b><u>42.8</u></b>	187	43.0	188	42.8	182	44.3	182	44.3	<b><u>182</u></b>	<b><u>44.3</u></b>
429.mcf	<b><u>116</u></b>	<b><u>78.8</u></b>	114	79.7	116	78.3	120	76.1	<b><u>117</u></b>	<b><u>77.8</u></b>	117	78.1
445.gobmk	<b><u>321</u></b>	<b><u>32.7</u></b>	321	32.7	321	32.7	319	32.9	319	32.9	<b><u>319</u></b>	<b><u>32.9</u></b>
456.hammer	105	89.1	<b><u>105</u></b>	<b><u>89.0</u></b>	105	88.5	105	89.1	<b><u>105</u></b>	<b><u>89.0</u></b>	105	88.5
458.sjeng	338	35.8	<b><u>338</u></b>	<b><u>35.8</u></b>	338	35.8	328	36.9	<b><u>328</u></b>	<b><u>36.9</u></b>	328	36.9
462.libquantum	2.74	7560	2.74	7580	<b><u>2.74</u></b>	<b><u>7570</u></b>	2.74	7560	2.74	7580	<b><u>2.74</u></b>	<b><u>7570</u></b>
464.h264ref	<b><u>344</u></b>	<b><u>64.4</u></b>	344	64.4	344	64.4	<b><u>344</u></b>	<b><u>64.4</u></b>	344	64.4	344	64.4
471.omnetpp	199	31.4	198	31.5	<b><u>198</u></b>	<b><u>31.5</u></b>	151	41.3	151	41.5	<b><u>151</u></b>	<b><u>41.5</u></b>
473.astar	181	38.9	<b><u>181</u></b>	<b><u>38.7</u></b>	181	38.7	182	38.6	183	38.4	<b><u>183</u></b>	<b><u>38.5</u></b>
483.xalancbmk	<b><u>82.0</u></b>	<b><u>84.2</u></b>	82.1	84.1	81.8	84.4	79.0	87.3	<b><u>79.0</u></b>	<b><u>87.3</u></b>	79.1	87.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

Sysinfo program /home/tianyan/benchmarks/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on localhost Sun Dec 10 00:59:33 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Sugon

SPECint2006 = 76.0

TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = 72.8

CPU2006 license: 9046  
Test sponsor: Sugon  
Tested by: Sugon

Test date: Dec-2017  
Hardware Availability: Dec-2017  
Software Availability: Apr-2017

## Platform Notes (Continued)

cache size : 19712 KB

From /proc/meminfo

MemTotal: 394687848 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"  
VERSION="7.3 (Maipo)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="7.3"  
PRETTY\_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"  
ANSI\_COLOR="0;31"  
CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.3:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.3:ga:server

uname -a:

Linux localhost 3.10.0-514.el7.x86\_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Dec 8 21:11

SPEC is set to: /home/tianyan/benchmarks/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	671G	41G	630G	7%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 0JGST024 11/15/2017 American Megatrends Inc. 0JGST024 11/15/2017

Memory:

24x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz  
24x NO DIMM NO DIMM

(End of data from sysinfo program)

The dmidecode information displayed in sysinfo should have one line reading as:  
12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Sugon

SPECint2006 = 76.0

TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = 72.8

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Apr-2017

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/tianyan/benchmarks/cpu2006/lib/ia32:/home/tianyan/benchmarks/cpu2006/lib/intel64:/home/tianyan/benchmarks/cpu2006/sh10.2"

OMP\_NUM\_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Sugon

SPECint2006 = 76.0

TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = 72.8

CPU2006 license: 9046  
Test sponsor: Sugon  
Tested by: Sugon

Test date: Dec-2017  
Hardware Availability: Dec-2017  
Software Availability: Apr-2017

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -auto-ilp32 -qopt-prefetch

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Sugon

SPECint2006 = 76.0

TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = 72.8

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Apr-2017

## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX512 -ipo -O3 -no-prec-div -inline-calloc  
-qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX512 -ipo -O3 -no-prec-div -parallel  
-qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)

456.hmmr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-ra-region-strategy=block  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Sugon-Purley-Platform-Settings-revA-I.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Purley-Platform-Settings-revA-I.xml>



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Sugon

SPECint2006 = 76.0

TC4600E(CX50-G30, Intel Gold 6132)

SPECint\_base2006 = 72.8

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Apr-2017

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Dec 27 12:06:18 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 December 2017.