



# SPEC® CPU2017 Floating Point Rate Result

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## Fujitsu

PRIMEQUEST 3800B, Intel Xeon Platinum 8153, 2.00GHz

SPECrate2017\_fp\_base = 604

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 19

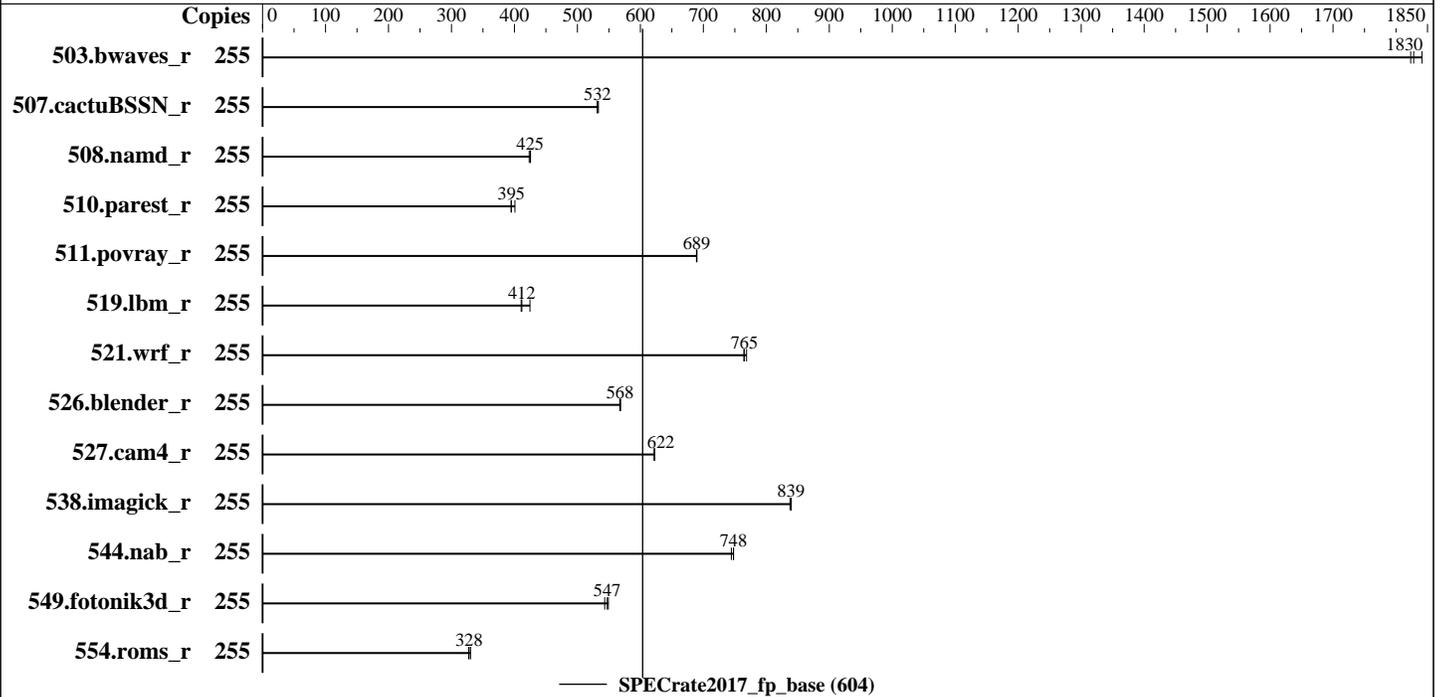
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Nov-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8153  
 Max MHz.: 2800  
 Nominal: 2000  
 Enabled: 128 cores, 8 chips, 2 threads/core  
 Orderable: 2,4,6,8 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 22 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (96 x 16 GB 2Rx4 PC4-2666V-R)  
 Storage: 768 GB tmpfs  
 Other: 1 x SAS HDD, 600 GB, 10.5K RPM, used for swap

### Software

OS: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Fujitsu BIOS Version V1.0.0.0 R1.21.0 for D3858-A1x. Released Dec-2017  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	255	1389	1840	<u>1399</u>	<u>1830</u>	1402	1820							
507.cactuBSSN_r	255	605	533	<u>607</u>	<u>532</u>	607	531							
508.namd_r	255	<u>570</u>	<u>425</u>	572	423	569	426							
510.parest_r	255	1664	401	<u>1689</u>	<u>395</u>	1690	395							
511.povray_r	255	<u>864</u>	<u>689</u>	864	689	863	690							
519.lbm_r	255	633	425	<u>653</u>	<u>412</u>	654	411							
521.wrf_r	255	743	769	747	764	<u>746</u>	<u>765</u>							
526.blender_r	255	<u>684</u>	<u>568</u>	685	567	683	569							
527.cam4_r	255	<u>717</u>	<u>622</u>	716	623	717	622							
538.imagick_r	255	757	838	755	840	<u>756</u>	<u>839</u>							
544.nab_r	255	576	744	<u>574</u>	<u>748</u>	574	748							
549.fotonik3d_r	255	1809	549	<u>1816</u>	<u>547</u>	1829	543							
554.roms_r	255	1227	330	<u>1236</u>	<u>328</u>	1237	327							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter : nohz_full=1-255 isolcpus=1-255
Set tmpfs filesystem with:
mkdir /home/memory
mount -t tmpfs -o size=768g,rw tmpfs /home/memory
Process tuning setting:
echo 0 > /proc/sys/kernel/numa_balancing
echo never > /sys/kernel/mm/transparent_hugepage/enabled
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
set affinity of rcu threads to the cpu0:
for i in `pgrep rcu` ; do taskset -pc 0 $i ; done
```



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### General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/memory/speccpu/lib/ia32:/home/memory/speccpu/lib/intel64"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/memory/speccpu/je5.0.1-32:/home/memory/speccpu/je5.0.1-64"  
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

### Platform Notes

BIOS configuration:  
DCU Streamer Prefetcher = Disabled  
Sub NUMA Clustering = Enabled  
Stale AtoS = Enabled  
LLC Dead Line Alloc = Disabled  
Fan Control = Full  
Sysinfo program /home/memory/speccpu/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on linux-k55j Thu Nov 9 20:28:00 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz  
8 "physical id"s (chips)  
256 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 16  
siblings : 32  
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 4: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 5: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 6: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 7: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:  
Architecture: x86\_64

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### Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              256
On-line CPU(s) list: 0-255
Thread(s) per core:  2
Core(s) per socket: 16
Socket(s):           8
NUMA node(s):        16
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz
Stepping:             4
CPU MHz:              2299.999
CPU max MHz:          2800.0000
CPU min MHz:          1000.0000
BogoMIPS:             4000.21
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             22528K
NUMA node0 CPU(s):   0-3,8-11,128-131,136-139
NUMA node1 CPU(s):   4-7,12-15,132-135,140-143
NUMA node2 CPU(s):   16-19,24-27,144-147,152-155
NUMA node3 CPU(s):   20-23,28-31,148-151,156-159
NUMA node4 CPU(s):   32-35,40-43,160-163,168-171
NUMA node5 CPU(s):   36-39,44-47,164-167,172-175
NUMA node6 CPU(s):   48-51,56-59,176-179,184-187
NUMA node7 CPU(s):   52-55,60-63,180-183,188-191
NUMA node8 CPU(s):   64-67,72-75,192-195,200-203
NUMA node9 CPU(s):   68-71,76-79,196-199,204-207
NUMA node10 CPU(s):  80-83,88-91,208-211,216-219
NUMA node11 CPU(s):  84-87,92-95,212-215,220-223
NUMA node12 CPU(s):  96-99,104-107,224-227,232-235
NUMA node13 CPU(s):  100-103,108-111,228-231,236-239
NUMA node14 CPU(s):  112-115,120-123,240-243,248-251
NUMA node15 CPU(s):  116-119,124-127,244-247,252-255
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm hwp
hwp_act_window hwp_epp hwp_pkg_req intel_pt tpr_shadow vnmi flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f
avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec

```

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### Platform Notes (Continued)

```
xgetbv1 cqm_llc cqm_occup_llc
```

```
/proc/cpuinfo cache data  
cache size : 22528 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 16 nodes (0-15)
node 0 cpus: 0 1 2 3 8 9 10 11 128 129 130 131 136 137 138 139
node 0 size: 95380 MB
node 0 free: 85784 MB
node 1 cpus: 4 5 6 7 12 13 14 15 132 133 134 135 140 141 142 143
node 1 size: 96765 MB
node 1 free: 96539 MB
node 2 cpus: 16 17 18 19 24 25 26 27 144 145 146 147 152 153 154 155
node 2 size: 96765 MB
node 2 free: 96539 MB
node 3 cpus: 20 21 22 23 28 29 30 31 148 149 150 151 156 157 158 159
node 3 size: 96765 MB
node 3 free: 96540 MB
node 4 cpus: 32 33 34 35 40 41 42 43 160 161 162 163 168 169 170 171
node 4 size: 96765 MB
node 4 free: 96539 MB
node 5 cpus: 36 37 38 39 44 45 46 47 164 165 166 167 172 173 174 175
node 5 size: 96765 MB
node 5 free: 96541 MB
node 6 cpus: 48 49 50 51 56 57 58 59 176 177 178 179 184 185 186 187
node 6 size: 96765 MB
node 6 free: 96538 MB
node 7 cpus: 52 53 54 55 60 61 62 63 180 181 182 183 188 189 190 191
node 7 size: 96765 MB
node 7 free: 96539 MB
node 8 cpus: 64 65 66 67 72 73 74 75 192 193 194 195 200 201 202 203
node 8 size: 96765 MB
node 8 free: 96538 MB
node 9 cpus: 68 69 70 71 76 77 78 79 196 197 198 199 204 205 206 207
node 9 size: 96765 MB
node 9 free: 96539 MB
node 10 cpus: 80 81 82 83 88 89 90 91 208 209 210 211 216 217 218 219
node 10 size: 96765 MB
node 10 free: 96538 MB
node 11 cpus: 84 85 86 87 92 93 94 95 212 213 214 215 220 221 222 223
node 11 size: 96765 MB
node 11 free: 96540 MB
node 12 cpus: 96 97 98 99 104 105 106 107 224 225 226 227 232 233 234 235
node 12 size: 96765 MB
node 12 free: 96538 MB
```

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### Platform Notes (Continued)

```

node 13 cpus: 100 101 102 103 108 109 110 111 228 229 230 231 236 237 238 239
node 13 size: 96765 MB
node 13 free: 96540 MB
node 14 cpus: 112 113 114 115 120 121 122 123 240 241 242 243 248 249 250 251
node 14 size: 96765 MB
node 14 free: 96539 MB
node 15 cpus: 116 117 118 119 124 125 126 127 244 245 246 247 252 253 254 255
node 15 size: 96617 MB
node 15 free: 96392 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
0:  10 11 35 35 35 35 40 40 40 40 40 40 35 35 40 40
1:  11 10 35 35 35 35 40 40 40 40 40 40 35 35 40 40
2:  35 35 10 11 40 40 35 35 40 40 35 35 40 40 40 40
3:  35 35 11 10 40 40 35 35 40 40 35 35 40 40 40 40
4:  35 35 40 40 10 11 35 35 35 35 40 40 40 40 40 40
5:  35 35 40 40 11 10 35 35 35 35 40 40 40 40 40 40
6:  40 40 35 35 35 35 10 11 40 40 40 40 40 40 35 35
7:  40 40 35 35 35 35 11 10 40 40 40 40 40 40 35 35
8:  40 40 40 40 35 35 40 40 10 11 35 35 35 35 40 40
9:  40 40 40 40 35 35 40 40 11 10 35 35 35 35 40 40
10: 40 40 35 35 40 40 40 40 35 35 10 11 40 40 35 35
11: 40 40 35 35 40 40 40 40 35 35 11 10 40 40 35 35
12: 35 35 40 40 40 40 40 40 35 35 40 40 10 11 35 35
13: 35 35 40 40 40 40 40 40 35 35 40 40 11 10 35 35
14: 40 40 40 40 40 40 35 35 40 40 35 35 35 35 10 11
15: 40 40 40 40 40 40 35 35 40 40 35 35 35 35 11 10

```

```

From /proc/meminfo
MemTotal:      1583837096 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"

```

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### Platform Notes (Continued)

CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

```
Linux linux-k55j 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 9 19:55

SPEC is set to: /home/memory/speccpu

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	768G	8.8G	760G	2%	/home/memory

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU V1.0.0.0 R1.21.0 for D3858-A1x 09/15/2017

Memory:

45x Hynix HMA42GR7BJR4N-VK 16 GB 2 rank 2666  
51x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
=====

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
CXXC 508.namd\_r(base) 510.parest\_r(base)  
=====

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
CC 511.povray\_r(base) 526.blender\_r(base)  
=====

icpc (ICC) 18.0.0 20170811

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icc (ICC) 18.0.0 20170811

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## Compiler Version Notes (Continued)

=====  
FC 507.cactuBSSN\_r(base)  
=====

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
FC 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

Benchmarks using both C and C++:  
icpc icc

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## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
 526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
 527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11
```

Benchmarks using both C and C++:

```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevC.html>

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevC.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

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