



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY CX2560 M4, Intel Xeon Gold 6126, 2.60GHz

SPECrate2017\_int\_base = 133

SPECrate2017\_int\_peak = 142

CPU2017 License: 19

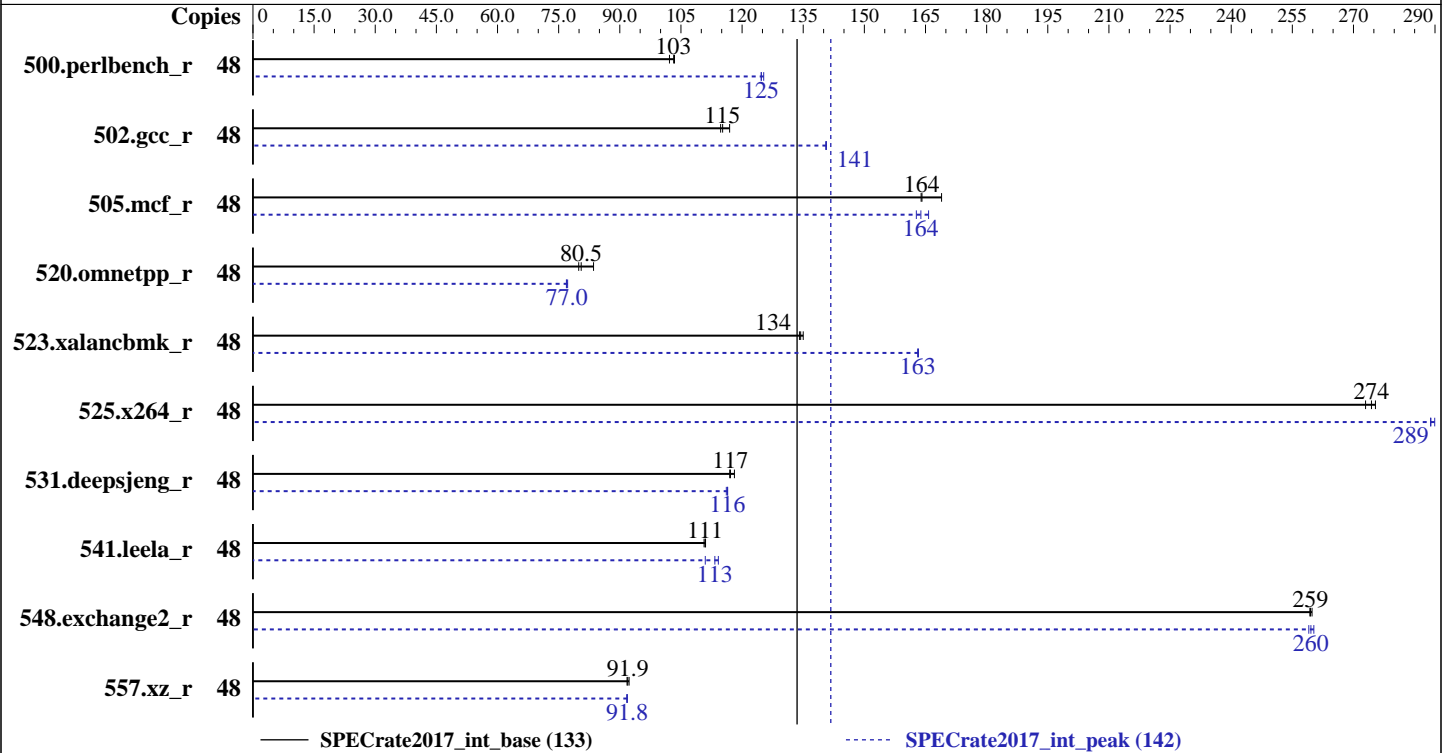
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Gold 6126  
 Max MHz.: 3700  
 Nominal: 2600  
 Enabled: 24 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 19.25 MB I+D on chip per chip  
 Other: None  
 Memory: 192 GB (12 x 16 GB 2Rx4 PC4-2666V-R)  
 Storage: 1 x SATA M.2 SSD, 128 GB  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Fujitsu BIOS Version V1.0.0.0 R1.18.0 for D3854-A1x. Released Dec-2017  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library V5.0.1;  
 jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets;  
 jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  
 jemalloc: sources available from jemalloc.net or releases



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## Results Table

| Benchmark       | Base   |                   |                   |                   |                    |                   |                   | Peak   |                   |                   |                   |                    |                   |                    |
|-----------------|--------|-------------------|-------------------|-------------------|--------------------|-------------------|-------------------|--------|-------------------|-------------------|-------------------|--------------------|-------------------|--------------------|
|                 | Copies | Seconds           | Ratio             | Seconds           | Ratio              | Seconds           | Ratio             | Copies | Seconds           | Ratio             | Seconds           | Ratio              | Seconds           | Ratio              |
| 500.perlbench_r | 48     | 739               | 103               | <b><u>740</u></b> | <b><u>103</u></b>  | 748               | 102               | 48     | 610               | 125               | 613               | 125                | <b><u>613</u></b> | <b><u>125</u></b>  |
| 502.gcc_r       | 48     | 581               | 117               | <b><u>590</u></b> | <b><u>115</u></b>  | 592               | 115               | 48     | 484               | 141               | <b><u>483</u></b> | <b><u>141</u></b>  | 483               | 141                |
| 505.mcf_r       | 48     | 459               | 169               | <b><u>473</u></b> | <b><u>164</u></b>  | 473               | 164               | 48     | 477               | 163               | 468               | 166                | <b><u>473</u></b> | <b><u>164</u></b>  |
| 520.omnetpp_r   | 48     | 754               | 83.6              | <b><u>782</u></b> | <b><u>80.5</u></b> | 788               | 79.9              | 48     | 819               | 76.9              | 816               | 77.2               | <b><u>818</u></b> | <b><u>77.0</u></b> |
| 523.xalancbmk_r | 48     | 375               | 135               | 378               | 134                | <b><u>377</u></b> | <b><u>134</u></b> | 48     | <b><u>311</u></b> | <b><u>163</u></b> | 310               | 163                | 311               | 163                |
| 525.x264_r      | 48     | 305               | 275               | <b><u>306</u></b> | <b><u>274</u></b>  | 308               | 273               | 48     | 290               | 290               | 291               | 289                | <b><u>291</u></b> | <b><u>289</u></b>  |
| 531.deepsjeng_r | 48     | 466               | 118               | <b><u>470</u></b> | <b><u>117</u></b>  | 470               | 117               | 48     | <b><u>473</u></b> | <b><u>116</u></b> | 472               | 116                | 474               | 116                |
| 541.leela_r     | 48     | <b><u>717</u></b> | <b><u>111</u></b> | 718               | 111                | 716               | 111               | 48     | 716               | 111               | <b><u>701</u></b> | <b><u>113</u></b>  | 696               | 114                |
| 548.exchange2_r | 48     | 485               | 259               | <b><u>485</u></b> | <b><u>259</u></b>  | 484               | 260               | 48     | 485               | 259               | 483               | 260                | <b><u>485</u></b> | <b><u>260</u></b>  |
| 557.xz_r        | 48     | 562               | 92.3              | <b><u>564</u></b> | <b><u>91.9</u></b> | 565               | 91.8              | 48     | 565               | 91.7              | <b><u>565</u></b> | <b><u>91.8</u></b> | 564               | 91.8               |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter: nohz_full=1-47
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Process tuning settings:
echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns
echo 15000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
echo 0 > /proc/sys/kernel/numa_balancing
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
```

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/Benchmark/speccpu2017/lib/ia32:/home/Benchmark/speccpu2017/lib/intel64"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/Benchmark/speccpu2017/je5.0.1-32:/home/Benchmark/speccpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default

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## General Notes (Continued)

Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS configuration:  
DCU Streamer Prefetcher = Disabled  
Intel Virtualization Technology = Disabled  
Power Technology = Custom  
HWPM Support = Disabled  
UPI Link Frequency Select = 10.4GT/s  
Sub NUMA Clustering = Enabled  
Stale AtoS = Enabled  
LLC dead line alloc = Disabled  
Sysinfo program /home/Benchmark/speccpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on linux-CX2560M4 Sat Dec 9 21:12:38 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz  
2 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12  
siblings : 24  
physical 0: cores 0 2 3 4 5 8 9 10 11 12 13 14  
physical 1: cores 0 1 3 4 5 6 8 9 10 11 12 13

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 48  
On-line CPU(s) list: 0-47  
Thread(s) per core: 2  
Core(s) per socket: 12  
Socket(s): 2  
NUMA node(s): 4

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### Platform Notes (Continued)

```

Vendor ID:           GenuineIntel
CPU family:         6
Model:              85
Model name:         Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz
Stepping:           4
CPU MHz:            2601.000
CPU max MHz:        2601.0000
CPU min MHz:        1000.0000
BogoMIPS:           5187.85
Virtualization:     VT-x
L1d cache:          32K
L1i cache:          32K
L2 cache:           1024K
L3 cache:           19712K
NUMA node0 CPU(s): 0-2,5-7,24-26,29-31
NUMA node1 CPU(s): 3,4,8-11,27,28,32-35
NUMA node2 CPU(s): 12-14,18-20,36-38,42-44
NUMA node3 CPU(s): 15-17,21-23,39-41,45-47

```

```

Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl6c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 19712 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 5 6 7 24 25 26 29 30 31
node 0 size: 46875 MB
node 0 free: 46581 MB
node 1 cpus: 3 4 8 9 10 11 27 28 32 33 34 35
node 1 size: 48379 MB
node 1 free: 48151 MB
node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44
node 2 size: 48379 MB
node 2 free: 48101 MB
node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47
node 3 size: 48246 MB
node 3 free: 47996 MB
node distances:

```

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### Platform Notes (Continued)

|      |    |    |    |    |
|------|----|----|----|----|
| node | 0  | 1  | 2  | 3  |
| 0:   | 10 | 11 | 23 | 23 |
| 1:   | 11 | 10 | 23 | 23 |
| 2:   | 23 | 23 | 10 | 11 |
| 3:   | 23 | 23 | 11 | 10 |

From /proc/meminfo

MemTotal: 196486472 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
SUSE Linux Enterprise Server 12 SP2

From /etc/\*release\* /etc/\*version\*

SuSE-release:  
SUSE Linux Enterprise Server 12 (x86\_64)  
VERSION = 12  
PATCHLEVEL = 2  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.

os-release:  
NAME="SLES"  
VERSION="12-SP2"  
VERSION\_ID="12.2"  
PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"  
ID="sles"  
ANSI\_COLOR="0;32"  
CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:  
Linux linux-CX2560M4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Dec 8 20:31

SPEC is set to: /home/Benchmark/speccpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sdb1 xfs 120G 19G 101G 16% /home/Benchmark

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU V1.0.0.0 R1.9.6 for D3854-A1x 10/06/2017  
Memory:  
12x Hynix HMA42GR7BJR4N-VK 16 GB 2 rank 2666

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## Platform Notes (Continued)

4x Not Specified Not Specified

(End of data from sysinfo program)

## Compiler Version Notes

=====  
CC 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
525.x264\_r(base, peak) 557.xz\_r(base, peak)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 500.perlbench\_r(peak) 502.gcc\_r(peak)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 520.omnetpp\_r(peak) 523.xalancbmk\_r(peak) 531.deepsjeng\_r(peak)  
541.leela\_r(peak)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 548.exchange2\_r(base, peak)  
-----

ifort (IFORT) 18.0.0 20170811  
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-----



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```



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## Base Other Flags

C benchmarks:  
-m64 -std=c11

C++ benchmarks:  
-m64

Fortran benchmarks:  
-m64

## Peak Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: -w1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib

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## Peak Optimization Flags (Continued)

500.perlbench\_r (continued):

-ljemalloc

502.gcc\_r: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32

-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo

-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3

-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf\_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib

-ljemalloc

525.x264\_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -fno-alias

-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

520.omnetpp\_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo

-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3

-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk\_r: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32

-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo

-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3

-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng\_r: Same as 520.omnetpp\_r

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

-L/usr/local/je5.0.1-64/lib -ljemalloc

## Peak Other Flags

C benchmarks (except as noted below):

-m64 -std=c11

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## Peak Other Flags (Continued)

502.gcc\_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk\_r: -m32

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.xml>

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