



# SPEC® CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

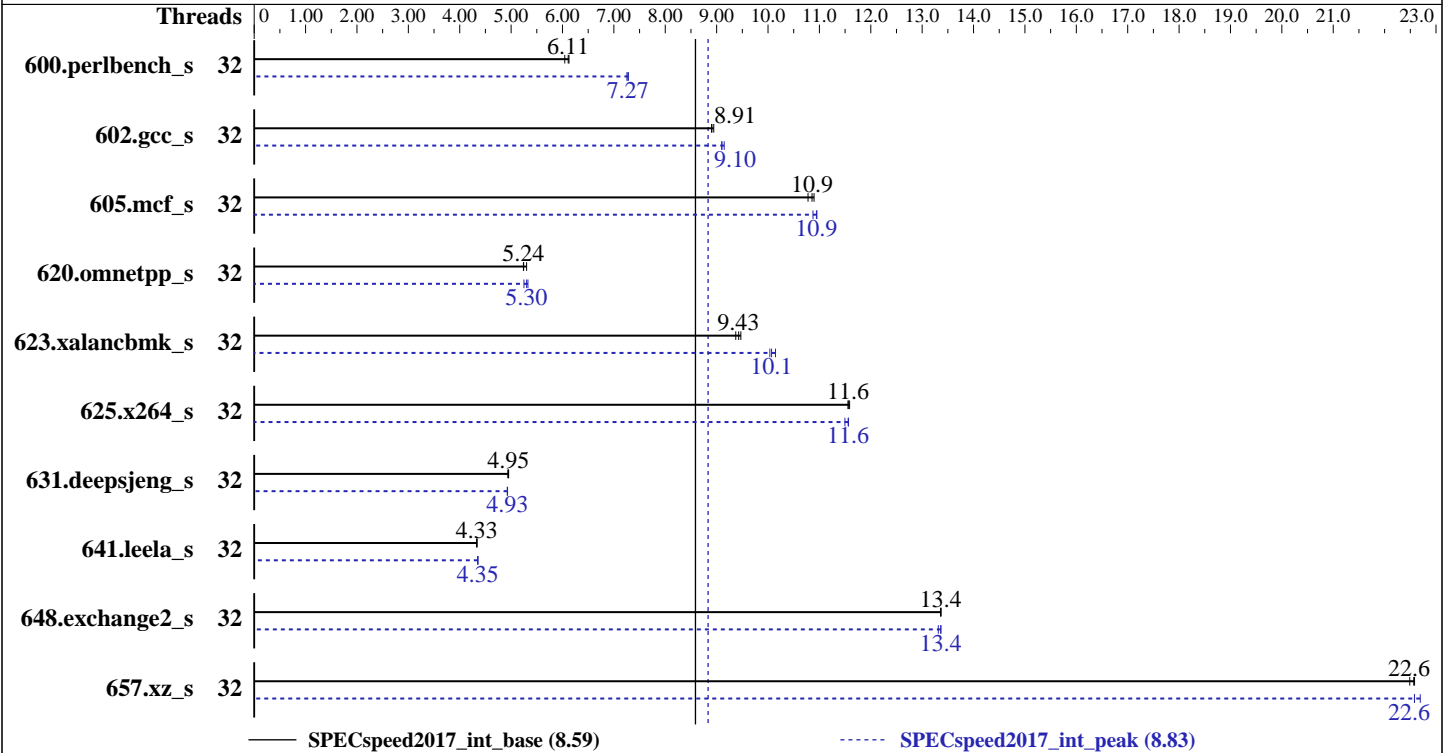
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8156  
 Max MHz.: 3700  
 Nominal: 3600  
 Enabled: 32 cores, 8 chips  
 Orderable: 2,4,8 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 3 TB (96 x 32 GB 2Rx4 PC4-2666V-R)  
 Storage: 1 x 800 GB SAS SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran  
 Compiler for Linux  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version PSE105X 1.00 released Aug-2017  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library  
 V5.0.1



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Jan-2018  
Hardware Availability: Sep-2017  
Software Availability: Sep-2017

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<b>290</b>	<b>6.11</b>	290	6.13	294	6.04	32	<b>244</b>	<b>7.27</b>	244	7.28	245	7.25
602.gcc_s	32	<b>447</b>	<b>8.91</b>	447	8.90	445	8.94	32	435	9.15	<b>437</b>	<b>9.10</b>	438	9.10
605.mcf_s	32	<b>435</b>	<b>10.9</b>	433	10.9	438	10.8	32	<b>431</b>	<b>10.9</b>	434	10.9	431	11.0
620.omnetpp_s	32	311	5.24	308	5.30	<b>311</b>	<b>5.24</b>	32	<b>308</b>	<b>5.30</b>	306	5.33	311	5.25
623.xalancbmk_s	32	151	9.37	<b>150</b>	<b>9.43</b>	150	9.47	32	<b>141</b>	<b>10.1</b>	141	10.0	140	10.1
625.x264_s	32	152	11.6	<b>153</b>	<b>11.6</b>	153	11.6	32	153	11.5	<b>153</b>	<b>11.6</b>	153	11.6
631.deepsjeng_s	32	290	4.94	<b>290</b>	<b>4.95</b>	289	4.95	32	291	4.93	<b>291</b>	<b>4.93</b>	291	4.93
641.leela_s	32	394	4.33	393	4.34	<b>394</b>	<b>4.33</b>	32	<b>392</b>	<b>4.35</b>	392	4.35	392	4.35
648.exchange2_s	32	220	13.4	<b>220</b>	<b>13.4</b>	220	13.4	32	<b>220</b>	<b>13.4</b>	220	13.4	221	13.3
657.xz_s	32	274	22.6	275	22.5	<b>274</b>	<b>22.6</b>	32	<b>274</b>	<b>22.6</b>	272	22.7	274	22.6

SPECspeed2017\_int\_base = **8.59**

SPECspeed2017\_int\_peak = **8.83**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc: configured and built at default for  
32bit (i686) and 64bit (x86\_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4,  
and the system compiler gcc 4.8.5;  
jemalloc: sources available from jemalloc.net or  
<https://github.com/jemalloc/jemalloc/releases>  
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed2017\_int\_base = 8.59

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

Test Date: Jan-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Sep-2017

Tested by: Lenovo Global Technology

Software Availability: Sep-2017

## General Notes (Continued)

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.html>. This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

Hyper-Threading set to Disable

MONITORM/WAIT set to Enable

Execute Disable Bit set to Disable

Intel Virtualization Technology set to Disable

Uncore Frequency Scaling set to Disable

XPT Prefetcher set to Enable

UPI Prefetcher set to Disable

Stale AtoS set to Enable

    Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo

    Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

    running on linux-boxi Fri Jan 12 18:58:45 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

    model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz

    8 "physical id"s (chips)

    32 "processors"

    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

    cpu cores : 4

    siblings : 4

    physical 0: cores 1 5 9 13

    physical 1: cores 1 2 5 11

    physical 2: cores 1 5 9 13

    physical 3: cores 1 2 5 11

    physical 4: cores 1 2 5 11

    physical 5: cores 2 3 4 9

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed2017\_int\_base = 8.59

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_peak = 8.83

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

```
physical 6: cores 1 5 9 13
physical 7: cores 1 5 9 13
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:   0-31
Thread(s) per core:    1
Core(s) per socket:    4
Socket(s):             8
NUMA node(s):         8
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
Stepping:              4
CPU MHz:               3591.555
BogoMIPS:              7183.11
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              16896K
NUMA node0 CPU(s):    0-3
NUMA node1 CPU(s):    4-7
NUMA node2 CPU(s):    8-11
NUMA node3 CPU(s):    12-15
NUMA node4 CPU(s):    16-19
NUMA node5 CPU(s):    20-23
NUMA node6 CPU(s):    24-27
NUMA node7 CPU(s):    28-31
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
```

```
/proc/cpuinfo cache data
cache size : 16896 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017

### Platform Notes (Continued)

```

physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3
node 0 size: 386513 MB
node 0 free: 385573 MB
node 1 cpus: 4 5 6 7
node 1 size: 387057 MB
node 1 free: 386069 MB
node 2 cpus: 8 9 10 11
node 2 size: 387057 MB
node 2 free: 386150 MB
node 3 cpus: 12 13 14 15
node 3 size: 387057 MB
node 3 free: 386145 MB
node 4 cpus: 16 17 18 19
node 4 size: 387057 MB
node 4 free: 386196 MB
node 5 cpus: 20 21 22 23
node 5 size: 387057 MB
node 5 free: 386163 MB
node 6 cpus: 24 25 26 27
node 6 size: 387057 MB
node 6 free: 386202 MB
node 7 cpus: 28 29 30 31
node 7 size: 387052 MB
node 7 free: 386109 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 21 31 21 21 31 31 31
  1: 21 10 21 31 31 21 31 31
  2: 31 21 10 21 31 31 21 31
  3: 21 31 21 10 31 31 31 21
  4: 21 31 31 31 10 21 21 31
  5: 31 21 31 31 21 10 31 21
  6: 31 31 21 31 21 31 10 21
  7: 31 31 31 21 31 21 21 10

```

```

From /proc/meminfo
MemTotal:      3170210052 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2

```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.

```
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-boxi 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 12 18:57
```

```
SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdb2       btrfs    744G   25G  719G   4% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Lenovo -[PSE105X-1.00]- 08/17/2017
Memory:
96x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base,  
peak) 657.xz_s(base)
```

```
-----  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
```

```
-----  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Compiler Version Notes (Continued)

=====  
CXXC 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base)  
641.leela\_s(base)  
=====

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 620.omnetpp\_s(peak) 623.xalancbmk\_s(peak) 631.deepsjeng\_s(peak)  
641.leela\_s(peak)  
=====

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 648.exchange2\_s(base, peak)  
=====

-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Base Portability Flags (Continued)

625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc

## Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

(Continued on next page)





# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018

**Hardware Availability:** Sep-2017

**Software Availability:** Sep-2017

## Peak Optimization Flags (Continued)

657.xz\_s: Same as 602.gcc\_s

C++ benchmarks:

```
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

631.deepsjeng\_s: Same as 620.omnetpp\_s

641.leela\_s: Same as 620.omnetpp\_s

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Peak Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks (except as noted below):

```
-m64
```

623.xalancbmk\_s: -m32

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.html>



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017\_int\_base = 8.59

SPECspeed2017\_int\_peak = 8.83

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018

**Hardware Availability:** Sep-2017

**Software Availability:** Sep-2017

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-01-12 05:58:44-0500.

Report generated on 2018-10-31 16:54:59 by CPU2017 PDF formatter v6067.

Originally published on 2018-03-06.