



# SPEC® CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### Synergy 480 Gen10

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

CPU2017 License: 3

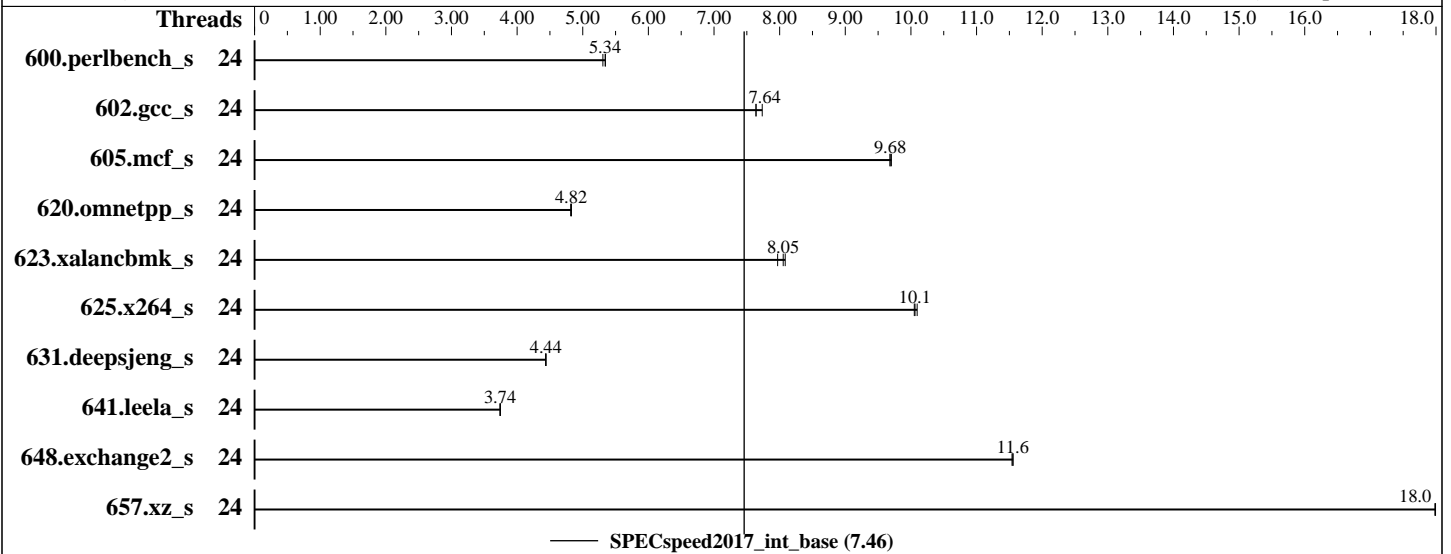
Test Sponsor: HPE

Tested by: HPE

Test Date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Gold 5118  
 Max MHz.: 3200  
 Nominal: 2300  
 Enabled: 24 cores, 2 chips  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
 Storage: 1 x 480 GB SATA SSD, RAID 0  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Kernel 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: HPE BIOS Version I42 11/14/2017 released Nov-2017  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator library V5.0.1



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**Synergy 480 Gen10**

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
600.perlbench_s	24	334	5.31	<u>332</u>	<u>5.34</u>	332	5.35									
602.gcc_s	24	<u>521</u>	<u>7.64</u>	521	7.64	515	7.73									
605.mcf_s	24	488	9.68	487	9.70	<b>488</b>	<b>9.68</b>									
620.omnetpp_s	24	338	4.83	<u>338</u>	<u>4.82</u>	339	4.82									
623.xalancbmk_s	24	175	8.08	178	7.97	<u>176</u>	<b>8.05</b>									
625.x264_s	24	<u>175</u>	<u>10.1</u>	175	10.1	175	10.1									
631.deepsjeng_s	24	<u>323</u>	<u>4.44</u>	323	4.44	323	4.43									
641.leela_s	24	<b>456</b>	<b>3.74</b>	456	3.75	456	3.74									
648.exchange2_s	24	254	11.6	255	11.5	<b>254</b>	<b>11.6</b>									
657.xz_s	24	344	18.0	344	18.0	<u>344</u>	<u>18.0</u>									

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled by default  
 Filesystem page cache cleared with:  
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
 irqbalance service stopped using "systemctl stop irqbalance.service"  
 Used throughput-performance profile for tuned-adm: "tuned-adm profile throughput-performance profile"

## General Notes

Environment variables set by runcpu before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,compact"  
 LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"  
 OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4  
 No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**Synergy 480 Gen10**

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## General Notes (Continued)

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.htm>.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets; built with RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; sources available from [jemalloc.net](http://jemalloc.net) or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Configuration:

Intel Hyper-Threading set to Disabled  
Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Peak Frequency Compute  
Energy/Performance Bias set to Maximum Performance  
Workload Profile set to Custom  
NUMA Group Size Optimization set to Flat  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on SY480\_M3\_RHEL Thu Dec 21 20:47:19 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**Synergy 480 Gen10**

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Platform Notes (Continued)

From `lscpu`:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 24
On-line CPU(s) list:   0-23
Thread(s) per core:    1
Core(s) per socket:    12
Socket(s):              2
NUMA node(s):          2
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
Stepping:               4
CPU MHz:                2300.000
BogoMIPS:               4605.29
Virtualization:        VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               16896K
NUMA node0 CPU(s):     0-11
NUMA node1 CPU(s):     12-23

```

```

/proc/cpuinfo cache data
cache size : 16896 KB

```

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

From `/proc/meminfo`

```

MemTotal:      395931452 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From `/etc/*release* /etc/*version*`

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**Synergy 480 Gen10**

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Platform Notes (Continued)

```
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux SY480_M3_RHEL 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 21 20:42
```

SPEC is set to: /home/cpu2017

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   392G   25G  368G   7% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 11/14/2017

Memory:

24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

## Compiler Version Notes

```
=====  
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)
657.xz_s(base)
```

```
-----  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
=====  
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)
```

```
-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
=====  
FC 648.exchange2_s(base)
```

```
-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**Synergy 480 Gen10**

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Compiler Version Notes (Continued)

---

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc



# SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

Synergy 480 Gen10

(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_int\_base = 7.46

SPECspeed2017\_int\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Dec-2017

**Hardware Availability:** Nov-2017

**Software Availability:** Sep-2017

## Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-12-21 10:17:18-0500.

Report generated on 2018-10-31 18:05:10 by CPU2017 PDF formatter v6067.

Originally published on 2018-06-13.