



# SPEC CPU®2017 Floating Point Rate Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL580 Gen10

(2.60 GHz, Intel Xeon Gold 6240M)

SPECrate®2017\_fp\_base = 400

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 3

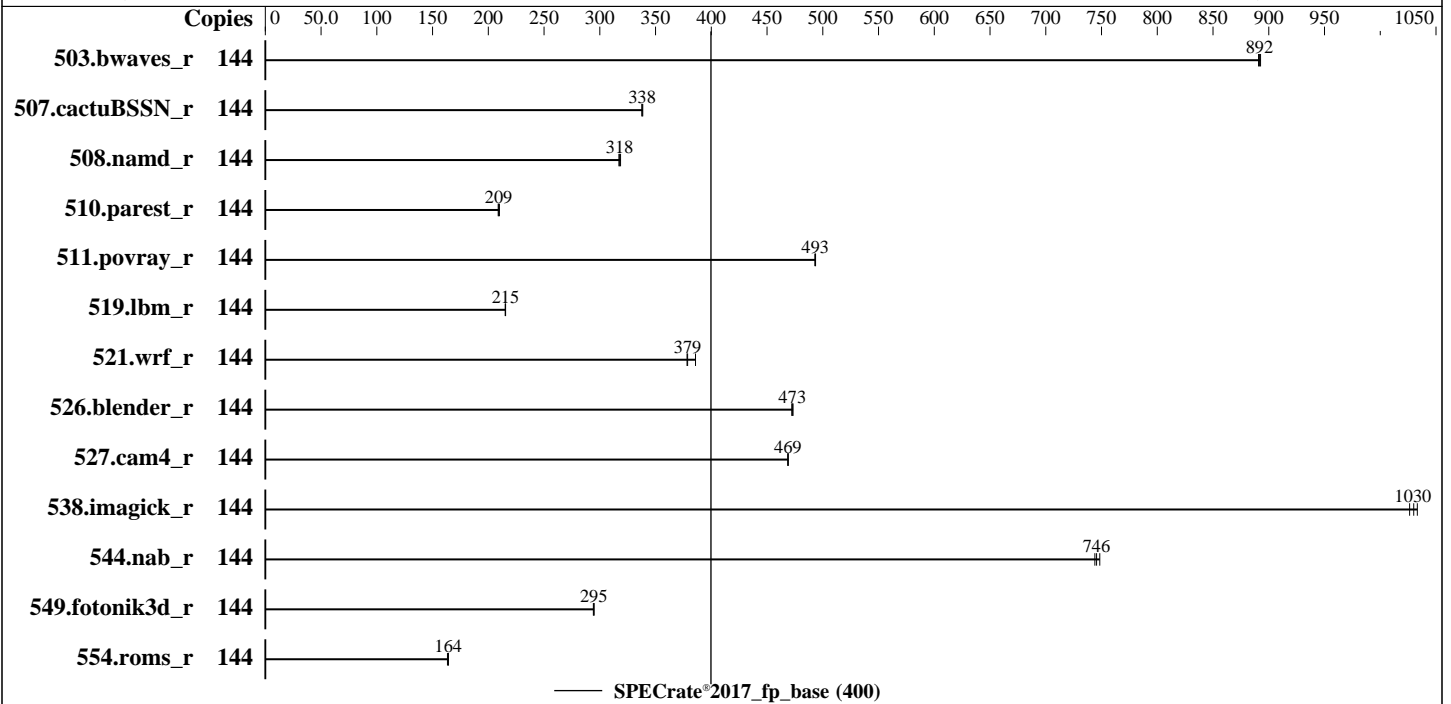
Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2019

Hardware Availability: Apr-2019

Software Availability: Feb-2019



## Hardware

CPU Name: Intel Xeon Gold 6240M  
 Max MHz: 3900  
 Nominal: 2600  
 Enabled: 72 cores, 4 chips, 2 threads/core  
 Orderable: 1, 2, 3, 4 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 400 GB SAS SSD, RAID 0  
 Other: None

## Software

OS: SUSE Linux Enterprise Server 15 (x86\_64)  
 Kernel 4.12.14-23-default  
 Compiler: C/C++: Version 19.0.2.187 of Intel C/C++  
 Compiler Build 20190117 for Linux;  
 Fortran: Version 19.0.2.187 of Intel Fortran  
 Compiler Build 20190117 for Linux  
 Parallel: No  
 Firmware: HPE BIOS Version U34 05/21/2019 released Apr-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: --



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	144	1621	891	<b>1619</b>	<b>892</b>	1618	893							
507.cactuBSSN_r	144	<b>539</b>	<b>338</b>	539	339	540	338							
508.namd_r	144	<b>431</b>	<b>318</b>	429	319	431	317							
510.parest_r	144	1794	210	1804	209	<b>1800</b>	<b>209</b>							
511.povray_r	144	<b>682</b>	<b>493</b>	682	493	681	494							
519.lbm_r	144	706	215	<b>704</b>	<b>215</b>	704	215							
521.wrf_r	144	<b>852</b>	<b>379</b>	836	386	853	378							
526.blender_r	144	463	473	464	472	<b>464</b>	<b>473</b>							
527.cam4_r	144	537	469	<b>537</b>	<b>469</b>	537	469							
538.imagick_r	144	<b>348</b>	<b>1030</b>	349	1030	347	1030							
544.nab_r	144	326	744	<b>325</b>	<b>746</b>	324	748							
549.fotonik3d_r	144	1903	295	<b>1904</b>	<b>295</b>	1907	294							
554.roms_r	144	1401	163	<b>1394</b>	<b>164</b>	1394	164							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017\_u2/lib/ia32:/home/cpu2017\_u2/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Enhanced Processor Performance set to Enabled  
Workload Profile set to General Throughput Compute  
Workload Profile set to Custom  
Energy/Performance Bias set to Balanced Performance  
Advanced Memory Protection set to Advanced ECC

Sysinfo program /home/cpu2017\_u2/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-sypg Fri Sep 7 21:26:03 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6240M CPU @ 2.60GHz
 4 "physical id"s (chips)
 144 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 18
siblings  : 36
physical 0: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 144
On-line CPU(s) list: 0-143
Thread(s) per core: 2
Core(s) per socket: 18
```

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## Platform Notes (Continued)

```

Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240M CPU @ 2.60GHz
Stepping: 7
CPU MHz: 2600.000
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-8,72-80
NUMA node1 CPU(s): 9-17,81-89
NUMA node2 CPU(s): 18-26,90-98
NUMA node3 CPU(s): 27-35,99-107
NUMA node4 CPU(s): 36-44,108-116
NUMA node5 CPU(s): 45-53,117-125
NUMA node6 CPU(s): 54-62,126-134
NUMA node7 CPU(s): 63-71,135-143
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
epb cat_l3 cdp_l3 invpcid_single intel_ppin mba tpr_shadow vnmi flexpriority ept
vpid fsgsbase tsc_adjust bml hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
ibpb ibrs stibp dtherm ida arat pln pts pku ospke avx512_vnni arch_capabilities ssbd

```

```
/proc/cpuinfo cache data
cache size : 25344 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 4 5 6 7 8 72 73 74 75 76 77 78 79 80
node 0 size: 96349 MB
node 0 free: 96004 MB
node 1 cpus: 9 10 11 12 13 14 15 16 17 81 82 83 84 85 86 87 88 89
node 1 size: 96764 MB
node 1 free: 96339 MB
node 2 cpus: 18 19 20 21 22 23 24 25 26 90 91 92 93 94 95 96 97 98

```

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## Platform Notes (Continued)

```

node 2 size: 96764 MB
node 2 free: 96639 MB
node 3 cpus: 27 28 29 30 31 32 33 34 35 99 100 101 102 103 104 105 106 107
node 3 size: 96764 MB
node 3 free: 96635 MB
node 4 cpus: 36 37 38 39 40 41 42 43 44 108 109 110 111 112 113 114 115 116
node 4 size: 96764 MB
node 4 free: 96642 MB
node 5 cpus: 45 46 47 48 49 50 51 52 53 117 118 119 120 121 122 123 124 125
node 5 size: 96764 MB
node 5 free: 96651 MB
node 6 cpus: 54 55 56 57 58 59 60 61 62 126 127 128 129 130 131 132 133 134
node 6 size: 96764 MB
node 6 free: 96646 MB
node 7 cpus: 63 64 65 66 67 68 69 70 71 135 136 137 138 139 140 141 142 143
node 7 size: 96734 MB
node 7 free: 96620 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 21 31 31 31 31 31 31
  1: 21 10 31 31 31 31 31 31
  2: 31 31 10 21 31 31 31 31
  3: 31 31 21 10 31 31 31 31
  4: 31 31 31 31 10 21 31 31
  5: 31 31 31 31 21 10 31 31
  6: 31 31 31 31 31 31 10 21
  7: 31 31 31 31 31 31 21 10

```

```

From /proc/meminfo
MemTotal: 792238768 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

```

```

uname -a:
Linux linux-sypg 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

```

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## Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected  
CVE-2017-5753 (Spectre variant 1): Mitigation: \_\_user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS\_FW

run-level 3 Sep 7 21:24

SPEC is set to: /home/cpu2017\_u2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda1	xfs	894G	84G	811G	10%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 05/21/2019

Memory:

24x UNKNOWN NOT AVAILABLE  
24x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2666

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
-----

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
-----

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
-----

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## Compiler Version Notes (Continued)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117

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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117

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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.2.187 Build 20190117

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117

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=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117

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=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.2.187 Build 20190117

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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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## Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:

icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64

507.cactuBSSN\_r: -DSPEC\_LP64

508.namd\_r: -DSPEC\_LP64

510.parest\_r: -DSPEC\_LP64

511.povray\_r: -DSPEC\_LP64

519.lbm\_r: -DSPEC\_LP64

521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char

527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG

538.imagick\_r: -DSPEC\_LP64

544.nab\_r: -DSPEC\_LP64

549.fotonik3d\_r: -DSPEC\_LP64

554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=4

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

-qopt-mem-layout-trans=4

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## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte
```

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml>

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