



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_fp\_base = 126

PowerEdge R7515 (AMD EPYC 7742, 2.25GHz)

SPECspeed®2017\_fp\_peak = 130

CPU2017 License: 55

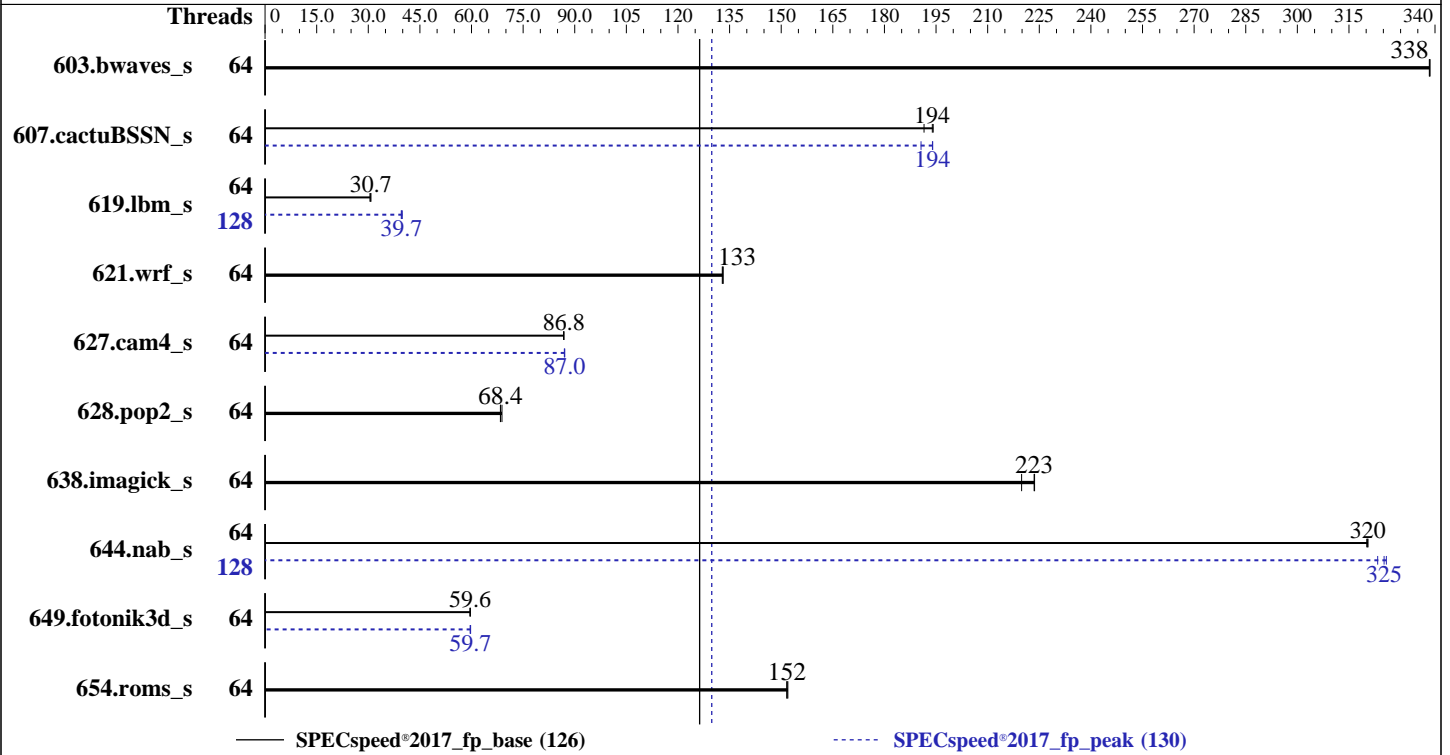
Test Date: Sep-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019



### Hardware

CPU Name: AMD EPYC 7742  
 Max MHz: 3400  
 Nominal: 2250  
 Enabled: 64 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 16 MB shared / 4 cores  
 Other: None  
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R, running at 3200)  
 Storage: 1 x 960 GB SAS SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP1  
 kernel 4.12.14-195-default  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version 1.0.4 released Aug-2019  
 File System: xfs  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: --



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	174	338	174	339	<b>174</b>	<b>338</b>	64	174	338	174	339	<b>174</b>	<b>338</b>
607.cactuBSSN_s	64	85.9	194	<b>86.0</b>	<b>194</b>	87.0	192	64	<b>85.9</b>	<b>194</b>	87.4	191	85.9	194
619.lbm_s	64	<b>171</b>	<b>30.7</b>	170	30.7	172	30.5	128	132	39.6	131	39.9	<b>132</b>	<b>39.7</b>
621.wrf_s	64	<b>99.4</b>	<b>133</b>	99.5	133	99.4	133	64	<b>99.4</b>	<b>133</b>	99.5	133	99.4	133
627.cam4_s	64	102	86.8	102	86.8	<b>102</b>	<b>86.8</b>	64	102	87.0	102	87.1	<b>102</b>	<b>87.0</b>
628.pop2_s	64	<b>174</b>	<b>68.4</b>	174	68.4	172	68.9	64	<b>174</b>	<b>68.4</b>	174	68.4	172	68.9
638.imagick_s	64	65.6	220	<b>64.5</b>	<b>223</b>	64.5	224	64	65.6	220	<b>64.5</b>	<b>223</b>	64.5	224
644.nab_s	64	54.5	321	54.6	320	<b>54.5</b>	<b>320</b>	128	53.6	326	54.0	323	<b>53.7</b>	<b>325</b>
649.fotonik3d_s	64	153	59.6	<b>153</b>	<b>59.6</b>	153	59.6	64	153	59.7	153	59.6	<b>153</b>	<b>59.7</b>
654.roms_s	64	104	152	104	152	<b>104</b>	<b>152</b>	64	104	152	104	152	<b>104</b>	<b>152</b>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)



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## General Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-127"

LD\_LIBRARY\_PATH = "/root/cpu2017-1.0.5/amd\_speed\_aocc200\_rome\_B\_lib/64;

/root/cpu2017-1.0.5/amd\_speed\_aocc200\_rome\_B\_lib/32:"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "128"

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS settings:

NUMA Nodes Per Socket set to 4

CCX as NUMA Domain set to Enabled

System Profile set to Custom

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost Enabled

Cstates set to Enabled

Memory Patrol Scrub Disabled

Memory Refresh Rate set to 1x

PCI ASPM L1 Link Power Management Disabled

Determinism Slider set to Power Determinism

Efficiency Optimized Mode Disabled

Sysinfo program /root/cpu2017-1.0.5/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on linux-g3ob Thu Sep 5 16:36:42 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : AMD EPYC 7742 64-Core Processor
 1 "physical id"s (chips)
128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 64
siblings  : 128
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63

```

```

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
Address sizes:          43 bits physical, 48 bits virtual
CPU(s):                 128
On-line CPU(s) list:   0-127
Thread(s) per core:    2
Core(s) per socket:    64
Socket(s):              1
NUMA node(s):          16
Vendor ID:              AuthenticAMD
CPU family:             23
Model:                  49
Model name:             AMD EPYC 7742 64-Core Processor
Stepping:               0
CPU MHz:                2245.880
BogoMIPS:               4491.76
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              32K
L2 cache:               512K
L3 cache:               16384K
NUMA node0 CPU(s):     0-3,64-67
NUMA node1 CPU(s):     4-7,68-71
NUMA node2 CPU(s):     8-11,72-75
NUMA node3 CPU(s):     12-15,76-79
NUMA node4 CPU(s):     16-19,80-83
NUMA node5 CPU(s):     20-23,84-87
NUMA node6 CPU(s):     24-27,88-91
NUMA node7 CPU(s):     28-31,92-95
NUMA node8 CPU(s):     32-35,96-99
NUMA node9 CPU(s):     36-39,100-103
NUMA node10 CPU(s):    40-43,104-107

```

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## Platform Notes (Continued)

NUMA node11 CPU(s): 44-47,108-111  
NUMA node12 CPU(s): 48-51,112-115  
NUMA node13 CPU(s): 52-55,116-119  
NUMA node14 CPU(s): 56-59,120-123  
NUMA node15 CPU(s): 60-63,124-127

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm  
constant\_tsc rep\_good nopl xtopology nonstop\_tsc cpuid extd\_apicid aperfmperf pni  
pclmulqdq monitor ssse3 fma cx16 sse4\_1 sse4\_2 movbe popcnt aes xsave avx f16c  
rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_l2 mwaitx cpb  
cat\_l3 cdp\_l3 hw\_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep  
bmi2 cqm rdt\_a rdseed adx smap clflushopt clwb sha\_ni xsaveopt xsavec xgetbv1 xsaves  
cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local clzero irperf xsaveerptr arat npt  
lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif umip rdpid overflow\_recov succor smca

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 16 nodes (0-15)
node 0 cpus: 0 1 2 3 64 65 66 67
node 0 size: 15691 MB
node 0 free: 15594 MB
node 1 cpus: 4 5 6 7 68 69 70 71
node 1 size: 16126 MB
node 1 free: 15466 MB
node 2 cpus: 8 9 10 11 72 73 74 75
node 2 size: 16126 MB
node 2 free: 15981 MB
node 3 cpus: 12 13 14 15 76 77 78 79
node 3 size: 16125 MB
node 3 free: 16055 MB
node 4 cpus: 16 17 18 19 80 81 82 83
node 4 size: 16126 MB
node 4 free: 15955 MB
node 5 cpus: 20 21 22 23 84 85 86 87
node 5 size: 16126 MB
node 5 free: 16004 MB
node 6 cpus: 24 25 26 27 88 89 90 91
node 6 size: 16126 MB
node 6 free: 16008 MB
node 7 cpus: 28 29 30 31 92 93 94 95
node 7 size: 16125 MB
node 7 free: 16056 MB
```

(Continued on next page)



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## Platform Notes (Continued)

```

node 8 cpus: 32 33 34 35 96 97 98 99
node 8 size: 16126 MB
node 8 free: 15933 MB
node 9 cpus: 36 37 38 39 100 101 102 103
node 9 size: 16096 MB
node 9 free: 16071 MB
node 10 cpus: 40 41 42 43 104 105 106 107
node 10 size: 16126 MB
node 10 free: 16066 MB
node 11 cpus: 44 45 46 47 108 109 110 111
node 11 size: 16125 MB
node 11 free: 16097 MB
node 12 cpus: 48 49 50 51 112 113 114 115
node 12 size: 16126 MB
node 12 free: 16032 MB
node 13 cpus: 52 53 54 55 116 117 118 119
node 13 size: 16126 MB
node 13 free: 16064 MB
node 14 cpus: 56 57 58 59 120 121 122 123
node 14 size: 16126 MB
node 14 free: 15889 MB
node 15 cpus: 60 61 62 63 124 125 126 127
node 15 size: 16111 MB
node 15 free: 16046 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
 0:  10 11 11 11 12 12 12 12 12 12 12 12 12 12 12 12
 1:  11 10 11 11 12 12 12 12 12 12 12 12 12 12 12 12
 2:  11 11 10 11 12 12 12 12 12 12 12 12 12 12 12 12
 3:  11 11 11 10 12 12 12 12 12 12 12 12 12 12 12 12
 4:  12 12 12 12 10 11 11 11 12 12 12 12 12 12 12 12
 5:  12 12 12 12 11 10 11 11 12 12 12 12 12 12 12 12
 6:  12 12 12 12 11 11 10 11 12 12 12 12 12 12 12 12
 7:  12 12 12 12 11 11 11 10 12 12 12 12 12 12 12 12
 8:  12 12 12 12 12 12 12 12 10 11 11 11 12 12 12 12
 9:  12 12 12 12 12 12 12 12 11 10 11 11 12 12 12 12
10:  12 12 12 12 12 12 12 12 11 11 10 11 12 12 12 12
11:  12 12 12 12 12 12 12 12 11 11 11 10 12 12 12 12
12:  12 12 12 12 12 12 12 12 12 12 12 12 10 11 11 11
13:  12 12 12 12 12 12 12 12 12 12 12 12 11 10 11 11
14:  12 12 12 12 12 12 12 12 12 12 12 12 11 11 10 11
15:  12 12 12 12 12 12 12 12 12 12 12 12 11 11 11 10

```

```

From /proc/meminfo
MemTotal:      263721456 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

(Continued on next page)



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## Platform Notes (Continued)

```

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15-SP1"
  VERSION_ID="15.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
  Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):          Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional,
IBRS_FW, STIBP: conditional, RSB filling

run-level 5 Sep 4 18:53

SPEC is set to: /root/cpu2017-1.0.5
  Filesystem      Type      Size      Used Avail Use% Mounted on
  /dev/sda2       xfs       440G      19G  422G   5% /

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Dell Inc. 1.0.4 08/26/2019
  Memory:
  5x 80AD80B380AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
  1x 80AD863280AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
  2x 80AD869D80AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
  8x Not Specified Not Specified

(End of data from sysinfo program)

```

## Compiler Version Notes

```

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)

```

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## Compiler Version Notes (Continued)

| 644.nab\_s(base, peak)

-----  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

-----  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
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Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

-----  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

-----  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
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## Compiler Version Notes (Continued)

```

Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

```

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-fltO -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC

```

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## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-DSPEC_OPENMP -fopenmp -DUSE_OPENMP -fopenmp=libomp -lomp -lpthread
-ldl -lmvec -lamdlibm -ljemalloc -lflang
```



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SPECspeed®2017\_fp\_base = 126

PowerEdge R7515 (AMD EPYC 7742, 2.25GHz)

SPECspeed®2017\_fp\_peak = 130

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Sep-2019

Hardware Availability: Sep-2019

Software Availability: Aug-2019

## Base Other Flags

C benchmarks:

-Wno-return-type

Fortran benchmarks:

-Wno-return-type

Benchmarks using both Fortran and C:

-Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
```

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## Peak Optimization Flags (Continued)

619.lbm\_s (continued):

```
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -lmvec -lamdlibm -fopenmp=libomp -lomp
-lpthread -ldl -ljemalloc -lflang
```

638.imagick\_s: basepeak = yes

644.nab\_s: Same as 619.lbm\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

```
649.fotonik3d_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver2 -funroll-loops -Mrecursive
-mllvm -vector-library=LIBMVEC -Kieee
-fno-finite-math-only -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

```
627.cam4_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
```

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## Peak Optimization Flags (Continued)

627.cam4\_s (continued):

```
-Mrecursive -Kieee -fno-finite-math-only -DSPEC_OPENMP
-fopenmp -DUSE_OPENMP -fopenmp=libomp -lomp -lpthread
-ldl -lmvec -lamdlibm -ljemalloc -lflang
```

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only
-DSPEC_OPENMP -fopenmp -DUSE_OPENMP -fopenmp=libomp -lomp -lpthread
-ldl -lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-return-type
```

Fortran benchmarks:

```
-Wno-return-type
```

Benchmarks using both Fortran and C:

```
-Wno-return-type
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE5.xml>



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