



# SPEC CPU®2017 Integer Speed Result

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## Nettrix

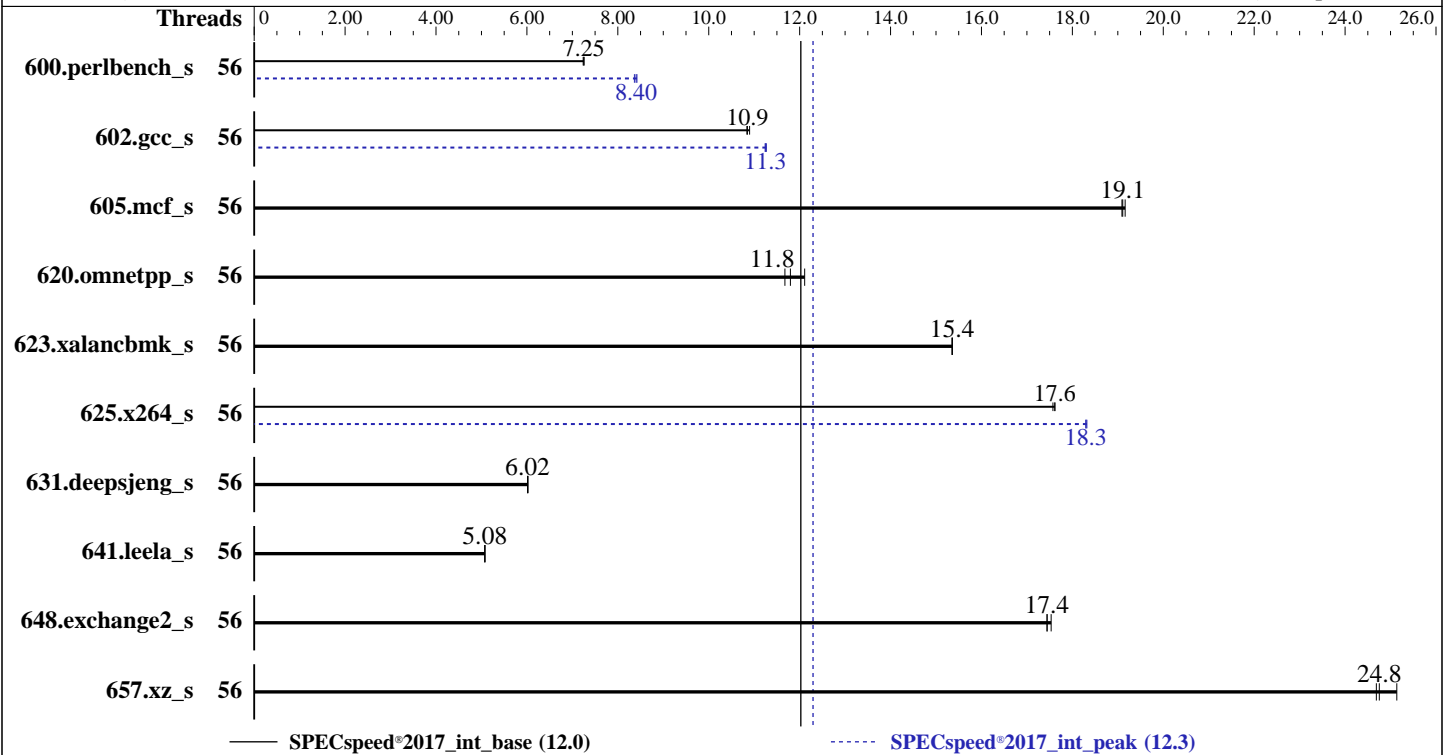
SPECspeed®2017\_int\_base = 12.0

## R620 G30 (Intel Xeon Platinum 8280)

SPECspeed®2017\_int\_peak = 12.3

CPU2017 License: 6138  
Test Sponsor: Nettrix  
Tested by: Nettrix

Test Date: Jun-2020  
Hardware Availability: May-2020  
Software Availability: Apr-2020



### Hardware

CPU Name: Intel Xeon Platinum 8280  
 Max MHz: 4000  
 Nominal: 2700  
 Enabled: 56 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 38.5 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-3200AA-R, running at 2667)  
 Storage: 1x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.0 (Ootpa) 4.18.0-80.el8.x86\_64  
 Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux Build 20200306;  
 Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux Build 20200306;  
 Parallel: Yes  
 Firmware: Nettrix BIOS Version NJGS041227 released May-2020  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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### Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	56	<b>245</b>	<b>7.25</b>	245	7.26	245	7.24	56	211	8.42	<b>211</b>	<b>8.40</b>	212	8.36
602.gcc_s	56	365	10.9	<b>367</b>	<b>10.9</b>	367	10.8	56	353	11.3	354	11.2	<b>354</b>	<b>11.3</b>
605.mcf_s	56	247	19.1	246	19.2	<b>247</b>	<b>19.1</b>	56	247	19.1	246	19.2	<b>247</b>	<b>19.1</b>
620.omnetpp_s	56	140	11.7	135	12.1	<b>138</b>	<b>11.8</b>	56	140	11.7	135	12.1	<b>138</b>	<b>11.8</b>
623.xalancbmk_s	56	92.3	15.4	<b>92.3</b>	<b>15.4</b>	92.3	15.4	56	92.3	15.4	<b>92.3</b>	<b>15.4</b>	92.3	15.4
625.x264_s	56	100	17.6	<b>100</b>	<b>17.6</b>	100	17.6	56	96.4	18.3	96.3	18.3	<b>96.4</b>	<b>18.3</b>
631.deepsjeng_s	56	<b>238</b>	<b>6.02</b>	238	6.02	238	6.01	56	<b>238</b>	<b>6.02</b>	238	6.02	238	6.01
641.leela_s	56	336	5.08	<b>336</b>	<b>5.08</b>	336	5.07	56	336	5.08	<b>336</b>	<b>5.08</b>	336	5.07
648.exchange2_s	56	168	17.5	169	17.4	<b>169</b>	<b>17.4</b>	56	168	17.5	169	17.4	<b>169</b>	<b>17.4</b>
657.xz_s	56	250	24.7	<b>250</b>	<b>24.8</b>	246	25.1	56	250	24.7	<b>250</b>	<b>24.8</b>	246	25.1

SPECSpeed®2017\_int\_base = **12.0**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Tuning Kernel Parameters:  
sched\_migration\_cost\_ns=600000  
sched\_rt\_runtime\_us=950000  
sched\_latency\_ns=24000000  
sched\_min\_granularity\_ns=8000000  
dirty\_background\_ratio=10  
dirty\_ratio=20  
dirty\_writeback\_centisecs=400  
dirty\_expire\_centisecs=5000  
swappiness=10  
numa\_balancing=0

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH =  
"/home/admin/benchmarks/cpu2017/lib/intel64:/home/admin/benchmarks/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

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## Environment Variables Notes (Continued)

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

Application Performance Profile Set to Computing Latency Mode  
Hyper-Threading set to Disabled  
MONITOR/MWAIT set to Enabled  
Autonomous Core C-State set to Enabled  
SNC set to Disabled  
IMC set to Auto  
XPT Prefetch set to Enabled  
KTI Prefetch set to Disabled  
Stale AtoS set to Enabled  
Patrol Scrub set to Disabled  
LLC Dead Line Allocation set to Disabled  
BMC Settings:  
Cooling Policy set to Manual Mode  
Fan Duty set to 95  
  
Sysinfo program /home/admin/benchmarks/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on localhost.localdomain Mon Jun 8 15:11:55 2020  
  
SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8280 CPU @ 2.70GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8280 CPU @ 2.70GHz
Stepping: 6
CPU MHz: 1675.882
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 5400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
```

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### Platform Notes (Continued)

```
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni flush_lld
arch_capabilities
```

```
/proc/cpuinfo cache data
cache size : 39424 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 191859 MB
node 0 free: 187054 MB
node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 193505 MB
node 1 free: 182184 MB
node distances:
node 0 1
0: 10 21
1: 21 10
```

```
From /proc/meminfo
MemTotal: 394613372 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.0 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.0"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.0:ga
```

```
uname -a:
Linux localhost.localdomain 4.18.0-80.el8.x86_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

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## Platform Notes (Continued)

CVE-2018-3620 (L1 Terminal Fault): Not affected  
 Microarchitectural Data Sampling: No status reported  
 CVE-2017-5754 (Meltdown): Not affected  
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp  
 CVE-2017-5753 (Spectre variant 1): Mitigation: \_\_user pointer sanitization  
 CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Jun 7 01:04

SPEC is set to: /home/admin/benchmarks/cpu2017  

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda5	xf	877G	109G	768G	13%	/home

From /sys/devices/virtual/dmi/id  
 BIOS: American Megatrends Inc. NJGS041227 05/16/2020  
 Vendor: Nettrix  
 Product: R620 G30  
 Product Family: Rack  
 Serial: 302000666

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
 24x Samsung M393A2K43DB2-CWE 16 GB 2 rank 3200

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
          | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----
```

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
 NextGen Build 20200304  
 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
=====
C          | 600.perlbench_s(peak)
-----
```

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## Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)  
=====

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
C | 600.perlbench\_s(peak)  
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)  
=====

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
Fortran | 648.exchange2\_s(base, peak)  
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:  
ifort

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries
```





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## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

(\*) Indicates a portability flag that was found in a non-portability variable.

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

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## Peak Optimization Flags (Continued)

605.mcf\_s: basepeak = yes

```
625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html)

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V3.0-CLX-revB.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml)

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V3.0-CLX-revB.xml>

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