



# SPEC CPU®2017 Integer Speed Result

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## New H3C Technologies Co., Ltd.

### H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

SPECspeed®2017\_int\_base = 11.9

SPECspeed®2017\_int\_peak = 12.2

CPU2017 License: 9066

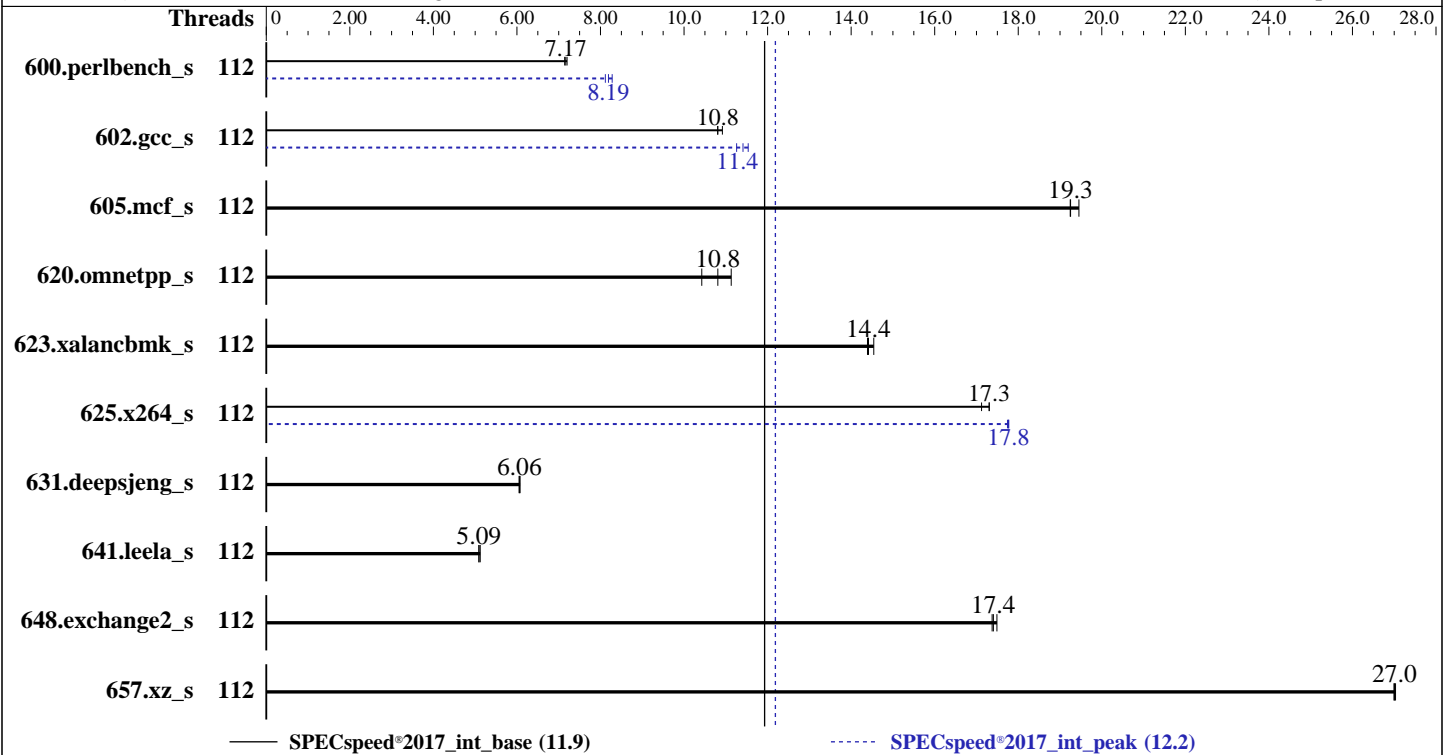
Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2020

Hardware Availability: Sep-2020

Software Availability: Apr-2020



### Hardware

CPU Name: Intel Xeon Platinum 8380HL  
 Max MHz: 4300  
 Nominal: 2900  
 Enabled: 112 cores, 4 chips  
 Orderable: 1,2,3,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 38.5 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-3200V-R, running at 2933)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86\_64  
 Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
 Parallel: Yes  
 Firmware: Version 5.19 released Jul-2020 BIOS  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	112	<b>248</b>	<b>7.17</b>	249	7.14	247	7.20	112	214	8.28	219	8.12	<b>217</b>	<b>8.19</b>
602.gcc_s	112	369	10.8	<b>368</b>	<b>10.8</b>	365	10.9	112	<b>349</b>	<b>11.4</b>	345	11.5	354	11.3
605.mcf_s	112	245	19.2	<b>245</b>	<b>19.3</b>	243	19.5	112	245	19.2	<b>245</b>	<b>19.3</b>	243	19.5
620.omnetpp_s	112	156	10.4	<b>151</b>	<b>10.8</b>	147	11.1	112	156	10.4	<b>151</b>	<b>10.8</b>	147	11.1
623.xalancbmk_s	112	<b>98.3</b>	<b>14.4</b>	98.5	14.4	97.4	14.5	112	<b>98.3</b>	<b>14.4</b>	98.5	14.4	97.4	14.5
625.x264_s	112	102	17.3	103	17.1	<b>102</b>	<b>17.3</b>	112	99.2	17.8	<b>99.3</b>	<b>17.8</b>	99.4	17.7
631.deepsjeng_s	112	237	6.05	<b>236</b>	<b>6.06</b>	236	6.08	112	237	6.05	<b>236</b>	<b>6.06</b>	236	6.08
641.leela_s	112	333	5.12	<b>335</b>	<b>5.09</b>	335	5.09	112	333	5.12	<b>335</b>	<b>5.09</b>	335	5.09
648.exchange2_s	112	<b>169</b>	<b>17.4</b>	168	17.5	169	17.4	112	<b>169</b>	<b>17.4</b>	168	17.5	169	17.4
657.xz_s	112	229	27.0	229	27.0	<b>229</b>	<b>27.0</b>	112	229	27.0	229	27.0	<b>229</b>	<b>27.0</b>

SPECspeed®2017\_int\_base = **11.9**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
MALLOCONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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## General Notes (Continued)

is mitigated in the system as tested and documented.  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  

```
sync; echo 3> /proc/sys/vm/drop_caches
```

 jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Set Hyper-Threading [ALL] to Disabled  
 Set Power Performance Tuning to BIOS Controls EPB  
 Set Energy Performance BIAS to Performance  
 Set XPT Prefetch to Auto  
 Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
 Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
 running on localhost.localdomain Wed Aug 19 21:03:24 2020

SUT (System Under Test) info as seen by some common utilities.  
 For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8380HL CPU @ 2.90GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
```

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## Platform Notes (Continued)

```

Byte Order:           Little Endian
CPU(s):               112
On-line CPU(s) list: 0-111
Thread(s) per core:  1
Core(s) per socket:  28
Socket(s):            4
NUMA node(s):         4
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Platinum 8380HL CPU @ 2.90GHz
Stepping:             11
CPU MHz:              1365.955
CPU max MHz:          4300.0000
CPU min MHz:          1000.0000
BogoMIPS:             5800.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             39424K
NUMA node0 CPU(s):   0-27
NUMA node1 CPU(s):   28-55
NUMA node2 CPU(s):   56-83
NUMA node3 CPU(s):   84-111
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx512_bf16 dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req pku ospke avx512_vnni md_clear flush_lld arch_capabilities

```

```
/proc/cpuinfo cache data
cache size : 39424 KB
```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 191823 MB
node 0 free: 191611 MB

```

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## Platform Notes (Continued)

```

node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 193529 MB
node 1 free: 192786 MB
node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80
81 82 83
node 2 size: 193529 MB
node 2 free: 192988 MB
node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105
106 107 108 109 110 111
node 3 size: 193529 MB
node 3 free: 193156 MB
node distances:
node  0  1  2  3
  0:  10  20  20  20
  1:  20  10  20  20
  2:  20  20  10  20
  3:  20  20  20  10

```

From /proc/meminfo

```

MemTotal:      790950240 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.2 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.2"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

```

uname -a:

```

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

itlb_multihit:          Not affected
CVE-2018-3620 (L1 Terminal Fault):  Not affected
Microarchitectural Data Sampling:   Not affected

```

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## Platform Notes (Continued)

CVE-2017-5754 (Meltdown): Not affected  
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp  
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and \_\_user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling  
tsx\_async\_abort: Not affected

run-level 3 Aug 19 20:56

SPEC is set to: /home/speccpu  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 839G 30G 809G 4% /home

From /sys/devices/virtual/dmi/id  
BIOS: American Megatrends International, LLC. 5.19 07/23/2020  
Product Family: SYSTEM\_FAMILY

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
45x Hynix HMA82GR7DJR8N-XN 16 GB 2 rank 3200  
3x Micron 18ASF2G72PDZ-3G2E1 16 GB 2 rank 3200

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)  
=====

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
C | 600.perlbench\_s(peak)  
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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## Compiler Version Notes (Continued)

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)  
=====

-----  
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(peak)  
=====

-----  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalanbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)  
=====

-----  
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)  
=====

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort



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## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

### C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

### Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries
```

## Peak Compiler Invocation

### C benchmarks:

```
icc
```

### C++ benchmarks:

```
icpc
```

(Continued on next page)





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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

(\*) Indicates a portability flag that was found in a non-portability variable.

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

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## Peak Optimization Flags (Continued)

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevA.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevA.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevA.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevA.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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