SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.

$SPECspeed^{®}2017\_int\_base = 11.3$

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)
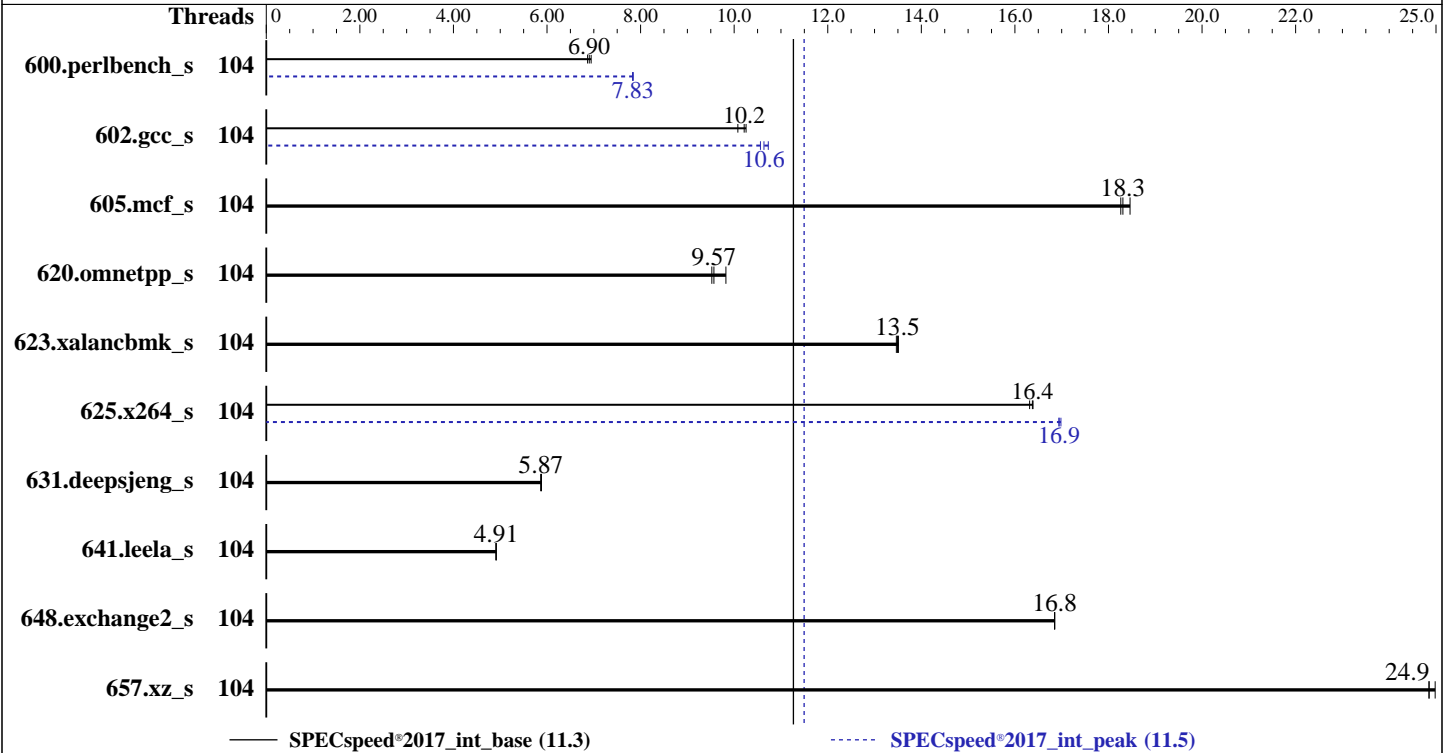
$SPECspeed^{®}2017\_int\_peak = 11.5$

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Threads

Benchmark	Threads	base	peak
600.perlbench_s	104	6.90	7.83
602.gcc_s	104	10.2	10.6
605.mcf_s	104	18.3	
620.omnetpp_s	104	9.57	
623.xalancbmk_s	104	13.5	
625.x264_s	104	16.4	16.9
631.deepsjeng_s	104	5.87	
641.leela_s	104	4.91	
648.exchange2_s	104	16.8	
657.xz_s	104	24.9	

SPECspeed®2017_int_base (11.3)   SPECspeed®2017_int_peak (11.5)

Hardware

CPU Name: Intel Xeon Platinum 8270
 Max MHz: 4000
 Nominal: 2700
Enabled: 104 cores, 4 chips
Orderable: 1,2,3,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 35.75 MB I+D on chip per chip
 Other: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2933V-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa)
 4.18.0-193.el8.x86_64
Compiler: C/C++: Version 19.1.1.217 of Intel C/C++
 Compiler Build 20200306 for Linux;
 Fortran: Version 19.1.1.217 of Intel Fortran
 Compiler Build 20200306 for Linux
Parallel: Yes
Firmware: Version 2.00.33 released Aug-2019 BIOS
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Integer Speed Result

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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3
SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066	**Test Date:** Nov-2020
Test Sponsor: New H3C Technologies Co., Ltd.	**Hardware Availability:** Jun-2019
Tested by: New H3C Technologies Co., Ltd.	**Software Availability:** Apr-2020

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	104	258	6.87	**257**	**6.90**	256	6.94	104	**227**	**7.83**	226	7.84	227	7.83
602.gcc_s	104	395	10.1	388	10.3	**390**	**10.2**	104	371	10.7	377	10.6	**374**	**10.6**
605.mcf_s	104	258	18.3	256	18.5	**258**	**18.3**	104	258	18.3	256	18.5	**258**	**18.3**
620.omnetpp_s	104	**170**	**9.57**	166	9.82	171	9.52	104	**170**	**9.57**	166	9.82	171	9.52
623.xalancbmk_s	104	105	13.5	105	13.5	**105**	**13.5**	104	105	13.5	105	13.5	**105**	**13.5**
625.x264_s	104	108	16.3	108	16.4	**108**	**16.4**	104	104	17.0	**104**	**16.9**	104	16.9
631.deepsjeng_s	104	244	5.87	244	5.88	**244**	**5.87**	104	244	5.87	244	5.88	**244**	**5.87**
641.leela_s	104	347	4.91	347	4.91	**347**	**4.91**	104	347	4.91	347	4.91	**347**	**4.91**
648.exchange2_s	104	174	16.8	**174**	**16.8**	174	16.8	104	174	16.8	**174**	**16.8**	174	16.8
657.xz_s	104	249	24.8	247	25.0	**249**	**24.9**	104	249	24.8	247	25.0	**249**	**24.9**

SPECspeed®2017_int_base =	**11.3**
SPECspeed®2017_int_peak =	**11.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

(Continued on next page)

SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

$SPECspeed®2017\_int\_base = 11.3$

$SPECspeed®2017\_int\_peak = 11.5$

CPU2017 License: 9066		**Test Date:**	Nov-2020
Test Sponsor:	New H3C Technologies Co., Ltd.	**Hardware Availability:**	Jun-2019
Tested by:	New H3C Technologies Co., Ltd.	**Software Availability:**	Apr-2020

General Notes (Continued)

```
is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Settings:
Set Hyper Threading to Disabled
Set Patrol Scrub to Disabled
Set IMC Interleaving to 2-way Interleave

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on localhost.localdomain Fri Nov 13 14:23:34 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8270 CPU @ 2.70GHz
      4  "physical id"s (chips)
      104 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 26
      siblings  : 26
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
      29
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
      29
      physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
      29
      physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
      29

From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     CPU(s):              104
```

(Continued on next page)

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SPEC CPU®2017 Integer Speed Result
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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3

SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Platform Notes (Continued)

```
       On-line CPU(s) list: 0-103
       Thread(s) per core:  1
       Core(s) per socket:  26
       Socket(s):           4
       NUMA node(s):        4
       Vendor ID:           GenuineIntel
       CPU family:          6
       Model:               85
       Model name:          Intel(R) Xeon(R) Platinum 8270 CPU @ 2.70GHz
       Stepping:            5
       CPU MHz:             1000.161
       CPU max MHz:         4000.0000
       CPU min MHz:         1000.0000
       BogoMIPS:            5400.00
       Virtualization:      VT-x
       L1d cache:           32K
       L1i cache:           32K
       L2 cache:            1024K
       L3 cache:            36608K
       NUMA node0 CPU(s):   0-25
       NUMA node1 CPU(s):   26-51
       NUMA node2 CPU(s):   52-77
       NUMA node3 CPU(s):   78-103
       Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
   pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
   aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
   xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
   avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
   invpcid_single intel_ppin ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept
   vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
   avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
   xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
   dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke flush_l1d
   arch_capabilities

 /proc/cpuinfo cache data
    cache size : 36608 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
    node 0 size: 95068 MB
    node 0 free: 94576 MB
    node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50
    51
```

(Continued on next page)

SPEC CPU®2017 Integer Speed Result

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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3

SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Platform Notes (Continued)

```
   node 1 size: 96762 MB
   node 1 free: 96594 MB
   node 2 cpus: 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76
   77
   node 2 size: 96762 MB
   node 2 free: 96270 MB
   node 3 cpus: 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101
   102 103
   node 3 size: 96734 MB
   node 3 free: 95481 MB
   node distances:
   node   0   1   2   3
     0:  10  21  21  21
     1:  21  10  21  21
     2:  21  21  10  21
     3:  21  21  21  10

 From /proc/meminfo
    MemTotal:       394574844 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.2 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.2"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 itlb_multihit:                         KVM: Mitigation: Split huge pages
 CVE-2018-3620 (L1 Terminal Fault):     Not affected
 Microarchitectural Data Sampling:      Vulnerable: Clear CPU buffers attempted, no
                                        microcode; SMT disabled
 CVE-2017-5754 (Meltdown):              Not affected
```

(Continued on next page)

SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

$SPECspeed^{®}2017\_int\_base = 11.3$

$SPECspeed^{®}2017\_int\_peak = 11.5$

CPU2017 License: 9066		**Test Date:** Nov-2020
Test Sponsor: New H3C Technologies Co., Ltd.		**Hardware Availability:** Jun-2019
Tested by: New H3C Technologies Co., Ltd.		**Software Availability:** Apr-2020

Platform Notes (Continued)

```
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                  via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):       Mitigation: usercopy/swapgs barriers and __user
                                  pointer sanitization
 CVE-2017-5715 (Spectre variant 2):       Mitigation: Full generic retpoline, IBPB:
                                  conditional, IBRS_FW, RSB filling
 tsx_async_abort:                         Vulnerable: Clear CPU buffers attempted, no
                                  microcode; SMT disabled


 run-level 3 Nov 13 14:22

 SPEC is set to: /home/speccpu
    Filesystem              Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   839G  143G  697G  17% /home

 From /sys/devices/virtual/dmi/id
    BIOS:    American Megatrends Inc. 2.00.33 08/22/2019
    Vendor:  New H3C Technologies Co., Ltd.
    Product: H3C UniServer R6900 G3
    Product Family: Rack
    Serial:  210235A3T0H204000004

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
    24x Micron 18ASF2G72PDZ-2G9E1 16 GB 2 rank 2933
    24x NO DIMM NO DIMM

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C        | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
         | 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 600.perlbench_s(peak)
------------------------------------------------------------------------------
```

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New H3C Technologies Co., Ltd.

$\text{SPECspeed}^{\circledR}2017\_\text{int\_base} = 11.3$

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

$\text{SPECspeed}^{\circledR}2017\_\text{int\_peak} = 11.5$

CPU2017 License: 9066		**Test Date:** Nov-2020
Test Sponsor: New H3C Technologies Co., Ltd.		**Hardware Availability:** Jun-2019
Tested by: New H3C Technologies Co., Ltd.		**Software Availability:** Apr-2020

Compiler Version Notes (Continued)

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------


==============================================================================
C        | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
         | 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
C        | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------


==============================================================================
C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
         | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

(Continued on next page)

SPEC CPU®2017 Integer Speed Result

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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3

SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:
```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries
```

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SPEC CPU®2017 Integer Speed Result

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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3

SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Peak Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Peak Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`(*) `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: `-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2)
`-xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4 -fno-strict-overflow`
`-mbranches-within-32B-boundaries`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

602.gcc_s: `-m64 -qnextgen -std=c11 -fuse-ld=gold`
`-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
`-Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX512 -flto`
`-Ofast`(pass 1) `-O3 -ffast-math -qopt-mem-layout-trans=4`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

(Continued on next page)

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https://www.spec.org/

New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Platinum 8270)

SPECspeed®2017_int_base = 11.3

SPECspeed®2017_int_peak = 11.5

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

Peak Optimization Flags (Continued)

605.mcf_s: `basepeak = yes`

625.x264_s: `-m64 -qnextgen -std=c11`
`-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
`-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math`
`-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

657.xz_s: `basepeak = yes`

C++ benchmarks:

620.omnetpp_s: `basepeak = yes`

623.xalancbmk_s: `basepeak = yes`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html`
`http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml`
`http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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