



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 343

ThinkSystem SR850P  
(2.70 GHz, Intel Xeon Gold 6226)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

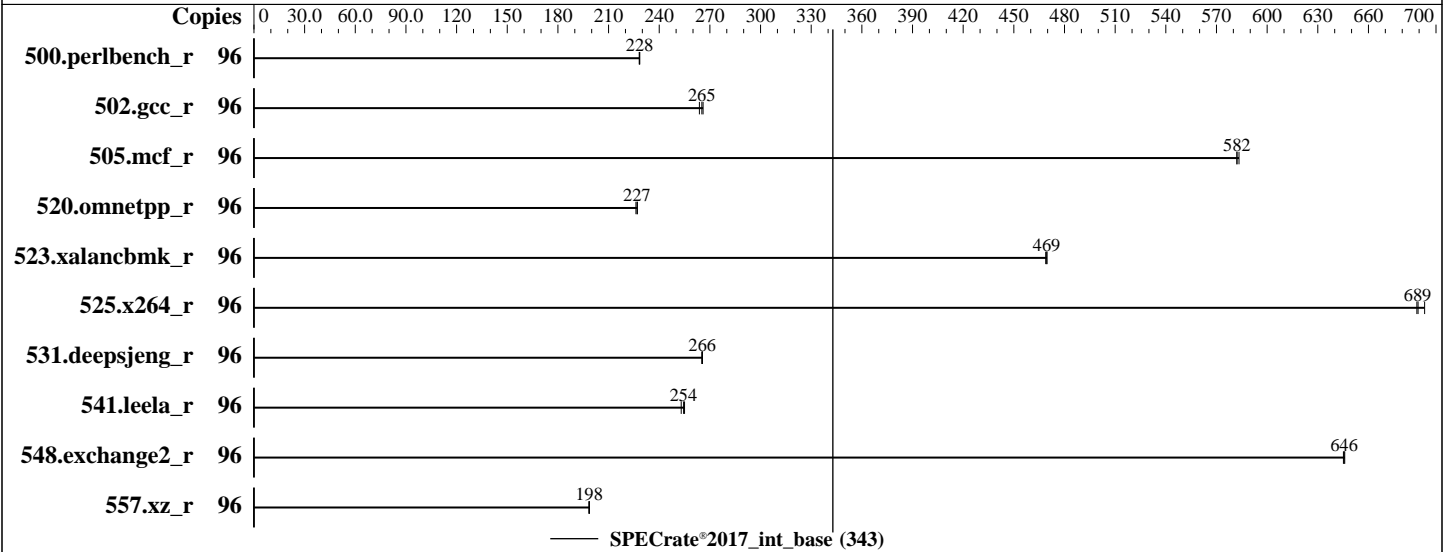
Test Date: Nov-2020

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jun-2020

Tested by: Lenovo Global Technology

Software Availability: Aug-2020



### Hardware

CPU Name: Intel Xeon Gold 6226  
 Max MHz: 3700  
 Nominal: 2700  
 Enabled: 48 cores, 4 chips, 2 threads/core  
 Orderable: 4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 19.25 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.0 (Ootpa)  
 Kernel 4.18.0-80.el8.x86\_64  
 Compiler: C/C++: Version 19.1.2.275 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.1.2.275 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Lenovo BIOS Version TEE156L 2.61 released May-2020  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
500.perlbench_r	96	669	228	670	228	<b>669</b>	<b>228</b>									
502.gcc_r	96	515	264	511	266	<b>512</b>	<b>265</b>									
505.mcf_r	96	<b>266</b>	<b>582</b>	267	582	266	583									
520.omnetpp_r	96	<b>555</b>	<b>227</b>	555	227	557	226									
523.xalancbmk_r	96	216	469	216	470	<b>216</b>	<b>469</b>									
525.x264_r	96	244	689	<b>244</b>	<b>689</b>	242	693									
531.deepsjeng_r	96	414	266	415	265	<b>414</b>	<b>266</b>									
541.leela_r	96	628	253	<b>625</b>	<b>254</b>	623	255									
548.exchange2_r	96	<b>390</b>	<b>646</b>	389	646	390	645									
557.xz_r	96	523	198	522	199	<b>522</b>	<b>198</b>									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/home/cpu2017-1.1.0-ic19.1u2/lib/intel64:/home/cpu2017-1.1.0-ic19.1u2/lib/ia32:/home/cpu2017-1.1.0-ic19.1u2/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

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### General Notes (Continued)

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
MONITOR/MWAIT set to Enable  
CPU P-state Control set to Legacy  
Adjacent Cache Prefetch set to Disable  
DCU Streamer Prefetcher set to Disable  
SNC set to Enable

sysinfo program /home/cpu2017-1.1.0-ic19.lu2/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on localhost.localdomain Fri Nov 27 01:40:15 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
 4 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 6 8 9 11 12 13
physical 2: cores 0 1 2 3 4 5 8 10 11 12 13 14
physical 3: cores 0 2 3 4 5 6 8 9 10 11 13 14
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
```

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### Platform Notes (Continued)

```

Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
Stepping: 7
CPU MHz: 2045.882
CPU max MHz: 3700.0000
CPU min MHz: 1200.0000
BogoMIPS: 5400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-2,6-8,48-50,54-56
NUMA node1 CPU(s): 3-5,9-11,51-53,57-59
NUMA node2 CPU(s): 12-15,19,20,60-63,67,68
NUMA node3 CPU(s): 16-18,21-23,64-66,69-71
NUMA node4 CPU(s): 24-27,30,31,72-75,78,79
NUMA node5 CPU(s): 28,29,32-35,76,77,80-83
NUMA node6 CPU(s): 36-38,42-44,84-86,90-92
NUMA node7 CPU(s): 39-41,45-47,87-89,93-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d
arch_capabilities

```

```
/proc/cpuinfo cache data
cache size : 19712 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 6 7 8 48 49 50 54 55 56
node 0 size: 193150 MB
```

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### Platform Notes (Continued)

```

node 0 free: 192851 MB
node 1 cpus: 3 4 5 9 10 11 51 52 53 57 58 59
node 1 size: 193533 MB
node 1 free: 192494 MB
node 2 cpus: 12 13 14 15 19 20 60 61 62 63 67 68
node 2 size: 193533 MB
node 2 free: 193373 MB
node 3 cpus: 16 17 18 21 22 23 64 65 66 69 70 71
node 3 size: 193533 MB
node 3 free: 193352 MB
node 4 cpus: 24 25 26 27 30 31 72 73 74 75 78 79
node 4 size: 193508 MB
node 4 free: 193334 MB
node 5 cpus: 28 29 32 33 34 35 76 77 80 81 82 83
node 5 size: 193533 MB
node 5 free: 193330 MB
node 6 cpus: 36 37 38 42 43 44 84 85 86 90 91 92
node 6 size: 193533 MB
node 6 free: 193344 MB
node 7 cpus: 39 40 41 45 46 47 87 88 89 93 94 95
node 7 size: 193532 MB
node 7 free: 193354 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 11 21 21 21 21 21 21
  1: 11 10 21 21 21 21 21 21
  2: 21 21 10 11 21 21 21 21
  3: 21 21 11 10 21 21 21 21
  4: 21 21 21 21 10 11 21 21
  5: 21 21 21 21 11 10 21 21
  6: 21 21 21 21 21 21 10 11
  7: 21 21 21 21 21 21 11 10

```

From /proc/meminfo

```

MemTotal:      1585007748 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.0 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.0"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"

```

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### Platform Notes (Continued)

ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.0:ga

uname -a:

Linux localhost.localdomain 4.18.0-80.el8.x86\_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	No status reported
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Nov 27 01:37

SPEC is set to: /home/cpu2017-1.1.0-ic19.lu2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	839G	65G	774G	8%	/home

From /sys/devices/virtual/dmi/id

BIOS: Lenovo -[TEE156L-2.61]- 05/20/2020  
 Vendor: Lenovo  
 Product: ThinkSystem SR850P -[7D2HCT01WW]-  
 Product Family: ThinkSystem  
 Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

48x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
```

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## Compiler Version Notes (Continued)

| 525.x264\_r(base) 557.xz\_r(base)

-----  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base)  
-----

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries  
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html)

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml)

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.xml>





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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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