



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_fp\_base = 337

ThinkSystem SR850 V2  
(3.90 GHz, Intel Xeon Platinum 8356H)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9017

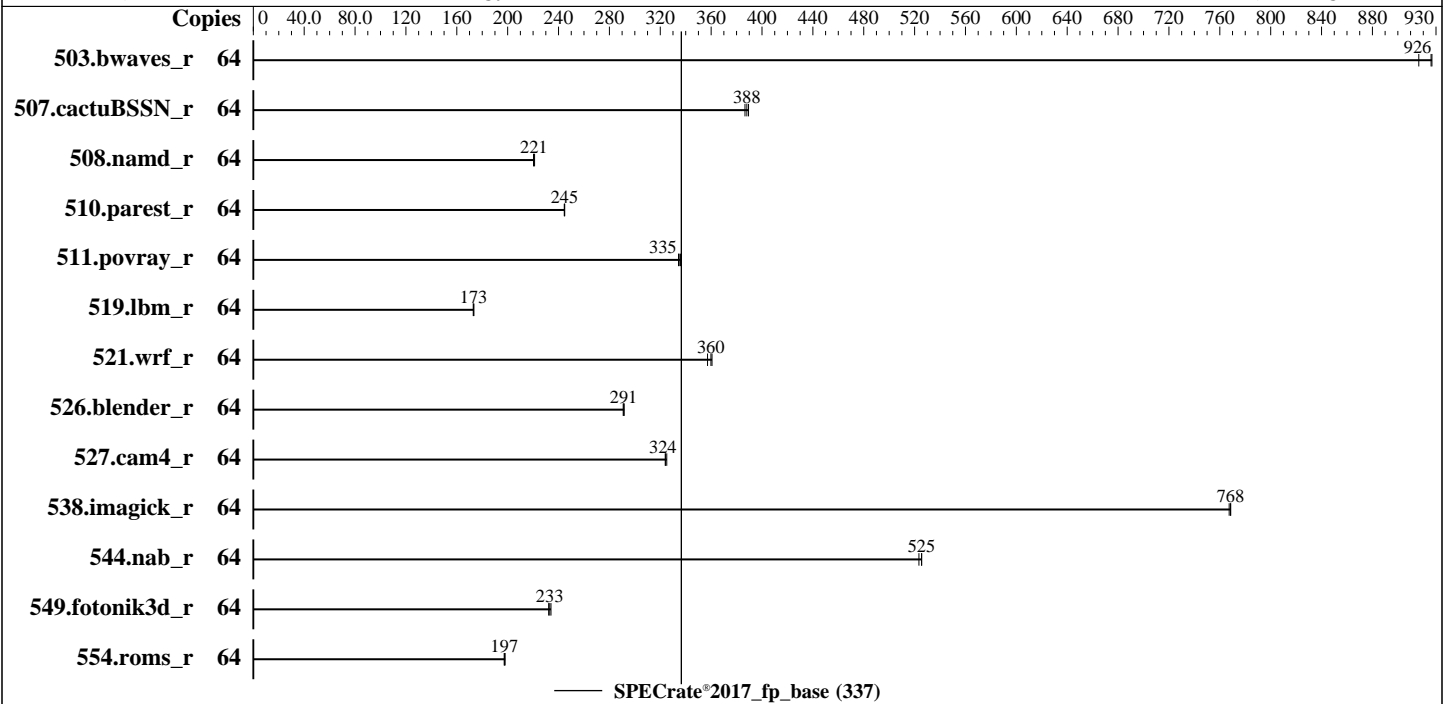
Test Date: Dec-2020

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2020

Tested by: Lenovo Global Technology

Software Availability: Aug-2020



### Hardware

CPU Name: Intel Xeon Platinum 8356H  
 Max MHz: 4400  
 Nominal: 3900  
 Enabled: 32 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.2 (Ootpa)  
 Kernel 4.18.0-193.el8.x86\_64  
 Compiler: C/C++: Version 19.1.2.275 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.1.2.275 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Lenovo BIOS Version MSE107I 1.01 released Nov-2020  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

| Benchmark       | Base   |             |            |            |            |            |            | Peak   |         |       |         |       |         |       |
|-----------------|--------|-------------|------------|------------|------------|------------|------------|--------|---------|-------|---------|-------|---------|-------|
|                 | Copies | Seconds     | Ratio      | Seconds    | Ratio      | Seconds    | Ratio      | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r    | 64     | 700         | 916        | <b>693</b> | <b>926</b> | 692        | 927        |        |         |       |         |       |         |       |
| 507.cactuBSSN_r | 64     | <b>209</b>  | <b>388</b> | 210        | 387        | 208        | 389        |        |         |       |         |       |         |       |
| 508.namd_r      | 64     | 276         | 220        | 275        | 221        | <b>276</b> | <b>221</b> |        |         |       |         |       |         |       |
| 510.parest_r    | 64     | <b>684</b>  | <b>245</b> | 684        | 245        | 685        | 245        |        |         |       |         |       |         |       |
| 511.povray_r    | 64     | <b>446</b>  | <b>335</b> | 445        | 336        | 447        | 334        |        |         |       |         |       |         |       |
| 519.lbm_r       | 64     | 390         | 173        | 389        | 173        | <b>389</b> | <b>173</b> |        |         |       |         |       |         |       |
| 521.wrf_r       | 64     | 401         | 357        | 397        | 361        | <b>398</b> | <b>360</b> |        |         |       |         |       |         |       |
| 526.blender_r   | 64     | <b>335</b>  | <b>291</b> | 334        | 292        | 335        | 291        |        |         |       |         |       |         |       |
| 527.cam4_r      | 64     | <b>345</b>  | <b>324</b> | 344        | 325        | 346        | 324        |        |         |       |         |       |         |       |
| 538.imagick_r   | 64     | <b>207</b>  | <b>768</b> | 207        | 767        | 207        | 769        |        |         |       |         |       |         |       |
| 544.nab_r       | 64     | <b>205</b>  | <b>525</b> | 205        | 526        | 206        | 523        |        |         |       |         |       |         |       |
| 549.fotonik3d_r | 64     | <b>1070</b> | <b>233</b> | 1074       | 232        | 1066       | 234        |        |         |       |         |       |         |       |
| 554.roms_r      | 64     | 515         | 197        | 514        | 198        | <b>515</b> | <b>197</b> |        |         |       |         |       |         |       |

SPECrate®2017\_fp\_base = 337

SPECrate®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/home/cpu2017-1.1.0-ic19.lu2/lib/intel64:/home/cpu2017-1.1.0-ic19.lu2/j
  e5.0.1-64"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default

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## General Notes (Continued)

Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
C-States set to Legacy  
SNC set to Enabled  
Stale Atos set to Enable

Sysinfo program /home/cpu2017-1.1.0-ic19.lu2/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on localhost.localdomain Fri Dec 25 14:38:37 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Platinum 8356H CPU @ 3.90GHz  
4 "physical id"s (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 1 2 3 6 13 18 28 29  
physical 1: cores 2 6 8 10 12 17 19 28  
physical 2: cores 3 6 10 13 17 18 28 29  
physical 3: cores 1 2 3 5 6 10 20 29

From lscpu:  
Architecture: x86\_64

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### Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              64
On-line CPU(s) list: 0-63
Thread(s) per core:  2
Core(s) per socket:  8
Socket(s):           4
NUMA node(s):        8
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Platinum 8356H CPU @ 3.90GHz
Stepping:             11
CPU MHz:              3281.905
CPU max MHz:          4400.0000
CPU min MHz:          1200.0000
BogoMIPS:             7800.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             36608K
NUMA node0 CPU(s):   0-2,5,32-34,37
NUMA node1 CPU(s):   3,4,6,7,35,36,38,39
NUMA node2 CPU(s):   8,10,11,13,40,42,43,45
NUMA node3 CPU(s):   9,12,14,15,41,44,46,47
NUMA node4 CPU(s):   16,18,20,21,48,50,52,53
NUMA node5 CPU(s):   17,19,22,23,49,51,54,55
NUMA node6 CPU(s):   24-26,29,56-58,61
NUMA node7 CPU(s):   27,28,30,31,59,60,62,63
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx512_bf16 dtherm ida arat pln pts pku ospke avx512_vnni md_clear
flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 36608 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a

(Continued on next page)



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### Platform Notes (Continued)

```

physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 5 32 33 34 37
node 0 size: 193123 MB
node 0 free: 192776 MB
node 1 cpus: 3 4 6 7 35 36 38 39
node 1 size: 193533 MB
node 1 free: 193387 MB
node 2 cpus: 8 10 11 13 40 42 43 45
node 2 size: 193533 MB
node 2 free: 193404 MB
node 3 cpus: 9 12 14 15 41 44 46 47
node 3 size: 193533 MB
node 3 free: 193402 MB
node 4 cpus: 16 18 20 21 48 50 52 53
node 4 size: 193533 MB
node 4 free: 193405 MB
node 5 cpus: 17 19 22 23 49 51 54 55
node 5 size: 193533 MB
node 5 free: 193401 MB
node 6 cpus: 24 25 26 29 56 57 58 61
node 6 size: 193533 MB
node 6 free: 193383 MB
node 7 cpus: 27 28 30 31 59 60 62 63
node 7 size: 193505 MB
node 7 free: 193303 MB
node distances:
node  0  1  2  3  4  5  6  7
  0:  10  11  20  20  20  20  20  20
  1:  11  10  20  20  20  20  20  20
  2:  20  20  10  11  20  20  20  20
  3:  20  20  11  10  20  20  20  20
  4:  20  20  20  20  10  11  20  20
  5:  20  20  20  20  11  10  20  20
  6:  20  20  20  20  20  20  10  11
  7:  20  20  20  20  20  20  11  10

```

```

From /proc/meminfo
MemTotal:      1584980336 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"

```

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### Platform Notes (Continued)

```
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
```

```
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
```

uname -a:

```
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
itlb_multihit: Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional,
RSB filling
tsx_async_abort: Not affected
```

run-level 3 Dec 25 14:24

SPEC is set to: /home/cpu2017-1.1.0-ic19.1u2

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 838G 35G 803G 5% /home
```

From /sys/devices/virtual/dmi/id

```
BIOS: Lenovo M5E107I-1.01 11/02/2020
Vendor: Lenovo
Product: ThinkSystem SR850 V2
Product Family: ThinkSystem
Serial: 1234567890
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

48x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

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### Platform Notes (Continued)

(End of data from sysinfo program)  
Memory on this system run at 2933 MHz due to CPU limitation.

### Compiler Version Notes

=====  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
-----

Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
-----

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
-----

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
-----

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
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## Compiler Version Notes (Continued)

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
-----

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
-----

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using both C and C++:

icpc icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64

507.cactuBSSN\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

```

508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

### C++ benchmarks:

```

-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

### Fortran benchmarks:

```

-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

### Benchmarks using both Fortran and C:

```

-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

```

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## Base Optimization Flags (Continued)

Benchmarks using both C and C++:

```
-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.html)

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Cooperlake-A.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.xml)

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Cooperlake-A.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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