



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380 Gen10

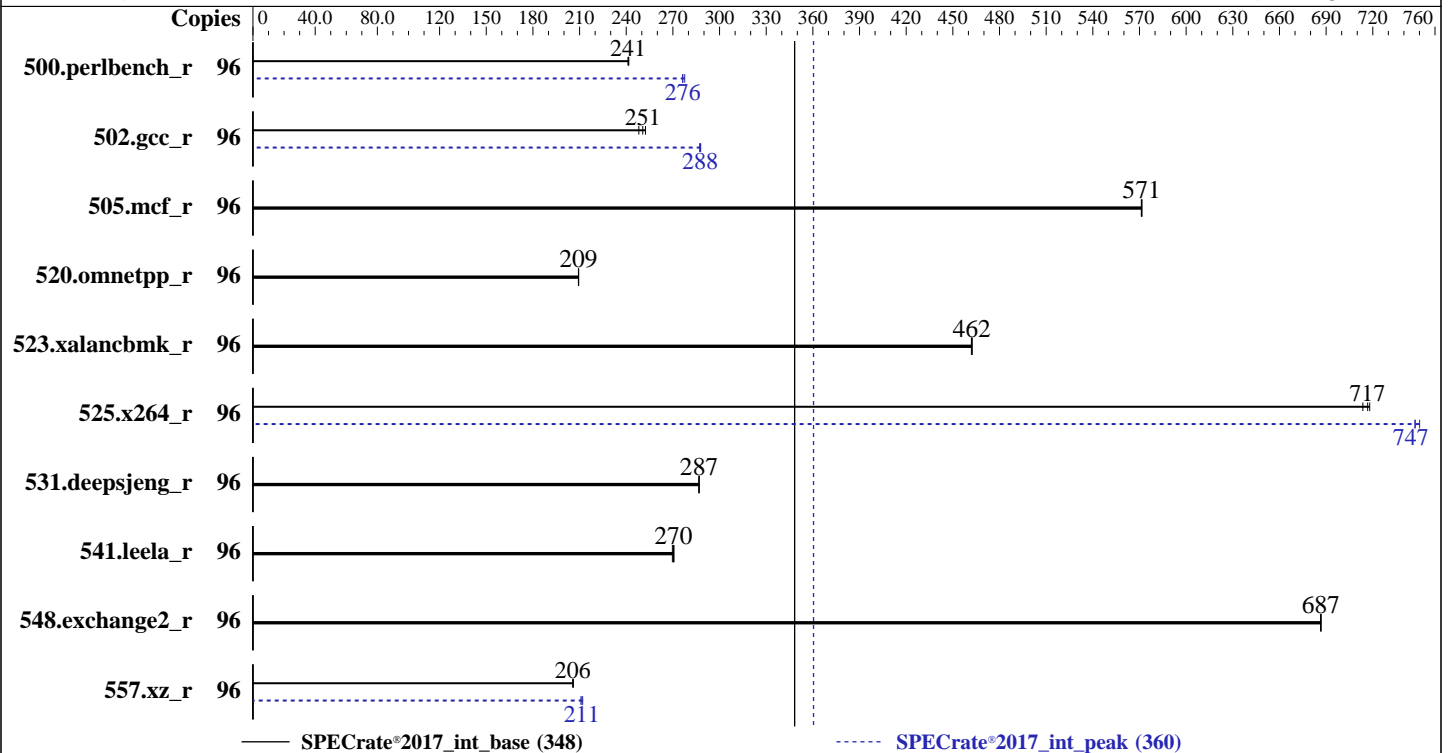
(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jan-2021  
Hardware Availability: Feb-2020  
Software Availability: Aug-2020



### Hardware

CPU Name: Intel Xeon Gold 6248R  
 Max MHz: 4000  
 Nominal: 3000  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)  
 Storage: 1 x 600 GB SAS SSD, RAID 0  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86\_64  
 Compiler: C/C++: Version 19.1.2.275 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.1.2.275 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: HPE BIOS Version U30 U30 v2.40(10/26/2020) released Oct-2020  
 File System: xfs  
 System State: Run level 3 (multi user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jan-2021  
Hardware Availability: Feb-2020  
Software Availability: Aug-2020

## Results Table

| Benchmark       | Base   |            |            |            |            |         |       | Peak   |            |            |            |            |            |            |
|-----------------|--------|------------|------------|------------|------------|---------|-------|--------|------------|------------|------------|------------|------------|------------|
|                 | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds | Ratio | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds    | Ratio      |
| 500.perlbench_r | 96     | <b>634</b> | <b>241</b> | 634        | 241        | 632     | 242   | 96     | 551        | 278        | <b>553</b> | <b>276</b> | 553        | 276        |
| 502.gcc_r       | 96     | <b>542</b> | <b>251</b> | 548        | 248        | 539     | 252   | 96     | <b>473</b> | <b>288</b> | 473        | 287        | 472        | 288        |
| 505.mcf_r       | 96     | <b>271</b> | <b>571</b> | 271        | 572        | 272     | 571   | 96     | <b>271</b> | <b>571</b> | 271        | 572        | 272        | 571        |
| 520.omnetpp_r   | 96     | 602        | 209        | <b>602</b> | <b>209</b> | 602     | 209   | 96     | 602        | 209        | <b>602</b> | <b>209</b> | 602        | 209        |
| 523.xalancbmk_r | 96     | <b>219</b> | <b>462</b> | 219        | 462        | 219     | 462   | 96     | <b>219</b> | <b>462</b> | 219        | 462        | 219        | 462        |
| 525.x264_r      | 96     | <b>235</b> | <b>717</b> | 236        | 714        | 234     | 718   | 96     | 225        | 747        | 224        | 750        | <b>225</b> | <b>747</b> |
| 531.deepsjeng_r | 96     | 383        | 287        | <b>384</b> | <b>287</b> | 384     | 287   | 96     | 383        | 287        | <b>384</b> | <b>287</b> | 384        | 287        |
| 541.leela_r     | 96     | 587        | 271        | <b>588</b> | <b>270</b> | 590     | 270   | 96     | 587        | 271        | <b>588</b> | <b>270</b> | 590        | 270        |
| 548.exchange2_r | 96     | 366        | 687        | <b>366</b> | <b>687</b> | 366     | 686   | 96     | 366        | 687        | <b>366</b> | <b>687</b> | 366        | 686        |
| 557.xz_r        | 96     | 504        | 206        | <b>504</b> | <b>206</b> | 503     | 206   | 96     | 489        | 212        | <b>490</b> | <b>211</b> | 492        | 211        |

SPECrate®2017\_int\_base = **348**

SPECrate®2017\_int\_peak = **360**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/home/cpu2017\_1.2\_binaries/lib/intel64:/home/cpu2017\_1.2\_binaries/lib/i  
a32:/home/cpu2017\_1.2\_binaries/je5.0.1-32"  
MALLOC\_CONF = "retain:true"



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2021  
**Hardware Availability:** Feb-2020  
**Software Availability:** Aug-2020

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

LLC Prefetch set to Enabled

LLC Dead Line Allocation set to Disabled

Enhanced Processor Performance set to Enabled

Workload Profile set to General Throughput Compute

Workload Profile set to Custom

DCU Stream Prefetcher set to Disabled

Sysinfo program /home/cpu2017\_1.2\_binaries/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011

running on localhost.localdomain Mon Jan 11 05:31:23 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz

2 "physical id"s (chips)

96 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 24

siblings : 48

physical 0: cores 0 1 2 3 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 1: cores 0 1 2 3 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2021  
**Hardware Availability:** Feb-2020  
**Software Availability:** Aug-2020

## Platform Notes (Continued)

```

Byte Order:           Little Endian
CPU(s):               96
On-line CPU(s) list: 0-95
Thread(s) per core:  2
Core(s) per socket:  24
Socket(s):            2
NUMA node(s):        4
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
Stepping:             7
CPU MHz:              2502.397
BogoMIPS:             6000.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             36608K
NUMA node0 CPU(s):   0-11,48-59
NUMA node1 CPU(s):   12-23,60-71
NUMA node2 CPU(s):   24-35,72-83
NUMA node3 CPU(s):   36-47,84-95
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 36608 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 96328 MB
node 0 free: 96059 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 1 size: 96762 MB

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2021  
**Hardware Availability:** Feb-2020  
**Software Availability:** Aug-2020

## Platform Notes (Continued)

```

node 1 free: 96227 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
node 2 size: 96762 MB
node 2 free: 96579 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 96762 MB
node 3 free: 96590 MB
node distances:
node  0  1  2  3
  0:  10  21  31  31
  1:  21  10  31  31
  2:  31  31  10  21
  3:  31  31  21  10

```

```

From /proc/meminfo
MemTotal:          395895272 kB
HugePages_Total:    0
Hugepagesize:      2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

```

```

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

itlb_multihit:                                KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault):            Not affected
Microarchitectural Data Sampling:            Not affected
CVE-2017-5754 (Meltdown):                    Not affected
CVE-2018-3639 (Speculative Store Bypass):    Mitigation: Speculative Store Bypass disabled
                                                via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):           Mitigation: usercopy/swapgs barriers and __user
                                                pointer sanitization

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

**SPECrate®2017\_int\_base = 348**

**SPECrate®2017\_int\_peak = 360**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2021  
**Hardware Availability:** Feb-2020  
**Software Availability:** Aug-2020

## Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling  
tsx\_async\_abort: Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Jan 11 05:29

SPEC is set to: /home/cpu2017\_1.2\_binaries

| Filesystem            | Type | Size | Used | Avail | Use% | Mounted on |
|-----------------------|------|------|------|-------|------|------------|
| /dev/mapper/rhel-home | xfs  | 504G | 117G | 387G  | 24%  | /home      |

From /sys/devices/virtual/dmi/id  
BIOS: HPE U30 10/26/2020  
Vendor: HPE  
Product: ProLiant DL380 Gen10  
Product Family: ProLiant  
Serial: 7CE714P19P

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2933

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 502.gcc\_r(peak)

-----  
Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275  
Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base)

-----  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jan-2021  
**Hardware Availability:** Feb-2020  
**Software Availability:** Aug-2020

## Compiler Version Notes (Continued)

C | 500.perlbench\_r(peak) 557.xz\_r(peak)

-----  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 502.gcc\_r(peak)

-----  
Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275  
Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base)

-----  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 500.perlbench\_r(peak) 557.xz\_r(peak)

-----  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 502.gcc\_r(peak)

-----  
Intel(R) C Compiler for applications running on IA-32, Version 19.1.2.275  
Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base)

-----  
Intel(R) C Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2021

Hardware Availability: Feb-2020

Software Availability: Aug-2020

## Compiler Version Notes (Continued)

=====  
C | 500.perlbench\_r(peak) 557.xz\_r(peak)  
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak)  
| 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version  
19.1.2.275 Build 20200604  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.275 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2021

**Hardware Availability:** Feb-2020

**Software Availability:** Aug-2020

## Base Portability Flags (Continued)

```
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmallo
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmallo
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmallo
```

## Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2021

**Hardware Availability:** Feb-2020

**Software Availability:** Aug-2020

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/ia32_lin
-std=gnu89
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(3.0 GHz, Intel Xeon Gold 6248R)

SPECrate®2017\_int\_base = 348

SPECrate®2017\_int\_peak = 360

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jan-2021

**Hardware Availability:** Feb-2020

**Software Availability:** Aug-2020

## Peak Optimization Flags (Continued)

525.x264\_r (continued):

```
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

557.xz\_r: -Wl, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div

```
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.3-CLX-revC.html>

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.3-CLX-revC.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.xml)

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2021-01-10 19:01:22-0500.

Report generated on 2021-02-02 19:47:01 by CPU2017 PDF formatter v6255.

Originally published on 2021-02-02.