



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_base = 11.1

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

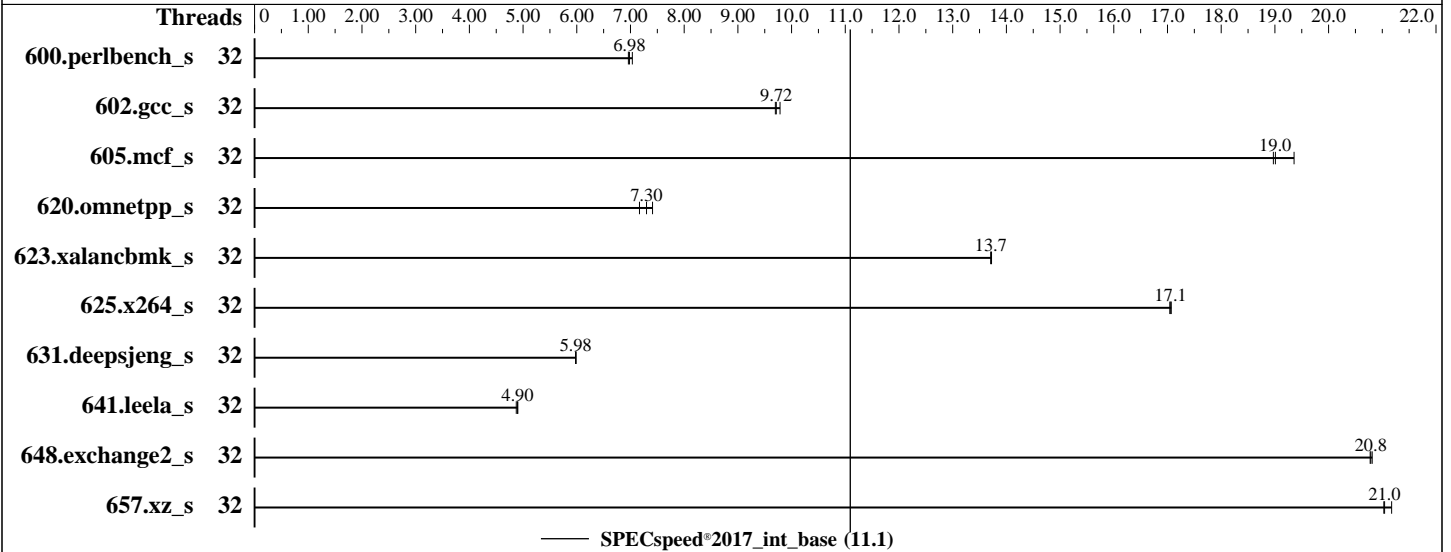
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jul-2021

Hardware Availability: Jul-2021

Software Availability: Dec-2020



### Hardware

CPU Name: Intel Xeon Gold 5315Y  
 Max MHz: 3600  
 Nominal: 3200  
 Enabled: 16 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 12 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)  
 Kernel 4.18.0-240.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
 Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version AFE111A 1.02 released May-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECSpeed®2017\_int\_base = 11.1

SPECSpeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jul-2021

Hardware Availability: Jul-2021

Software Availability: Dec-2020

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	255	6.96	<b>254</b>	<b>6.98</b>	252	7.03							
602.gcc_s	32	<b>410</b>	<b>9.72</b>	411	9.70	407	9.79							
605.mcf_s	32	244	19.4	<b>248</b>	<b>19.0</b>	249	19.0							
620.omnetpp_s	32	220	7.41	228	7.17	<b>223</b>	<b>7.30</b>							
623.xalancbmk_s	32	<b>103</b>	<b>13.7</b>	103	13.7	103	13.7							
625.x264_s	32	103	17.1	<b>103</b>	<b>17.1</b>	104	17.0							
631.deepsjeng_s	32	240	5.98	<b>240</b>	<b>5.98</b>	239	5.99							
641.leela_s	32	350	4.87	<b>348</b>	<b>4.90</b>	348	4.90							
648.exchange2_s	32	142	20.8	141	20.8	<b>141</b>	<b>20.8</b>							
657.xz_s	32	<b>294</b>	<b>21.0</b>	292	21.2	294	21.0							

SPECSpeed®2017\_int\_base = 11.1

SPECSpeed®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
  "/home/cpu2017-1.1.8-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.8-ic202
  1.1-revB/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
 sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 11.1

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jul-2021

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2021

Tested by: Lenovo Global Technology

Software Availability: Dec-2020

### General Notes (Continued)

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
C-States set to Legacy

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Mon Jul 5 02:11:46 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz  
2 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following  
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 0 1 2 3 4 5 6 7  
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 32  
On-line CPU(s) list: 0-31  
Thread(s) per core: 2  
Core(s) per socket: 8  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 106  
Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz  
Stepping: 6  
CPU MHz: 801.204  
BogoMIPS: 6400.00

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 11.1

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jul-2021

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2021

Tested by: Lenovo Global Technology

Software Availability: Dec-2020

### Platform Notes (Continued)

```

Virtualization:      VT-x
L1d cache:          48K
L1i cache:          32K
L2 cache:           1280K
L3 cache:           12288K
NUMA node0 CPU(s):  0-7,16-23
NUMA node1 CPU(s):  8-15,24-31
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 12288 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 504580 MB
node 0 free: 514944 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 505029 MB
node 1 free: 515479 MB
node distances:
node  0  1
 0:  10  20
 1:  20  10

```

```

From /proc/meminfo
MemTotal:      1056495860 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/sbin/tuned-adm active
Current active profile: throughput-performance

```

```

From /etc/*release* /etc/*version*

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_base = 11.1

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jul-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

### Platform Notes (Continued)

```
os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.3 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.3"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
```

```
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 3 Jul 5 02:10

```
SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb4       xfs   819G  268G  551G  33% /home
```

```
From /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem SR650 V2 MB
Product Family: ThinkSystem
Serial:          1234567890
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 11.1

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jul-2021

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2021

Tested by: Lenovo Global Technology

Software Availability: Dec-2020

### Platform Notes (Continued)

interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:

BIOS Vendor: Lenovo  
BIOS Version: AFE111A-1.02  
BIOS Date: 05/07/2021  
BIOS Revision: 1.2  
Firmware Revision: 1.10

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base) 605.mcf\_s(base)  
| 625.x264\_s(base) 657.xz\_s(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
C++ | 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base)  
| 641.leela\_s(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
Fortran | 648.exchange2\_s(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017\_int\_base = 11.1

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jul-2021

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2021

Tested by: Lenovo Global Technology

Software Availability: Dec-2020

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX2
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmallo
```

Fortran benchmarks:

```
-m64 -xCORE-AVX2 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
```



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V2  
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_base = 11.1

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jul-2021

**Hardware Availability:** Jul-2021

**Software Availability:** Dec-2020

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2021-07-04 14:11:45-0400.

Report generated on 2021-07-21 15:51:11 by CPU2017 PDF formatter v6442.

Originally published on 2021-07-20.