



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

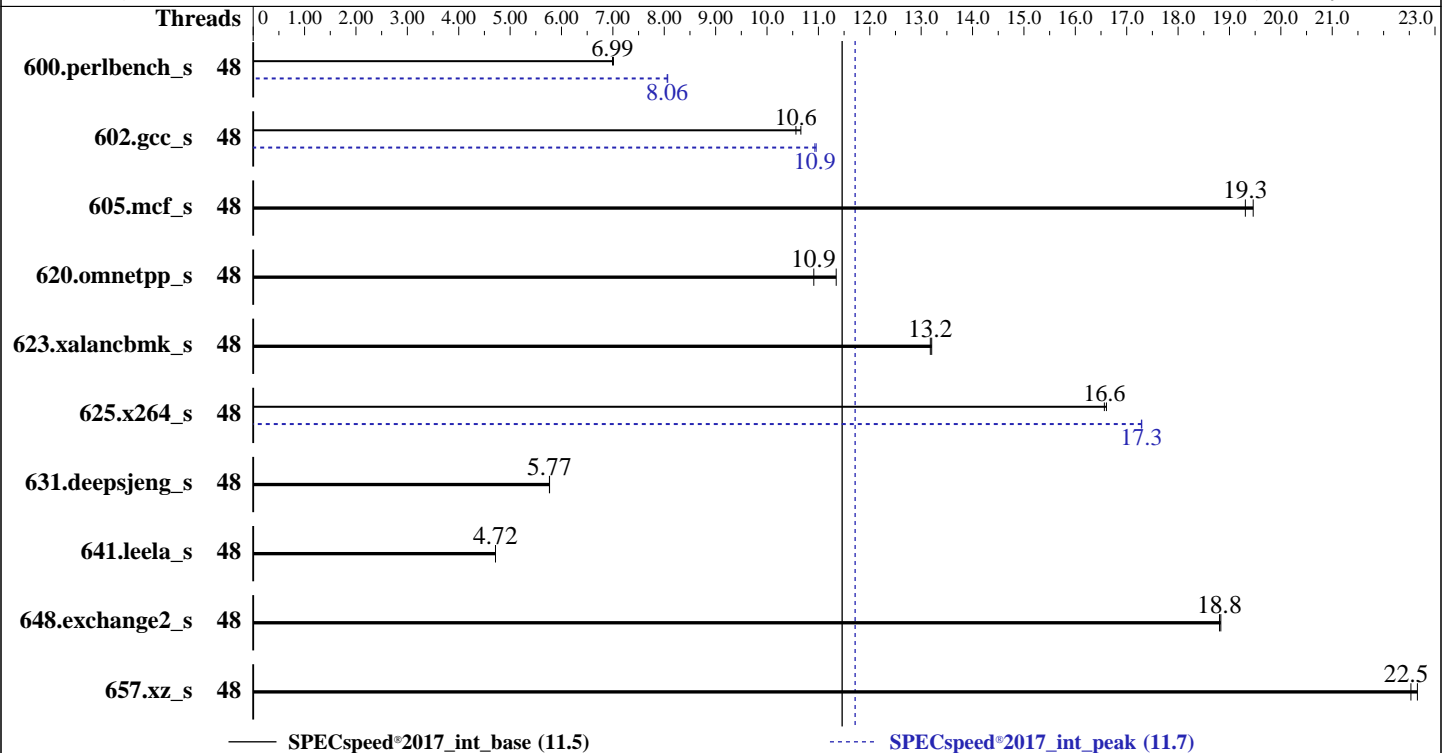
Test Date: Dec-2021

Test Sponsor: Dell Inc.

Hardware Availability: Oct-2021

Tested by: Dell Inc.

Software Availability: May-2021



Hardware

CPU Name: Intel Xeon Gold 5318Y
 Max MHz: 3400
 Nominal: 2100
 Enabled: 48 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 36 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)
 Storage: 125 GB on tmpfs
 Other: None

Software

OS: Red Hat Enterprise Linux 8.4 (Ootpa)
 4.18.0-305.el8.x86_64
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
 Compiler Build 20201113 for Linux;
 Fortran: Version 2021.1 of Intel Fortran Compiler
 Classic Build 20201112 for Linux;
 C/C++: Version 2021.1 of Intel C/C++ Compiler
 Classic Build 20201112 for Linux
 Parallel: Yes
 Firmware: Version 1.3.8 released Aug-2021
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance
 at the cost of additional power usage.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECSpeed®2017_int_peak = 11.7

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2021
Hardware Availability: Oct-2021
Software Availability: May-2021

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	253	7.01	<u>254</u>	<u>6.99</u>			48	<u>220</u>	<u>8.06</u>	220	8.07		
602.gcc_s	48	<u>377</u>	<u>10.6</u>	374	10.7			48	363	11.0	<u>364</u>	<u>10.9</u>		
605.mcf_s	48	243	19.5	<u>244</u>	<u>19.3</u>			48	243	19.5	<u>244</u>	<u>19.3</u>		
620.omnetpp_s	48	144	11.3	<u>149</u>	<u>10.9</u>			48	144	11.3	<u>149</u>	<u>10.9</u>		
623.xalancbmk_s	48	<u>108</u>	<u>13.2</u>	107	13.2			48	<u>108</u>	<u>13.2</u>	107	13.2		
625.x264_s	48	106	16.6	<u>106</u>	<u>16.6</u>			48	<u>102</u>	<u>17.3</u>	102	17.3		
631.deepsjeng_s	48	248	5.77	<u>249</u>	<u>5.77</u>			48	248	5.77	<u>249</u>	<u>5.77</u>		
641.leela_s	48	<u>362</u>	<u>4.72</u>	362	4.72			48	<u>362</u>	<u>4.72</u>	362	4.72		
648.exchange2_s	48	156	18.8	<u>156</u>	<u>18.8</u>			48	156	18.8	<u>156</u>	<u>18.8</u>		
657.xz_s	48	273	22.7	<u>274</u>	<u>22.5</u>			48	273	22.7	<u>274</u>	<u>22.5</u>		

SPECSpeed®2017_int_base = **11.5**

SPECSpeed®2017_int_peak = **11.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

Logical Processor : Disabled

Virtualization Technology : Disabled

System Profile : Custom

CPU Power Management : Maximum Performance

C1E : Disabled

C States : Autonomous

Memory Patrol Scrub : Disabled

Energy Efficiency Policy : Performance

CPU Interconnect Bus Link

Power Management : Disabled

PCI ASPM L1 Link

Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Mon Dec 6 13:12:23 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz

2 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 24

siblings : 24

physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23

physical 1: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:

Architecture: x86_64

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Date: Dec-2021

Test Sponsor: Dell Inc.

Hardware Availability: Oct-2021

Tested by: Dell Inc.

Software Availability: May-2021

Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              48
On-line CPU(s) list: 0-47
Thread(s) per core:  1
Core(s) per socket: 24
Socket(s):           2
NUMA node(s):        2
Vendor ID:           GenuineIntel
BIOS Vendor ID:      Intel
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
BIOS Model name:     Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
Stepping:            6
CPU MHz:             3231.968
BogoMIPS:            4200.00
Virtualization:      VT-x
L1d cache:           48K
L1i cache:           32K
L2 cache:            1280K
L3 cache:            36864K

```

```

NUMA node0 CPU(s):  0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, 40, 42, 44, 46

```

```

NUMA node1 CPU(s):  1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, 39, 41, 43, 45, 47

```

```

Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bml hle avx2
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd
dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 36864 KB

```

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

Platform Notes (Continued)

```

node 0 size: 257181 MB
node 0 free: 256257 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
node 1 size: 258003 MB
node 1 free: 249179 MB
node distances:
node 0 1
0: 10 20
1: 20 10

```

```

From /proc/meminfo
MemTotal:      527548600 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/sbin/tuned-adm active
Current active profile: throughput-performance

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

```

```

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): sanitization
 Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 6 13:10

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	125G	4.1G	121G	4%	/mnt/ramdisk

```
From /sys/devices/virtual/dmi/id
Vendor:      Dell Inc.
Product:     PowerEdge R450
Product Family: PowerEdge
Serial:      1S31501
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
16x 002C00B3002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933
```

BIOS:

```
BIOS Vendor:      Dell Inc.
BIOS Version:     1.3.8
BIOS Date:        08/31/2021
BIOS Revision:    1.3
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====
C      | 600.perlbench_s(peak)
-----
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

=====
C      | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
      | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Date: Dec-2021

Test Sponsor: Dell Inc.

Hardware Availability: Oct-2021

Tested by: Dell Inc.

Software Availability: May-2021

Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmallo
```

Fortran benchmarks:

```
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
```




SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

Peak Compiler Invocation

C benchmarks (except as noted below):

icx

600.perlbench_s: icc

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 11.5

PowerEdge R450 (Intel Xeon Gold 5318Y, 2.10 GHz)

SPECspeed®2017_int_peak = 11.7

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2021

Hardware Availability: Oct-2021

Software Availability: May-2021

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.5.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.5.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-12-06 13:12:22-0500.

Report generated on 2022-01-05 13:30:32 by CPU2017 PDF formatter v6442.

Originally published on 2022-01-04.