



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

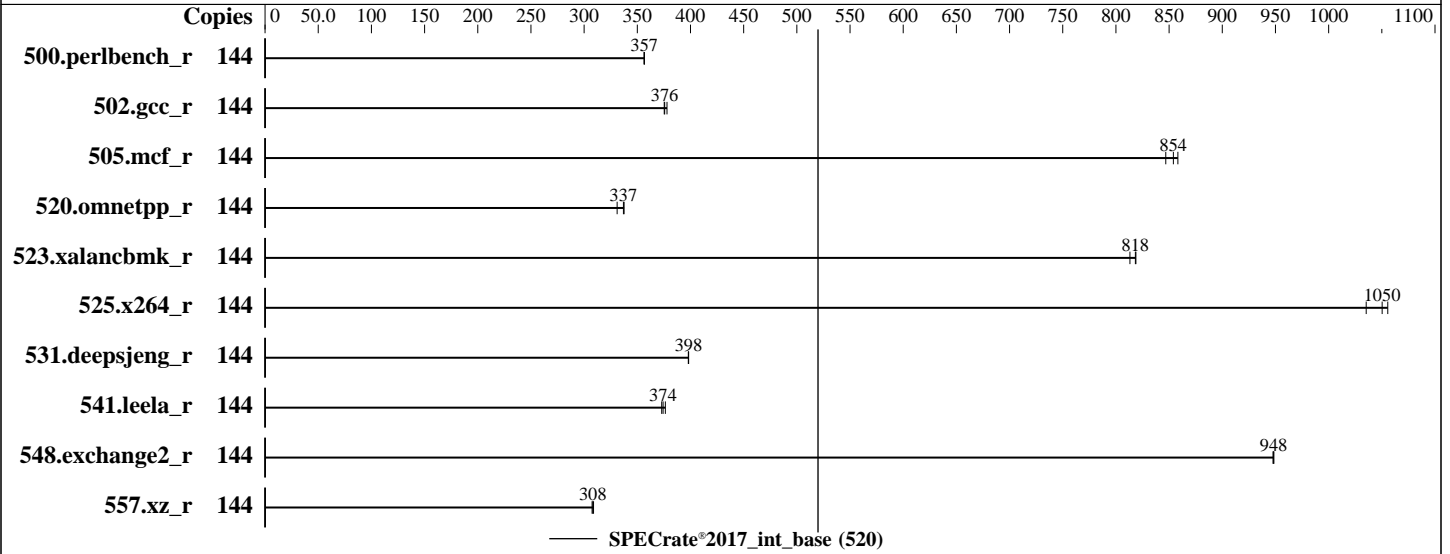
SPECrate®2017\_int\_base = 520

### xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 6488  
Test Sponsor: xFusion  
Tested by: xFusion

Test Date: Apr-2022  
Hardware Availability: Jun-2020  
Software Availability: Dec-2021



### Hardware

CPU Name: Intel Xeon Gold 5318H  
 Max MHz: 3800  
 Nominal: 2500  
 Enabled: 72 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-3200AA-R, running at 2666)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86\_64  
 Compiler: C/C++: Version 2022.0.0 of Intel oneAPI DPC++/C++ Compiler Build 20211123 for Linux; Fortran: Version 2021.5.0 of Intel Fortran Compiler Classic Build 20211109 for Linux  
 Parallel: No  
 Firmware: Version 0.81 Released Feb-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_int\_base = 520

xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 6488  
Test Sponsor: xFusion  
Tested by: xFusion

Test Date: Apr-2022  
Hardware Availability: Jun-2020  
Software Availability: Dec-2021

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	144	<b>643</b>	<b>357</b>	643	357	643	356							
502.gcc_r	144	543	375	540	378	<b>543</b>	<b>376</b>							
505.mcf_r	144	<b>272</b>	<b>854</b>	271	858	275	847							
520.omnetpp_r	144	560	338	<b>561</b>	<b>337</b>	571	331							
523.xalancbmk_r	144	<b>186</b>	<b>818</b>	186	819	187	813							
525.x264_r	144	244	1040	239	1060	<b>240</b>	<b>1050</b>							
531.deepsjeng_r	144	<b>414</b>	<b>398</b>	414	398	415	398							
541.leela_r	144	<b>637</b>	<b>374</b>	639	373	634	376							
548.exchange2_r	144	398	948	398	948	<b>398</b>	<b>948</b>							
557.xz_r	144	506	307	504	309	<b>505</b>	<b>308</b>							

SPECrate®2017\_int\_base = 520

SPECrate®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/opt/intel/oneapi/compiler/2022.0.2/linux/compiler/lib/intel64:  
/opt/intel/oneapi/compiler/2022.0.2/linux/compiler/lib/ia32: /spec2017/je5.0.1-32"  
MALLOCONF = "retain:true"



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_int\_base = 520

### xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Apr-2022  
**Hardware Availability:** Jun-2020  
**Software Availability:** Dec-2021

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Red Hat Enterprise Linux 8.1  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
Performance Profile Set to Performance  
SNC Set to Enabled

Sysinfo program /spec2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Sat Apr 9 05:04:55 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5318H CPU @ 2.50GHz  
4 "physical id"s (chips)  
144 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following  
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 18  
siblings : 36  
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu from util-linux 2.32.1:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 144  
On-line CPU(s) list: 0-143  
Thread(s) per core: 2  
Core(s) per socket: 18  
Socket(s): 4  
NUMA node(s): 8  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 5318H CPU @ 2.50GHz  
Stepping: 11  
CPU MHz: 3300.030  
BogoMIPS: 5000.00

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_int\_base = 520

### xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Apr-2022  
**Hardware Availability:** Jun-2020  
**Software Availability:** Dec-2021

## Platform Notes (Continued)

```

Virtualization:      VT-x
L1d cache:          32K
L1i cache:          32K
L2 cache:           1024K
L3 cache:           25344K
NUMA node0 CPU(s): 0-2,5,6,9,10,14,15,72-74,77,78,81,82,86,87
NUMA node1 CPU(s): 3,4,7,8,11-13,16,17,75,76,79,80,83-85,88,89
NUMA node2 CPU(s): 18-20,23,24,27,28,32,33,90-92,95,96,99,100,104,105
NUMA node3 CPU(s): 21,22,25,26,29-31,34,35,93,94,97,98,101-103,106,107
NUMA node4 CPU(s): 36-38,41,42,45,46,50,51,108-110,113,114,117,118,122,123
NUMA node5 CPU(s): 39,40,43,44,47-49,52,53,111,112,115,116,119-121,124,125
NUMA node6 CPU(s): 54-56,59,60,63,64,68,69,126-128,131,132,135,136,140,141
NUMA node7 CPU(s): 57,58,61,62,65-67,70,71,129,130,133,134,137-139,142,143
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
avx512_bf16 dtherm ida arat pln pts hwp_epp pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 2 5 6 9 10 14 15 72 73 74 77 78 81 82 86 87
node 0 size: 184612 MB
node 0 free: 191384 MB
node 1 cpus: 3 4 7 8 11 12 13 16 17 75 76 79 80 83 84 85 88 89
node 1 size: 186751 MB
node 1 free: 192952 MB
node 2 cpus: 18 19 20 23 24 27 28 32 33 90 91 92 95 96 99 100 104 105
node 2 size: 187215 MB
node 2 free: 193284 MB
node 3 cpus: 21 22 25 26 29 30 31 34 35 93 94 97 98 101 102 103 106 107
node 3 size: 187355 MB
node 3 free: 193284 MB
node 4 cpus: 36 37 38 41 42 45 46 50 51 108 109 110 113 114 117 118 122 123
node 4 size: 186809 MB
node 4 free: 193325 MB
node 5 cpus: 39 40 43 44 47 48 49 52 53 111 112 115 116 119 120 121 124 125
node 5 size: 186593 MB
node 5 free: 193373 MB
node 6 cpus: 54 55 56 59 60 63 64 68 69 126 127 128 131 132 135 136 140 141
node 6 size: 187781 MB
node 6 free: 193354 MB
node 7 cpus: 57 58 61 62 65 66 67 70 71 129 130 133 134 137 138 139 142 143
node 7 size: 187659 MB
node 7 free: 193291 MB
node distances:
node  0  1  2  3  4  5  6  7
  0:  10  11  20  20  20  20  20  20
  1:  11  10  20  20  20  20  20  20

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_int\_base = 520

### xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Apr-2022  
**Hardware Availability:** Jun-2020  
**Software Availability:** Dec-2021

## Platform Notes (Continued)

```
2: 20 20 10 11 20 20 20 20
3: 20 20 11 10 20 20 20 20
4: 20 20 20 20 10 11 20 20
5: 20 20 20 20 11 10 20 20
6: 20 20 20 20 20 20 10 11
7: 20 20 20 20 20 20 11 10
```

From /proc/meminfo

```
MemTotal: 1583423320 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/sbin/tuned-adm active

Current active profile: throughput-performance

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
```

uname -a:

```
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
```

run-level 3 Apr 9 05:01

SPEC is set to: /spec2017

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 270G 86G 184G 32% /
```

From /sys/devices/virtual/dmi/id

```
Vendor: XFUSION
Product: 2488H V6
Product Family: Cedar Island
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## xFusion

SPECrate®2017\_int\_base = 520

### xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Apr-2022  
**Hardware Availability:** Jun-2020  
**Software Availability:** Dec-2021

## Platform Notes (Continued)

Serial: 123456

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

48x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2666

**BIOS:**

BIOS Vendor: ByoSoft  
BIOS Version: 0.81  
BIOS Date: 02/10/2022

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557.xz\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.0.0 Build 20211123  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base) 541.leela\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.0.0 Build 20211123  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 548.exchange2\_r(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0  
Build 20211109\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

**C benchmarks:**

icx

**C++ benchmarks:**

icpx

**Fortran benchmarks:**

ifort



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017\_int\_base = 520

xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion

**Test Date:** Apr-2022  
**Hardware Availability:** Jun-2020  
**Software Availability:** Dec-2021

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2022.0.2/linux/compiler/lib/intel64_lin
-lqkmalloc
```

### C++ benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2022.0.2/linux/compiler/lib/intel64_lin
-lqkmalloc
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2022.0.2/linux/compiler/lib/intel64_lin
-lqkmalloc
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revC.2022-04-29.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revC.2022-04-29.html)  
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CPX-V1.1.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revC.2022-04-29.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revC.2022-04-29.xml)  
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-CPX-V1.1.xml>



# SPEC CPU<sup>®</sup>2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**xFusion**

SPECrate<sup>®</sup>2017\_int\_base = 520

xFusion 2488H V6 (Intel Xeon Gold 5318H)

SPECrate<sup>®</sup>2017\_int\_peak = Not Run

**CPU2017 License:** 6488

**Test Sponsor:** xFusion

**Tested by:** xFusion

**Test Date:** Apr-2022

**Hardware Availability:** Jun-2020

**Software Availability:** Dec-2021

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU<sup>®</sup>2017 v1.1.8 on 2022-04-08 17:04:55-0400.

Report generated on 2024-01-29 17:04:53 by CPU2017 PDF formatter v6716.

Originally published on 2022-04-27.