



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

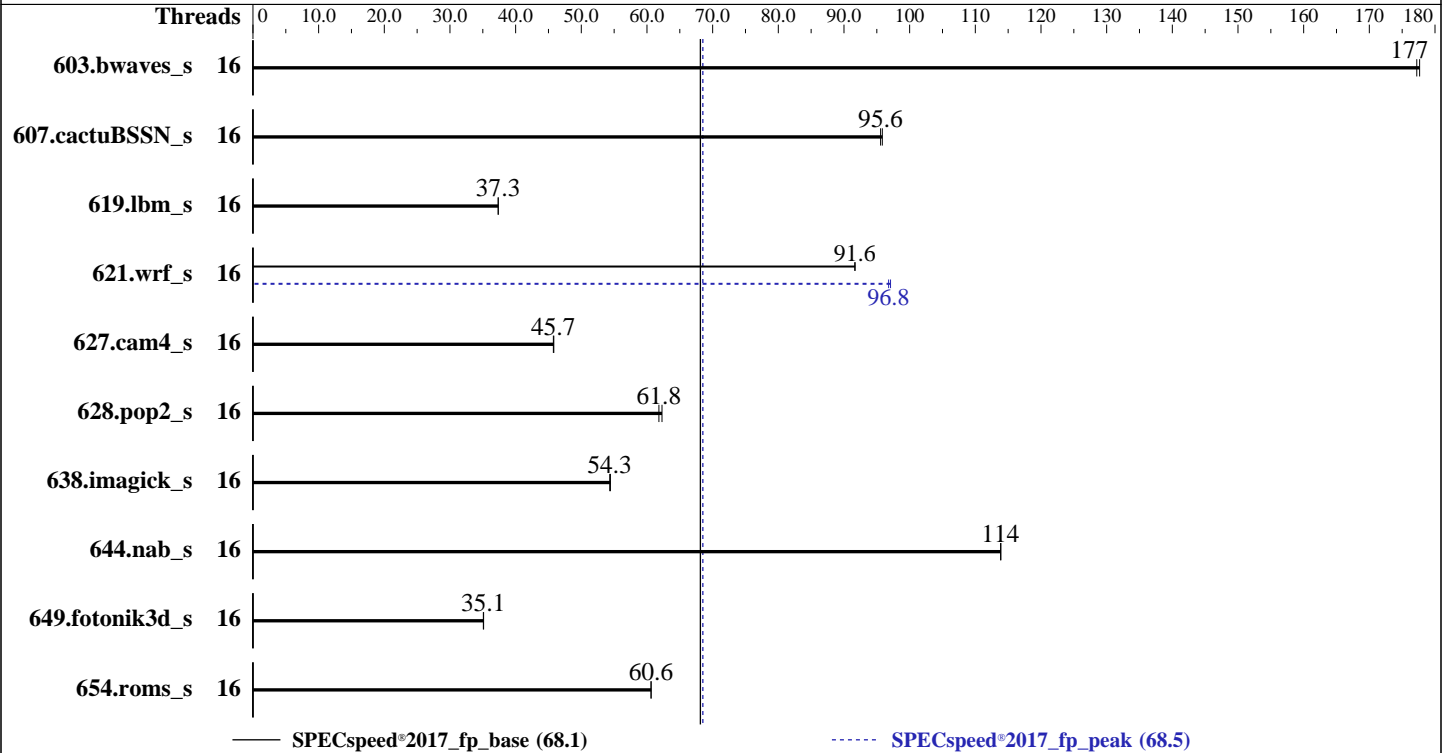
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022



Hardware

CPU Name: Intel Xeon D-2776NT
 Max MHz: 3200
 Nominal: 2100
 Enabled: 16 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 25 MB I+D on chip per chip
 Other: None
 Memory: 128 GB (4 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)
 Storage: 125 GB on tmpfs
 Other: None

Software

OS: Red Hat Enterprise Linux 8.6 (Ootpa)
 4.18.0-372.19.1.el8_6.x86_64
 Compiler: Fortran: Version 2021.5 of Intel Fortran Compiler Classic for Linux;
 C/C++: Version 2021.5 of Intel C/C++ Compiler Classic for Linux
 Parallel: Yes
 Firmware: Version 0.5.6 released Aug-2022
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECSpeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	332	178	<u>333</u>	<u>177</u>			16	332	178	<u>333</u>	<u>177</u>		
607.cactuBSSN_s	16	174	95.8	<u>174</u>	<u>95.6</u>			16	174	95.8	<u>174</u>	<u>95.6</u>		
619.lbm_s	16	<u>140</u>	<u>37.3</u>	140	37.3			16	<u>140</u>	<u>37.3</u>	140	37.3		
621.wrf_s	16	<u>144</u>	<u>91.6</u>	144	91.7			16	136	97.1	<u>137</u>	<u>96.8</u>		
627.cam4_s	16	193	45.8	<u>194</u>	<u>45.7</u>			16	193	45.8	<u>194</u>	<u>45.7</u>		
628.pop2_s	16	<u>192</u>	<u>61.8</u>	191	62.3			16	<u>192</u>	<u>61.8</u>	191	62.3		
638.imagick_s	16	<u>266</u>	<u>54.3</u>	265	54.4			16	<u>266</u>	<u>54.3</u>	265	54.4		
644.nab_s	16	153	114	<u>153</u>	<u>114</u>			16	153	114	<u>153</u>	<u>114</u>		
649.fotonik3d_s	16	260	35.1	<u>260</u>	<u>35.1</u>			16	260	35.1	<u>260</u>	<u>35.1</u>		
654.roms_s	16	<u>260</u>	<u>60.6</u>	260	60.6			16	<u>260</u>	<u>60.6</u>	260	60.6		

SPECSpeed®2017_fp_base = **68.1**

SPECSpeed®2017_fp_peak = **68.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.8-ic2022.0-DL/lib/intel64:/mnt/ramdisk/cpu2017
  -1.1.8-ic2022.0-DL/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

 jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECSpeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

Logical Processor : Disabled

Virtualization Technology : Disabled

System Profile : Custom

CPU Power Management : Maximum Performance

C1E : Disabled

C States : Autonomous

Memory Patrol Scrub : Disabled

Energy Efficiency Policy : Performance

CPU Interconnect Bus Link

Power Management : Disabled

PCI ASPM L1 Link

Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.0-DL/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Sun Aug 28 11:37:38 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) D-2776NT CPU @ 2.10GHz

1 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECSpeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Platform Notes (Continued)

```

Byte Order:           Little Endian
CPU(s):               16
On-line CPU(s) list: 0-15
Thread(s) per core:  1
Core(s) per socket:  16
Socket(s):            1
NUMA node(s):        1
Vendor ID:            GenuineIntel
BIOS Vendor ID:      Intel
CPU family:           6
Model:                108
Model name:           Intel(R) Xeon(R) D-2776NT CPU @ 2.10GHz
BIOS Model name:     Intel(R) Xeon(R) D-2776NT CPU @ 2.10GHz
Stepping:             1
CPU MHz:              2101.000
CPU max MHz:          2101.0000
CPU min MHz:          800.0000
BogoMIPS:             4200.00
L1d cache:            48K
L1i cache:            32K
L2 cache:             1280K
L3 cache:             25600K
NUMA node0 CPU(s):   0-15
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3
invpcid_single ssbd ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep
bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm
ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 25600 KB

```

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 128150 MB
node 0 free: 112140 MB
node distances:

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Platform Notes (Continued)

```
node 0
0: 10
```

```
From /proc/meminfo
MemTotal: 131225740 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.6 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.6"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.6 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.6 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.6 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
```

```
uname -a:
Linux localhost.localdomain 4.18.0-372.19.1.el8_6.x86_64 #1 SMP Mon Jul 18 11:14:02
EDT 2022 x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Platform Notes (Continued)

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Aug 28 08:41 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2022.0-DL

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	125G	9.6G	116G	8%	/mnt/ramdisk

From /sys/devices/virtual/dmi/id

```
Vendor: Dell Inc.
Product: PowerEdge XR4520c
Product Family: PowerEdge
Serial: 1234567
```

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

4x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933

BIOS:

```
BIOS Vendor: Dell Inc.
BIOS Version: 0.5.6
BIOS Date: 08/22/2022
BIOS Revision: 0.5
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====  
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
 | 644.nab_s(base, peak)  
=====
```

```
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.5.0 Build 20211109_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.  
=====
```

```
=====  
C++, C, Fortran | 607.cactuBSSN_s(base, peak)  
=====
```

```
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.5.0 Build 20211109_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0 Build 20211109_000000

Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0 Build 20211109_000000

Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0 Build 20211109_000000

Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
| 628.pop2_s(base, peak)
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0 Build 20211109_000000

Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.5.0 Build 20211109_000000

Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icc

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_fp_base = 68.1

PowerEdge XR4520c (Intel Xeon D-2776NT, 2.10 GHz)

SPECspeed®2017_fp_peak = 68.5

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2022

Hardware Availability: Dec-2022

Software Availability: Jul-2022

Peak Optimization Flags (Continued)

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Dell-ic2022-linux64-v1.0.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Dell-ic2022-linux64-v1.0.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.5.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-08-28 11:37:37-0400.

Report generated on 2022-10-26 10:31:00 by CPU2017 PDF formatter v6442.

Originally published on 2022-10-25.