SPEC CPU®2017 Floating Point Rate Result

Lenovo Global Technology

SPECrate®2017_fp_base =   814

ThinkSystem SR650 V3
(1.70 GHz, Intel Xeon Platinum 8470N)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017	**Test Date:** Jan-2023
Test Sponsor: Lenovo Global Technology	**Hardware Availability:** Feb-2023
Tested by: Lenovo Global Technology	**Software Availability:** Jun-2022

Copies

Benchmark	Copies	Value
503.bwaves_r	208	3950
507.cactuBSSN_r	208	942
508.namd_r	208	600
510.parest_r	208	377
511.povray_r	208	894
519.lbm_r	208	378
521.wrf_r	208	585
526.blender_r	208	833
527.cam4_r	208	868
538.imagick_r	208	2320
544.nab_r	208	1510
549.fotonik3d_r	208	553
554.roms_r	208	297

SPECrate®2017_fp_base (814)

Hardware

CPU Name: Intel Xeon Platinum 8470N
 Max MHz: 3600
 Nominal: 1700
Enabled: 104 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 97.5 MB I+D on chip per chip
 Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
 Kernel 5.14.21-150400.22-default
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2022.1 of Intel Fortran Compiler
 for Linux;
Parallel: No
Firmware: Lenovo BIOS Version ESE109L 1.10 released Jan-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost
 of additional power usage

SPEC CPU®2017 Floating Point Rate Result

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Lenovo Global Technology

ThinkSystem SR650 V3
(1.70 GHz, Intel Xeon Platinum 8470N)

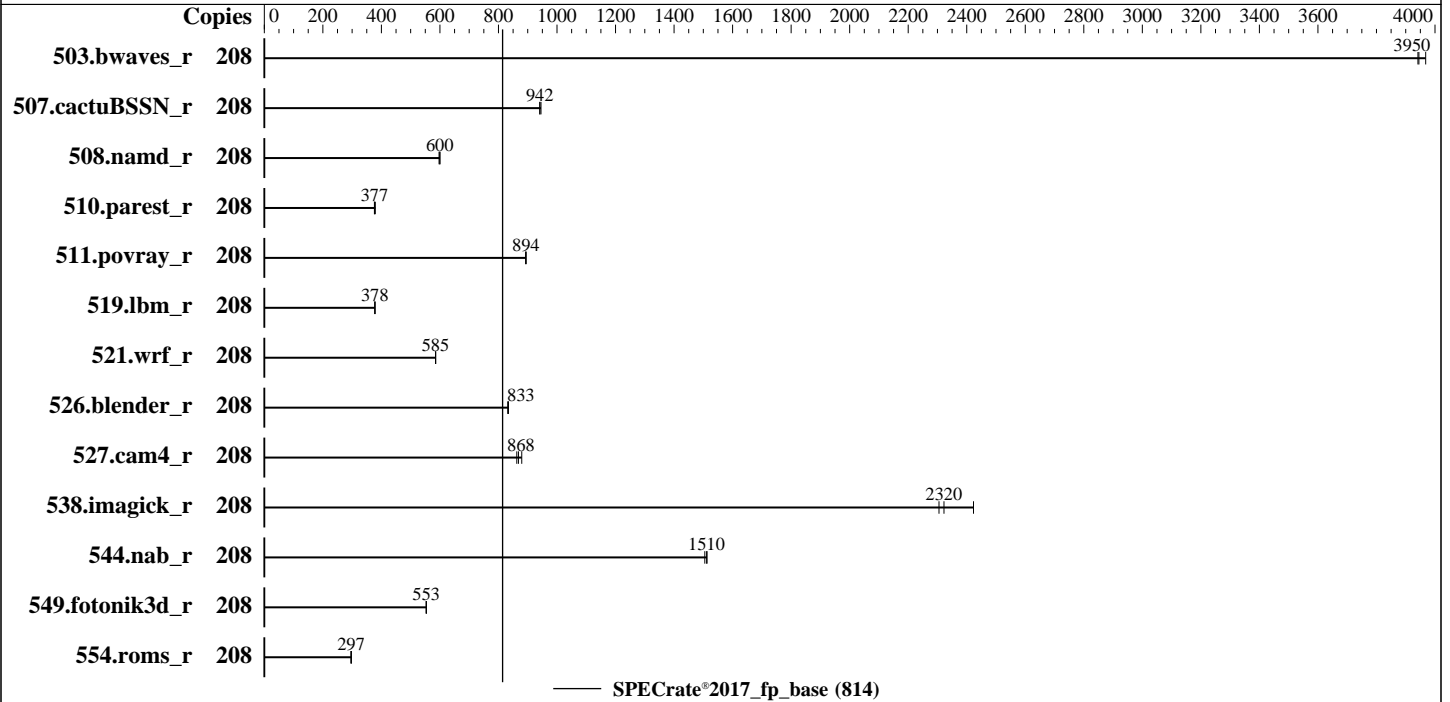SPECrate®2017_fp_base = 814

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017	**Test Date:** Jan-2023
Test Sponsor: Lenovo Global Technology	**Hardware Availability:** Feb-2023
Tested by: Lenovo Global Technology	**Software Availability:** Jun-2022

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	208	526	3970	529	3940	**529**	**3950**							
507.cactuBSSN_r	208	279	945	**280**	**942**	280	941							
508.namd_r	208	**330**	**600**	331	597	329	600							
510.parest_r	208	**1445**	**377**	1435	379	1447	376							
511.povray_r	208	**543**	**894**	544	893	543	894							
519.lbm_r	208	580	378	**580**	**378**	581	378							
521.wrf_r	208	795	586	797	585	**796**	**585**							
526.blender_r	208	380	834	381	832	**380**	**833**							
527.cam4_r	208	414	880	**419**	**868**	422	863							
538.imagick_r	208	224	2310	**223**	**2320**	213	2420							
544.nab_r	208	231	1510	**232**	**1510**	233	1510							
549.fotonik3d_r	208	1465	553	1465	553	**1465**	**553**							
554.roms_r	208	**1114**	**297**	1112	297	1115	296							

SPECrate®2017_fp_base = 814

SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
      "/home/cpu2017-1.1.8-ic2022.1/lib/intel64:/home/cpu2017-1.1.8-ic2022.1/j
      e5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

Lenovo Global Technology

ThinkSystem SR650 V3
(1.70 GHz, Intel Xeon Platinum 8470N)

SPECrate®2017_fp_base =   814

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CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2023

Hardware Availability: Feb-2023

Software Availability: Jun-2022

General Notes (Continued)

```
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>         /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Operating Mode set to Maximum Performance and then set it to Custom Mode
MONITOR/MWAIT set to Enabled
SNC set to SNC4
LLC Prefetch set to Disabled
XPT Prefetcher set to Disabled

 Sysinfo program /home/cpu2017-1.1.8-ic2022.1/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on localhost Wed Jan 11 01:27:46 2023

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8470N
       2  "physical id"s (chips)
       208 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 52
       siblings  : 104
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
          25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
          25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51

 From lscpu from util-linux 2.37.2:
```

(Continued on next page)

Lenovo Global Technology

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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Platform Notes (Continued)

```
        Architecture:                    x86_64
        CPU op-mode(s):                  32-bit, 64-bit
        Address sizes:                   46 bits physical, 57 bits virtual
        Byte Order:                      Little Endian
        CPU(s):                          208
        On-line CPU(s) list:             0-207
        Vendor ID:                       GenuineIntel
        Model name:                      Intel(R) Xeon(R) Platinum 8470N
        CPU family:                      6
        Model:                           143
        Thread(s) per core:              2
        Core(s) per socket:              52
        Socket(s):                       2
        Stepping:                        8
        BogoMIPS:                        3400.00
        Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
    pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
    pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx
    smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
    tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
    epb cat_l3 cat_l2 cdp_l3 invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
    ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1
    hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
    xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
    split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi
    umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
    tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd
    fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d
    arch_capabilities
        Virtualization:                  VT-x
        L1d cache:                       4.9 MiB (104 instances)
        L1i cache:                       3.3 MiB (104 instances)
        L2 cache:                        208 MiB (104 instances)
        L3 cache:                        195 MiB (2 instances)
        NUMA node(s):                    8
        NUMA node0 CPU(s):               0-12,104-116
        NUMA node1 CPU(s):               13-25,117-129
        NUMA node2 CPU(s):               26-38,130-142
        NUMA node3 CPU(s):               39-51,143-155
        NUMA node4 CPU(s):               52-64,156-168
        NUMA node5 CPU(s):               65-77,169-181
        NUMA node6 CPU(s):               78-90,182-194
        NUMA node7 CPU(s):               91-103,195-207
        Vulnerability Itlb multihit:     Not affected
        Vulnerability L1tf:              Not affected
```

(Continued on next page)

Lenovo Global Technology

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Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Platform Notes (Continued)

```
     Vulnerability Mds:                 Not affected
     Vulnerability Meltdown:            Not affected
     Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
     prctl and seccomp
     Vulnerability Spectre v1:          Mitigation; usercopy/swapgs barriers and __user
     pointer sanitization
     Vulnerability Spectre v2:          Mitigation; Enhanced IBRS, IBPB conditional, RSB
     filling
     Vulnerability Srbds:               Not affected
     Vulnerability Tsx async abort:     Not affected


  From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL     SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     4.9M  12 Data              1       64       1               64
     L1i       32K     3.3M   8 Instruction       1       64       1               64
     L2         2M     208M  16 Unified           2     2048       1               64
     L3      97.5M     195M  15 Unified           3   106496       1               64


  /proc/cpuinfo cache data
     cache size : 99840 KB


  From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 104 105 106 107 108 109 110 111 112 113 114
    115 116
    node 0 size: 64168 MB
    node 0 free: 63376 MB
    node 1 cpus: 13 14 15 16 17 18 19 20 21 22 23 24 25 117 118 119 120 121 122 123 124 125
    126 127 128 129
    node 1 size: 64505 MB
    node 1 free: 64171 MB
    node 2 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 130 131 132 133 134 135 136 137 138
    139 140 141 142
    node 2 size: 64505 MB
    node 2 free: 64166 MB
    node 3 cpus: 39 40 41 42 43 44 45 46 47 48 49 50 51 143 144 145 146 147 148 149 150 151
    152 153 154 155
    node 3 size: 64505 MB
    node 3 free: 64101 MB
    node 4 cpus: 52 53 54 55 56 57 58 59 60 61 62 63 64 156 157 158 159 160 161 162 163 164
    165 166 167 168
    node 4 size: 64505 MB
    node 4 free: 63883 MB
    node 5 cpus: 65 66 67 68 69 70 71 72 73 74 75 76 77 169 170 171 172 173 174 175 176 177
    178 179 180 181
    node 5 size: 64505 MB
```

(Continued on next page)

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Platform Notes (Continued)

```
  node 5 free: 64162 MB
  node 6 cpus: 78 79 80 81 82 83 84 85 86 87 88 89 90 182 183 184 185 186 187 188 189 190
  191 192 193 194
  node 6 size: 64505 MB
  node 6 free: 64164 MB
  node 7 cpus: 91 92 93 94 95 96 97 98 99 100 101 102 103 195 196 197 198 199 200 201 202
  203 204 205 206 207
  node 7 size: 64424 MB
  node 7 free: 64079 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  21  21  21  21
    1:  12  10  12  12  21  21  21  21
    2:  12  12  10  12  21  21  21  21
    3:  12  12  12  10  21  21  21  21
    4:  21  21  21  21  10  12  12  12
    5:  21  21  21  21  12  10  12  12
    6:  21  21  21  21  12  12  10  12
    7:  21  21  21  21  12  12  12  10


From /proc/meminfo
   MemTotal:       527999976 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP4"
      VERSION_ID="15.4"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp4"

uname -a:
   Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18
   UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux


Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                   Not affected
CVE-2018-3620 (L1 Terminal Fault):                Not affected
Microarchitectural Data Sampling:                 Not affected
CVE-2017-5754 (Meltdown):                         Not affected
CVE-2018-3639 (Speculative Store Bypass):         Mitigation: Speculative Store
```

(Continued on next page)

Lenovo Global Technology

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Platform Notes (Continued)

```
                                                 Bypass disabled via prctl and
                                                 seccomp
   CVE-2017-5753 (Spectre variant 1):            Mitigation: usercopy/swapgs
                                                 barriers and __user pointer
                                                 sanitization
   CVE-2017-5715 (Spectre variant 2):            Mitigation: Enhanced IBRS, IBPB:
                                                 conditional, RSB filling
   CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
   CVE-2019-11135 (TSX Asynchronous Abort):              Not affected

   run-level 3 Jan 11 01:26

   SPEC is set to: /home/cpu2017-1.1.8-ic2022.1
      Filesystem      Type  Size  Used Avail Use% Mounted on
      /dev/sda3       xfs   889G   24G  865G   3% /

   From /sys/devices/virtual/dmi/id
       Vendor:         Lenovo
       Product:        ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U
       Product Family: ThinkSystem
       Serial:         1234567890

   Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
   interpret this section. The 'dmidecode' program reads system data which is "intended to
   allow hardware to be accurately determined", but the intent may not be met, as there are
   frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
     Memory:
       16x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800

   BIOS:
      BIOS Vendor:      Lenovo
      BIOS Version:     ESE109L-1.10
      BIOS Date:        01/07/2023
      BIOS Revision:    1.10
      Firmware Revision: 1.0

   (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

(Continued on next page)

Lenovo Global Technology

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SPECrate®2017_fp_base =   814

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Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------

==========================================================================
C++              | 508.namd_r(base) 510.parest_r(base)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------

==========================================================================
C++, C           | 511.povray_r(base) 526.blender_r(base)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------

==========================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
--------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------

==========================================================================
Fortran          | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
--------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------

==========================================================================
Fortran, C       | 521.wrf_r(base) 527.cam4_r(base)
--------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
```

(Continued on next page)

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Compiler Version Notes (Continued)

```
   2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

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Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.html
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.xml
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml

SPEC CPU®2017 Floating Point Rate Result
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Lenovo Global Technology

SPECrate®2017_fp_base =   814

ThinkSystem SR650 V3
(1.70 GHz, Intel Xeon Platinum 8470N)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Jun-2022

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2023-01-10 12:27:45-0500.
Report generated on 2023-02-01 18:26:31 by CPU2017 PDF formatter v6442.
Originally published on 2023-02-01.