



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 3

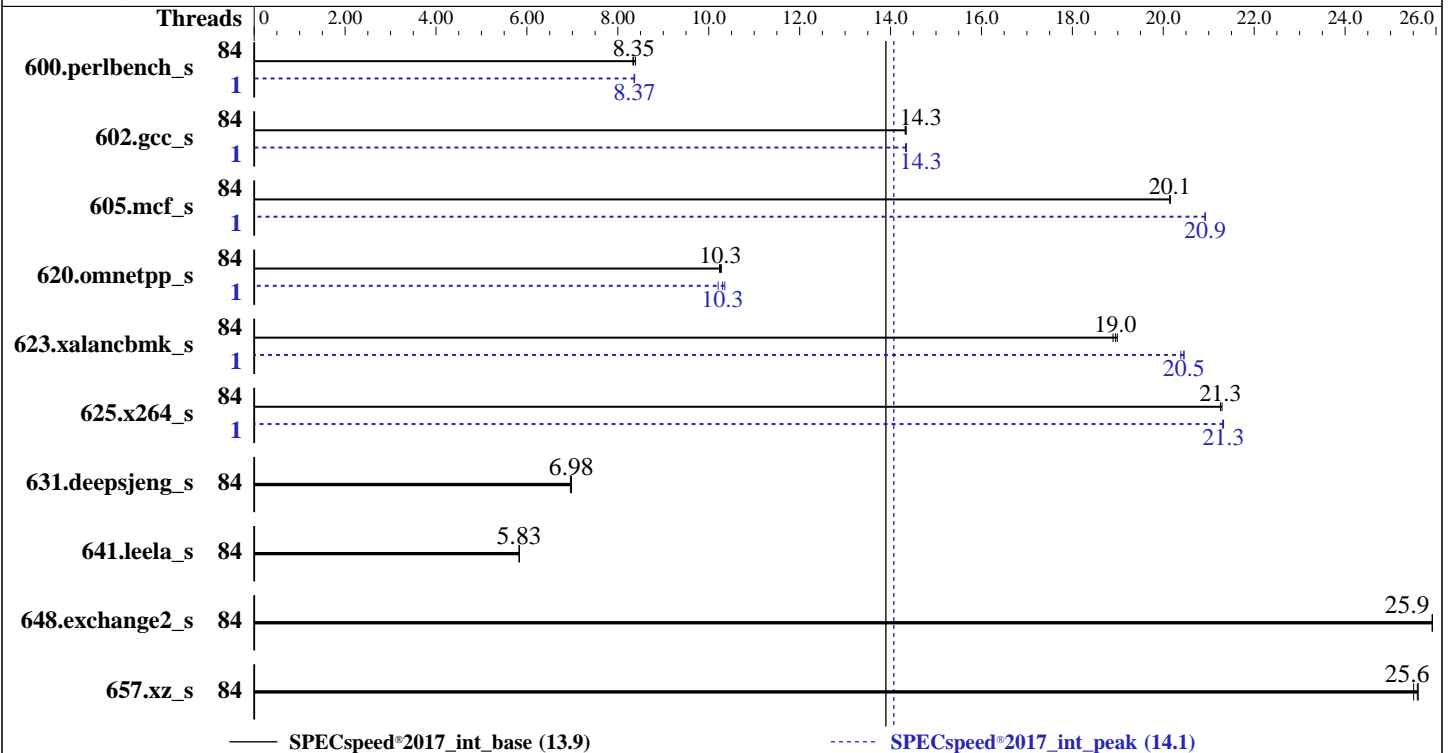
Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2023

Hardware Availability: Dec-2022

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9634
 Max MHz: 3700
 Nominal: 2250
 Enabled: 84 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 384 MB I+D on chip per chip,
 32 MB shared / 7 cores
 Other: None
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)
 Storage: 1 x 1.6 TB NVMe SSD, RAID 0
 Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
 Kernel 5.14.0-70.13.1.el9_0.x86_64
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: Yes
 Firmware: HPE BIOS Version v1.12 11/24/2022 released
 Nov-2022
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	84	212	8.39	213	8.33	<u>213</u>	<u>8.35</u>	1	212	8.37	<u>212</u>	<u>8.37</u>	212	8.36
602.gcc_s	84	278	14.3	278	14.3	<u>278</u>	<u>14.3</u>	1	278	14.3	<u>278</u>	<u>14.3</u>	278	14.3
605.mcf_s	84	<u>234</u>	<u>20.1</u>	234	20.2	234	20.1	1	<u>226</u>	<u>20.9</u>	226	20.9	226	20.9
620.omnetpp_s	84	159	10.2	159	10.3	<u>159</u>	<u>10.3</u>	1	158	10.4	160	10.2	<u>158</u>	<u>10.3</u>
623.xalancbmk_s	84	74.6	19.0	75.0	18.9	<u>74.8</u>	<u>19.0</u>	1	<u>69.3</u>	<u>20.5</u>	69.3	20.5	69.5	20.4
625.x264_s	84	83.0	21.3	<u>83.0</u>	<u>21.3</u>	82.8	21.3	1	<u>82.7</u>	<u>21.3</u>	82.8	21.3	82.7	21.3
631.deepsjeng_s	84	<u>205</u>	<u>6.98</u>	206	6.96	205	6.98	84	<u>205</u>	<u>6.98</u>	206	6.96	205	6.98
641.leela_s	84	293	5.83	<u>293</u>	<u>5.83</u>	292	5.83	84	293	5.83	<u>293</u>	<u>5.83</u>	292	5.83
648.exchange2_s	84	113	25.9	<u>113</u>	<u>25.9</u>	113	25.9	84	113	25.9	<u>113</u>	<u>25.9</u>	113	25.9
657.xz_s	84	241	25.6	<u>242</u>	<u>25.6</u>	242	25.5	84	241	25.6	<u>242</u>	<u>25.6</u>	242	25.5

SPECspeed®2017_int_base = **13.9**

SPECspeed®2017_int_peak = **14.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

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Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-83"
LD_LIBRARY_PATH = "/home/CPU2017/amd_speed_aocc400_genoa_B_lib/lib:"
LIBBOMM_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "84"

Environment variables set by runcpu during the 600.perlbench_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 602.gcc_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 605.mcf_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 620.omnetpp_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 625.x264_s peak run:
GOMP_CPU_AFFINITY = "15"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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Platform Notes

BIOS Configuration

Workload Profile set to General Peak Frequency Compute

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

AMD SMT Option set to Disabled

NUMA memory domains per socket set to Four memory domains per socket

Last-Level Cache (LLC) as NUMA Node set to Enabled

ACPI CST C2 Latency set to 18 microseconds

Memory PStates set to Disabled

Thermal Configuration set to Maximum Cooling

The system ROM used for this result contains microcode version 0x0A10110e for the AMD EPYC 9nn4X family of processors. The reference code/AGESA version used in this ROM is version GenoAPI 1.0.0.1-L6

Sysinfo program /home/CPU2017/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Thu Apr 7 05:32:12 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 9634 84-Core Processor

1 "physical id"s (chips)

84 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 84

siblings : 84

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30 32 33 34 35 36 37 38 40 41 42 43 44 45 46 48 49 50 51 52 53 54 56 57 58 59
60 61 62 64 65 66 67 68 69 70 72 73 74 75 76 77 78 80 81 82 83 84 85 86 88 89 90 91
92 93 94

From lscpu from util-linux 2.37.4:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 84
On-line CPU(s) list: 0-83
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9634 84-Core Processor
BIOS Model name: AMD EPYC 9634 84-Core Processor

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Platform Notes (Continued)

```

CPU family:                25
Model:                     17
Thread(s) per core:       1
Core(s) per socket:       84
Socket(s):                 1
Stepping:                  1
BogoMIPS:                  4493.37
Flags:                     fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
                             pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
                             pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
                             aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                             popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
                             misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
                             bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
                             ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
                             avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
                             avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                             cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt
                             lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                             pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke
                             avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57
                             rdpid overflow_recov succor smca fsrm flush_lld
Virtualization:            AMD-V
L1d cache:                 2.6 MiB (84 instances)
L1i cache:                 2.6 MiB (84 instances)
L2 cache:                  84 MiB (84 instances)
L3 cache:                  384 MiB (12 instances)
NUMA node(s):              12
NUMA node0 CPU(s):         0-6
NUMA node1 CPU(s):         28-34
NUMA node2 CPU(s):         56-62
NUMA node3 CPU(s):         14-20
NUMA node4 CPU(s):         42-48
NUMA node5 CPU(s):         70-76
NUMA node6 CPU(s):         21-27
NUMA node7 CPU(s):         49-55
NUMA node8 CPU(s):         77-83
NUMA node9 CPU(s):         7-13
NUMA node10 CPU(s):        35-41
NUMA node11 CPU(s):        63-69
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:        Not affected
Vulnerability Mds:         Not affected
Vulnerability Meltdown:    Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:   Mitigation; usercopy/swapgs barriers and __user

```

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Platform Notes (Continued)

pointer sanitization
 Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW,
 STIBP disabled, RSB filling
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	2.6M	8	Data	1	64	1	64
L1i	32K	2.6M	8	Instruction	1	64	1	64
L2	1M	84M	8	Unified	2	2048	1	64
L3	32M	384M	16	Unified	3	32768	1	64

/proc/cpuinfo cache data
cache size : 1024 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)
 node 0 cpus: 0 1 2 3 4 5 6
 node 0 size: 32008 MB
 node 0 free: 31747 MB
 node 1 cpus: 28 29 30 31 32 33 34
 node 1 size: 32254 MB
 node 1 free: 31707 MB
 node 2 cpus: 56 57 58 59 60 61 62
 node 2 size: 32254 MB
 node 2 free: 32020 MB
 node 3 cpus: 14 15 16 17 18 19 20
 node 3 size: 32254 MB
 node 3 free: 32125 MB
 node 4 cpus: 42 43 44 45 46 47 48
 node 4 size: 32254 MB
 node 4 free: 32117 MB
 node 5 cpus: 70 71 72 73 74 75 76
 node 5 size: 32217 MB
 node 5 free: 32082 MB
 node 6 cpus: 21 22 23 24 25 26 27
 node 6 size: 32254 MB
 node 6 free: 32134 MB
 node 7 cpus: 49 50 51 52 53 54 55
 node 7 size: 32254 MB
 node 7 free: 32130 MB
 node 8 cpus: 77 78 79 80 81 82 83
 node 8 size: 32254 MB
 node 8 free: 32109 MB
 node 9 cpus: 7 8 9 10 11 12 13

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Platform Notes (Continued)

```

node 9 size: 32254 MB
node 9 free: 32127 MB
node 10 cpus: 35 36 37 38 39 40 41
node 10 size: 32254 MB
node 10 free: 32133 MB
node 11 cpus: 63 64 65 66 67 68 69
node 11 size: 32204 MB
node 11 free: 32073 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11
 0:  10 11 11 12 12 12 12 12 12 12 12 12
 1:  11 10 11 12 12 12 12 12 12 12 12 12
 2:  11 11 10 12 12 12 12 12 12 12 12 12
 3:  12 12 12 10 11 11 12 12 12 12 12 12
 4:  12 12 12 11 10 11 12 12 12 12 12 12
 5:  12 12 12 11 11 10 12 12 12 12 12 12
 6:  12 12 12 12 12 12 10 11 11 12 12 12
 7:  12 12 12 12 12 12 11 10 11 12 12 12
 8:  12 12 12 12 12 12 11 11 10 12 12 12
 9:  12 12 12 12 12 12 12 12 12 10 11 11
10:  12 12 12 12 12 12 12 12 12 11 10 11
11:  12 12 12 12 12 12 12 12 12 11 11 10

```

```

From /proc/meminfo
MemTotal:      396000520 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="9.0 (Plow)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="9.0"
PLATFORM_ID="platform:el9"
PRETTY_NAME="Red Hat Enterprise Linux 9.0 (Plow)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 9.0 (Plow)
system-release: Red Hat Enterprise Linux release 9.0 (Plow)
system-release-cpe: cpe:/o:redhat:enterprise_linux:9::baseos

```

```

uname -a:
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14
12:42:38 EDT 2022 x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

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Platform Notes (Continued)

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Retpolines, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 3 Apr 7 05:30

SPEC is set to: /home/CPU2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	819G	23G	796G	3%	/home

From /sys/devices/virtual/dmi/id

```
Vendor:          HPE
Product:         ProLiant DL345 Gen11
Product Family: ProLiant
Serial:          DL345G11-004
```

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Hynix HMC88MEBRA113N 32 GB 2 rank 4800

BIOS:

```
BIOS Vendor:     HPE
BIOS Version:    1.12
BIOS Date:       11/24/2022
BIOS Revision:   1.12
Firmware Revision: 1.10
```

(End of data from sysinfo program)



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Compiler Version Notes

```

=====
C          | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
          | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

```

```

=====
C++       | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
          | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

```

```

=====
Fortran   | 648.exchange2_s(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

```

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang



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Base Portability Flags

```
600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```



SPEC CPU®2017 Integer Speed Result

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(Test Sponsor: HPE)

ProLiant DL345 Gen11

(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2023

Hardware Availability: Dec-2022

Software Availability: Nov-2022

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

```
602.gcc_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
```

(Continued on next page)



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Peak Optimization Flags (Continued)

602.gcc_s (continued):

```
-Wl,-allow-multiple-definition -z muldefs -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

605.mcf_s: Same as 600.perlbench_s

625.x264_s: Same as 600.perlbench_s

657.xz_s: basepeak = yes

C++ benchmarks:

```
620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

```
623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes



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Software Availability: Nov-2022

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.html>

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.xml>

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

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