



SPEC CPU®2017 Floating Point Rate Result

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Inspur Corporation

SPECrate®2017_fp_base = 1390

Inspur NF5280A7 (AMD EPYC 9654)

SPECrate®2017_fp_peak = 1520

CPU2017 License: 3358

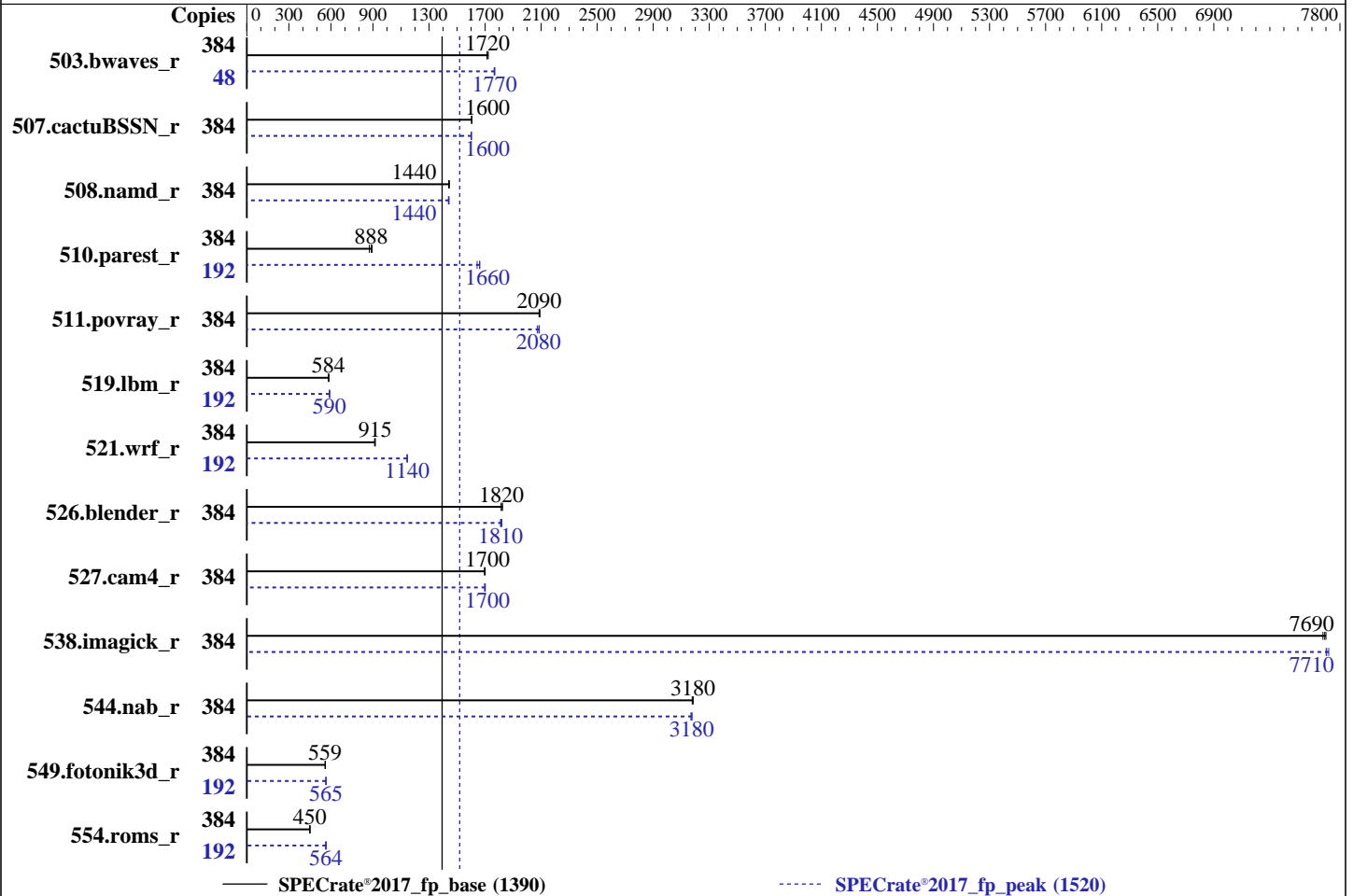
Test Date: Feb-2023

Test Sponsor: Inspur Corporation

Hardware Availability: Feb-2023

Tested by: Inspur Corporation

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9654
 Max MHz: 3700
 Nominal: 2400
 Enabled: 192 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 1 TB NVME SSD
 Other: None

Software

OS: Red Hat Enterprise Linux release 9 (Plow)
 5.14.0-70.22.1.el9_0.x86_64
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: No
 Firmware: Version 04.02.14 released Dec-2022
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	384	<u>2245</u>	<u>1720</u>	2235	1720	2246	1710	48	272	1770	273	1760	<u>272</u>	<u>1770</u>
507.cactuBSSN_r	384	303	1600	<u>303</u>	<u>1600</u>	303	1610	384	<u>304</u>	<u>1600</u>	303	1600	304	1600
508.namd_r	384	253	1440	<u>253</u>	<u>1440</u>	252	1450	384	254	1440	<u>253</u>	<u>1440</u>	253	1440
510.parest_r	384	<u>1131</u>	<u>888</u>	1124	894	1149	874	192	302	1660	<u>302</u>	<u>1660</u>	306	1640
511.povray_r	384	430	2090	<u>430</u>	<u>2090</u>	429	2090	384	<u>430</u>	<u>2080</u>	430	2090	433	2070
519.lbm_r	384	692	585	694	583	<u>693</u>	<u>584</u>	192	<u>343</u>	<u>590</u>	342	591	343	590
521.wrf_r	384	939	916	944	912	<u>940</u>	<u>915</u>	192	376	1140	<u>376</u>	<u>1140</u>	376	1140
526.blender_r	384	<u>322</u>	<u>1820</u>	320	1830	322	1810	384	323	1810	<u>323</u>	<u>1810</u>	321	1820
527.cam4_r	384	396	1700	395	1700	<u>395</u>	<u>1700</u>	384	396	1700	<u>395</u>	<u>1700</u>	395	1700
538.imagick_r	384	124	7680	124	7700	<u>124</u>	<u>7690</u>	384	<u>124</u>	<u>7710</u>	124	7720	124	7700
544.nab_r	384	203	3180	<u>203</u>	<u>3180</u>	203	3180	384	204	3170	203	3180	<u>203</u>	<u>3180</u>
549.fotonik3d_r	384	<u>2675</u>	<u>559</u>	2671	560	2677	559	192	1325	564	1325	565	<u>1325</u>	<u>565</u>
554.roms_r	384	1352	451	<u>1356</u>	<u>450</u>	1359	449	192	539	566	542	563	<u>541</u>	<u>564</u>

SPECrate®2017_fp_base = **1390**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.

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Operating System Notes (Continued)

To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations, 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/CPU2017/amd_rate_aocc400_genoa_B_lib/lib:/home/CPU2017/amd_rate_a  
    occ400_genoa_B_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
SVM Mode = disable
DRAM Scrub time = disable
NUMA nodes per socket = NPS4
Determinism Slider = Power
cTDP = 400
Package Power Limit = 400

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Tue Feb 21 18:49:47 2023

SUT (System Under Test) info as seen by some common utilities.

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10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a

```
Linux localhost.localdomain 5.14.0-70.22.1.el9_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
```

2. w

```
18:49:47 up 6:05, 1 user, load average: 182.05, 328.22, 359.41
USER      TTY      LOGIN@  IDLE   JCPU   PCPU WHAT
root      tty1    12:44   5:52m  1.49s  0.35s /bin/bash ./amd_rate_aocc400_genoa_B1.sh
```

3. Username

From environment variable \$USER: root

4. ulimit -a

real-time non-blocking time (microseconds, -R) unlimited

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Platform Notes (Continued)

```

core file size          (blocks, -c) 0
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 6191058
max locked memory      (kbytes, -l) 2097152
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes    (-u) 6191058
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 18
login -- root
-bash
python3 ./run_amd_rate_aocc400_genoa_B1.py
/bin/bash ./amd_rate_aocc400_genoa_B1.sh
runcpu --config amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower --runmode
rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.027/templogs/preenv.fprate.027.0.log --lognum 027.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

```

6. /proc/cpuinfo

```

model name      : AMD EPYC 9654 96-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa101111
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 96
siblings      : 192
2 physical ids (chips)
384 processors (hardware threads)
physical id 0: core ids 0-95
physical id 1: core ids 0-95

```

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Platform Notes (Continued)

physical id 0: apicids 0-191
physical id 1: apicids 256-447

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 384
On-line CPU(s) list: 0-383
Vendor ID: AuthenticAMD
BIOS Vendor ID: Advanced Micro Devices, Inc.
Model name: AMD EPYC 9654 96-Core Processor
BIOS Model name: AMD EPYC 9654 96-Core Processor
CPU family: 25
Model: 17
Thread(s) per core: 2
Core(s) per socket: 96
Socket(s): 2
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3707.8120
CPU min MHz: 1500.0000
BogoMIPS: 4799.95

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld

Virtualization: AMD-V
L1d cache: 6 MiB (192 instances)

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Platform Notes (Continued)

```

L1i cache:                6 MiB (192 instances)
L2 cache:                 192 MiB (192 instances)
L3 cache:                 768 MiB (24 instances)
NUMA node(s):             24
NUMA node0 CPU(s):        0-7,192-199
NUMA node1 CPU(s):        8-15,200-207
NUMA node2 CPU(s):        16-23,208-215
NUMA node3 CPU(s):        24-31,216-223
NUMA node4 CPU(s):        32-39,224-231
NUMA node5 CPU(s):        40-47,232-239
NUMA node6 CPU(s):        48-55,240-247
NUMA node7 CPU(s):        56-63,248-255
NUMA node8 CPU(s):        64-71,256-263
NUMA node9 CPU(s):        72-79,264-271
NUMA node10 CPU(s):       80-87,272-279
NUMA node11 CPU(s):       88-95,280-287
NUMA node12 CPU(s):       96-103,288-295
NUMA node13 CPU(s):       104-111,296-303
NUMA node14 CPU(s):       112-119,304-311
NUMA node15 CPU(s):       120-127,312-319
NUMA node16 CPU(s):       128-135,320-327
NUMA node17 CPU(s):       136-143,328-335
NUMA node18 CPU(s):       144-151,336-343
NUMA node19 CPU(s):       152-159,344-351
NUMA node20 CPU(s):       160-167,352-359
NUMA node21 CPU(s):       168-175,360-367
NUMA node22 CPU(s):       176-183,368-375
NUMA node23 CPU(s):       184-191,376-383
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:         Not affected
Vulnerability Meltdown:    Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:   Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:   Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
                             filling
Vulnerability Srbds:        Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	6M	8	Data	1	64	1	64
L1i	32K	6M	8	Instruction	1	64	1	64
L2	1M	192M	8	Unified	2	2048	1	64
L3	32M	768M	16	Unified	3	32768	1	64

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Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 24 nodes (0-23)
node 0 cpus: 0-7,192-199
node 0 size: 64310 MB
node 0 free: 63484 MB
node 1 cpus: 8-15,200-207
node 1 size: 64507 MB
node 1 free: 63709 MB
node 2 cpus: 16-23,208-215
node 2 size: 64470 MB
node 2 free: 63657 MB
node 3 cpus: 24-31,216-223
node 3 size: 64507 MB
node 3 free: 63685 MB
node 4 cpus: 32-39,224-231
node 4 size: 64507 MB
node 4 free: 63688 MB
node 5 cpus: 40-47,232-239
node 5 size: 64507 MB
node 5 free: 63691 MB
node 6 cpus: 48-55,240-247
node 6 size: 64507 MB
node 6 free: 63709 MB
node 7 cpus: 56-63,248-255
node 7 size: 64507 MB
node 7 free: 63709 MB
node 8 cpus: 64-71,256-263
node 8 size: 64507 MB
node 8 free: 63691 MB
node 9 cpus: 72-79,264-271
node 9 size: 64507 MB
node 9 free: 63660 MB
node 10 cpus: 80-87,272-279
node 10 size: 64507 MB
node 10 free: 63488 MB
node 11 cpus: 88-95,280-287
node 11 size: 64507 MB
node 11 free: 63642 MB
node 12 cpus: 96-103,288-295
node 12 size: 64507 MB
node 12 free: 63679 MB
node 13 cpus: 104-111,296-303
node 13 size: 64507 MB
node 13 free: 63694 MB
node 14 cpus: 112-119,304-311
node 14 size: 64507 MB

```

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Platform Notes (Continued)

```

node 14 free: 63681 MB
node 15 cpus: 120-127,312-319
node 15 size: 64507 MB
node 15 free: 63714 MB
node 16 cpus: 128-135,320-327
node 16 size: 64507 MB
node 16 free: 63720 MB
node 17 cpus: 136-143,328-335
node 17 size: 64507 MB
node 17 free: 63703 MB
node 18 cpus: 144-151,336-343
node 18 size: 64507 MB
node 18 free: 63711 MB
node 19 cpus: 152-159,344-351
node 19 size: 64507 MB
node 19 free: 63708 MB
node 20 cpus: 160-167,352-359
node 20 size: 64507 MB
node 20 free: 63710 MB
node 21 cpus: 168-175,360-367
node 21 size: 64507 MB
node 21 free: 63706 MB
node 22 cpus: 176-183,368-375
node 22 size: 64507 MB
node 22 free: 63715 MB
node 23 cpus: 184-191,376-383
node 23 size: 64431 MB
node 23 free: 63634 MB

```

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
0:	10	11	11	12	12	12	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
1:	11	10	11	12	12	12	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
2:	11	11	10	12	12	12	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
3:	12	12	12	10	11	11	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
4:	12	12	12	11	10	11	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
5:	12	12	12	11	11	10	12	12	12	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
6:	12	12	12	12	12	12	10	11	11	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
7:	12	12	12	12	12	12	11	10	11	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
8:	12	12	12	12	12	12	11	11	10	12	12	12	32	32	32	32	32	32	32	32	32	32	32	32
9:	12	12	12	12	12	12	12	12	12	10	11	11	32	32	32	32	32	32	32	32	32	32	32	32
10:	12	12	12	12	12	12	12	12	12	11	10	11	32	32	32	32	32	32	32	32	32	32	32	32
11:	12	12	12	12	12	12	12	12	12	11	11	10	32	32	32	32	32	32	32	32	32	32	32	32
12:	32	32	32	32	32	32	32	32	32	32	32	32	10	11	11	12	12	12	12	12	12	12	12	12
13:	32	32	32	32	32	32	32	32	32	32	32	32	11	10	11	12	12	12	12	12	12	12	12	12
14:	32	32	32	32	32	32	32	32	32	32	32	32	11	11	10	12	12	12	12	12	12	12	12	12
15:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	10	11	11	12	12	12	12	12	12
16:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	11	10	11	12	12	12	12	12	12

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Platform Notes (Continued)

17:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	11	11	10	12	12	12	12	12	
18:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	10	11	11	12	12	12
19:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	11	10	11	12	12	12
20:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	11	11	10	12	12	12
21:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	12	12	12	10	11	11
22:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	12	12	12	11	10	11
23:	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	12	12	12	11	11	10

```
-----
9. /proc/meminfo
   MemTotal:      1585017572 kB
-----
```

```
-----
10. who -r
     run-level 3 Feb 21 12:44
-----
```

```
-----
11. Systemd service manager version: systemd 250 (250-6.el9_0)
     Default Target      Status
     multi-user          degraded
-----
```

```
-----
12. Failed units, from systemctl list-units --state=failed
     UNIT                                LOAD    ACTIVE SUB    DESCRIPTION
     * dnf-makecache.service loaded failed failed dnf makecache
-----
```

```
-----
13. Services, from systemctl list-unit-files
     STATE      UNIT FILES
     enabled    NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
                nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
                systemd-network-generator udisks2 upower
     enabled-runtime  systemd-remount-fs
     disabled      blk-availability canberra-system-bootup canberra-system-shutdown
                canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell kvm_stat
                man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmbd-rebuild serial-getty@
                sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
     indirect     sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
-----
```

```
-----
14. Linux kernel boot-time arguments, from /proc/cmdline
     BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-70.22.1.el9_0.x86_64
     root=/dev/mapper/rhel-root
     ro
     resume=/dev/mapper/rhel-swap
     rd.lvm.lv=rhel/root
-----
```

(Continued on next page)



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Inspur NF5280A7 (AMD EPYC 9654)

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CPU2017 License: 3358

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Tested by: Inspur Corporation

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Hardware Availability: Feb-2023

Software Availability: Nov-2022

Platform Notes (Continued)

rd.lvm.lv=rhel/swap

15. cpupower frequency-info

analyzing CPU 0:

current policy: frequency should be within 1.50 GHz and 2.40 GHz.

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

Boost States: 0

Total States: 3

Pstate-P0: 2400MHz

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

defrag	[always]	defer	defer+madvise	madvise	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs 60000

(Continued on next page)



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Platform Notes (Continued)

```

defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000

```

19. OS release

```

From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)

```

20. Disk information

SPEC is set to: /home/CPU2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	856G	14G	843G	2%	/home

21. /sys/devices/virtual/dmi/id

```

Vendor:      Inspur
Product:     NF5280A7
Product Family: Not specified
Serial:      190251214

```

22. dmidecode

Additional information from dmidecode 3.3 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```

BIOS Vendor:      American Megatrends International, LLC.
BIOS Version:     04.02.14
BIOS Date:        12/29/2022

```



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Software Availability: Nov-2022

Compiler Version Notes

```
=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
  | 544.nab_r(base, peak)
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====
```

```
=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====
```

```
=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====
```

```
=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
=====
```

(Continued on next page)



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Tested by: Inspur Corporation

Test Date: Feb-2023

Hardware Availability: Feb-2023

Software Availability: Nov-2022

Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
```

```
=====  
Fortran          | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)  
                  | 554.roms_r(base, peak)  
-----
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
```

```
=====  
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)  
-----
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

(Continued on next page)



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Tested by: Inspur Corporation

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Hardware Availability: Feb-2023

Software Availability: Nov-2022

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
 507.cactuBSSN_r: -DSPEC_LP64
 508.namd_r: -DSPEC_LP64
 510.parest_r: -DSPEC_LP64
 511.povray_r: -DSPEC_LP64
 519.lbm_r: -DSPEC_LP64
 521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
 526.blender_r: -funsigned-char -DSPEC_LP64
 527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
 538.imagick_r: -DSPEC_LP64
 544.nab_r: -DSPEC_LP64
 549.fotonik3d_r: -DSPEC_LP64
 554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3
 -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
 -march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7
 -mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
 -fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
 -zopt -lamdlibm -lamdalloc -lflang

C++ benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
 -Wl,-mllvm -Wl,-reduce-array-computations=3
 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
 -fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
 -finline-aggressive -mllvm -loop-unswitch-threshold=200000

(Continued on next page)



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Tested by: Inspur Corporation

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Hardware Availability: Feb-2023

Software Availability: Nov-2022

Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```




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Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

538.imagick_r: Same as 519.lbm_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

C++ benchmarks:

```
508.namd_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

```
510.parest_r: -m64 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

Fortran benchmarks:

```
503.bwaves_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
```

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Hardware Availability: Feb-2023

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

503.bwaves_r (continued):

-lamdalloc -lflang

549.fotonik3d_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -Kieee
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -lflang

554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-lflang

527.cam4_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt
-Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-lflang

Benchmarks using both C and C++:

511.povray_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -finline-aggressive

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Tested by: Inspur Corporation

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Software Availability: Nov-2022

Peak Optimization Flags (Continued)

511.povray_r (continued):

```
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc
```

526.blender_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-finline-aggressive -mllvm -unroll-threshold=100 -lamdlibm
-lamdalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-finline-aggressive -faggressive-loop-transform -fvector-transform
-fscalar-transform -Mrecursive -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Inspur Corporation

SPECrate®2017_fp_base = 1390

Inspur NF5280A7 (AMD EPYC 9654)

SPECrate®2017_fp_peak = 1520

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Feb-2023

Hardware Availability: Feb-2023

Software Availability: Nov-2022

Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V2.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V2.0.xml>

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