



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

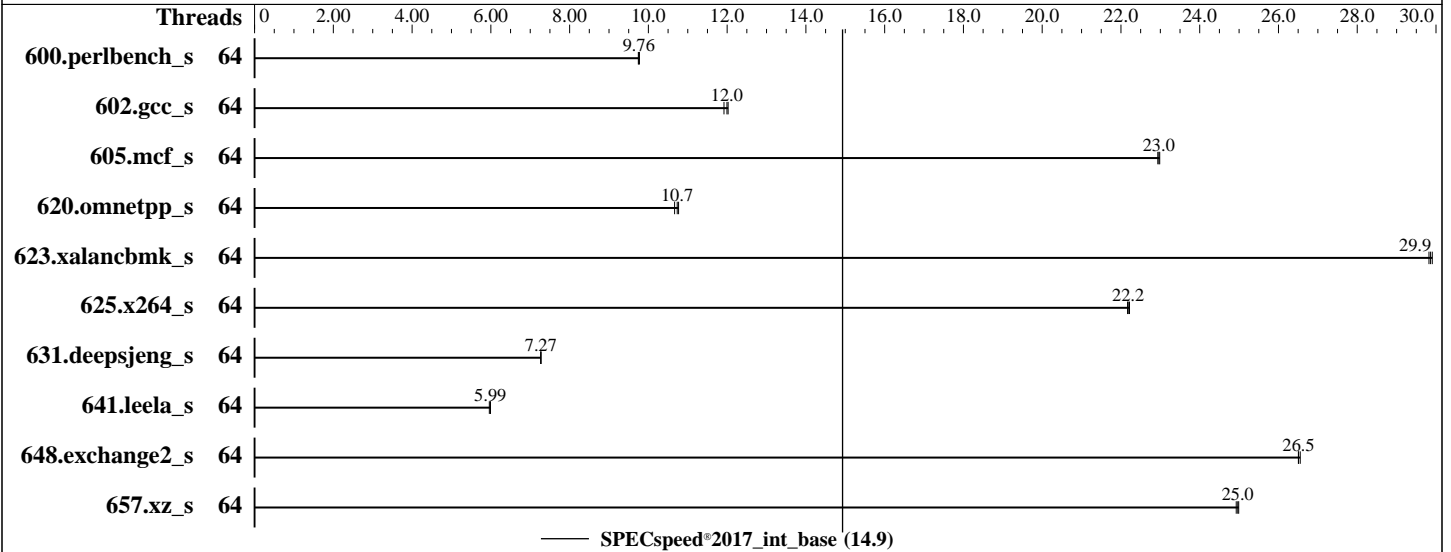
ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_base = 14.9

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Gold 5416S
Max MHz: 4000
Nominal: 2000
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 30 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R, running at 4400)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
Kernel 5.14.21-150400.22-default
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2023.0 of Intel Fortran Compiler Classic for Linux;
C/C++: Version 2023.0 of Intel C/C++ Compiler Classic for Linux
Parallel: Yes
Firmware: Lenovo BIOS Version ESE109L 1.10 released Jan-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_base = 14.9

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	64	182	9.77	<u>182</u>	<u>9.76</u>	182	9.75							
602.gcc_s	64	<u>332</u>	<u>12.0</u>	331	12.0	334	11.9							
605.mcf_s	64	<u>206</u>	<u>23.0</u>	206	22.9	205	23.0							
620.omnetpp_s	64	151	10.8	<u>152</u>	<u>10.7</u>	153	10.7							
623.xalancbmk_s	64	47.5	29.8	<u>47.5</u>	<u>29.9</u>	47.4	29.9							
625.x264_s	64	79.4	22.2	79.6	22.2	<u>79.5</u>	<u>22.2</u>							
631.deepsjeng_s	64	197	7.27	<u>197</u>	<u>7.27</u>	197	7.27							
641.leela_s	64	286	5.96	285	5.99	<u>285</u>	<u>5.99</u>							
648.exchange2_s	64	<u>111</u>	<u>26.5</u>	111	26.6	111	26.5							
657.xz_s	64	<u>248</u>	<u>25.0</u>	247	25.0	248	24.9							

SPECspeed®2017_int_base = 14.9

SPECspeed®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2023.0/lib/intel64:/home/cpu2017-1.1.9-ic2023.0/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

General Notes (Continued)

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

Operating Mode set to Custom Mode

CPU P-State Control set to Legacy

Sysinfo program /home/cpu2017-1.1.9-ic2023.0/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Mar 15 01:36:40 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022

Platform Notes (Continued)

```
01:36:40 up 1 min, 1 user, load average: 2.22, 1.01, 0.37
USER      TTY      FROM          LOGIN@      IDLE        JCPU        PCPU        WHAT
root      tty1    -             01:36      8.00s      0.83s      0.00s      -bash
```

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062622
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062622
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=32 --tune base -o all --define
intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=32 --tune base --output_format all --define
intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base --size
refspeed intspeed --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.074/templogs/preenv.intspeed.074.0.log --lognum 074.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017-1.1.9-ic2023.0

6. /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5416S
vendor_id : GenuineIntel
cpu family : 6
model : 143
stepping : 8
microcode : 0x2b000161
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores : 16
siblings : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-15
physical id 1: core ids 0-15

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022

Platform Notes (Continued)

physical id 0: apicids 0-31
physical id 1: apicids 128-159
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 64
On-line CPU(s) list:   0-63
Vendor ID:              GenuineIntel
Model name:             Intel(R) Xeon(R) Gold 5416S
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    16
Socket(s):              2
Stepping:               8
Frequency boost:        enabled
CPU max MHz:            2001.0000
CPU min MHz:            800.0000
BogoMIPS:               4000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                        tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil hle
                        avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                        tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization:         VT-x
L1d cache:              1.5 MiB (32 instances)
L1i cache:              1 MiB (32 instances)
L2 cache:               64 MiB (32 instances)
L3 cache:               60 MiB (2 instances)
NUMA node(s):          2
NUMA node0 CPU(s):     0-15,32-47
NUMA node1 CPU(s):     16-31,48-63
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:    Not affected

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

Platform Notes (Continued)

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	30M	60M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-15,32-47
node 0 size: 257702 MB
node 0 free: 256464 MB
node 1 cpus: 16-31,48-63
node 1 size: 257976 MB
node 1 free: 257355 MB
node distances:
node  0  1
  0: 10  21
  1: 21  10

```

9. /proc/meminfo

MemTotal: 528055792 kB

10. who -r

run-level 3 Mar 15 01:35

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

```

Default Target  Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
smartd sshd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged-switch-root ipmi ipmievdev issue-add-ssh-keys kexec-load
lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd
sapconf serial-getty@ smartd_generate_opts snmpd snmptrapd sysstat
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd tuned
indirect uidd wickedd

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=461ffbd6-8da0-4c20-adb7-d9d3143b6aa5
splash=silent
mitigations=auto
quiet

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022

Platform Notes (Continued)

security=apparmor

```

-----
14. cpupower frequency-info
    analyzing CPU 0:
        current policy: frequency should be within 800 MHz and 2.00 GHz.
                        The governor "performance" may decide which speed to use
                        within this range.
    boost state support:
        Supported: yes
        Active: yes
-----

```

```

-----
15. tuned-adm active
    It seems that tuned daemon is not running, preset profile is not activated.
    Preset profile: virtual-guest
-----

```

```

-----
16. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space     2
    vm.compaction_proactiveness    20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     10
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs     3000
    vm.dirty_ratio                 20
    vm.dirty_writeback_centisecs  500
    vm.dirtytime_expire_seconds   43200
    vm.extfrag_threshold           500
    vm.min_unmapped_ratio         1
    vm.nr_hugepages                0
    vm.nr_hugepages_mempolicy     0
    vm.nr_overcommit_hugepages    0
    vm.swappiness                  60
    vm.watermark_boost_factor     15000
    vm.watermark_scale_factor     10
    vm.zone_reclaim_mode          0
-----

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag          always defer defer+madvise [madvise] never
    enabled         [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force
-----

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                  1
    max_ptes_none          511
    max_ptes_shared        256
    max_ptes_swap           64
    pages_to_scan          4096
    scan_sleep_millisecs   10000
-----

```

```

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP4
-----

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_base = 14.9

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022

Platform Notes (Continued)

20. Disk information

SPEC is set to: /home/cpu2017-1.1.9-ic2023.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 889G 112G 777G 13% /

21. /sys/devices/virtual/dmideid

Vendor: Lenovo
Product: ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U
Product Family: ThinkSystem
Serial: 1234567890

22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800, configured at 4400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo
BIOS Version: ESE109L-1.10
BIOS Date: 01/07/2023
BIOS Revision: 1.10
Firmware Revision: 1.0

Compiler Version Notes

=====
C | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 648.exchange2_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.9

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V3
(2.00 GHz, Intel Xeon Gold 5416S)

SPECspeed®2017_int_base = 14.9

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Mar-2023

Hardware Availability: Feb-2023

Software Availability: Dec-2022

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-0.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-0.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-03-14 13:36:40-0400.

Report generated on 2024-01-29 17:30:46 by CPU2017 PDF formatter v6716.

Originally published on 2023-04-11.