



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.  
NF5280M7 (Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 829  
SPECrate®2017\_fp\_peak = 872

CPU2017 License: 3358

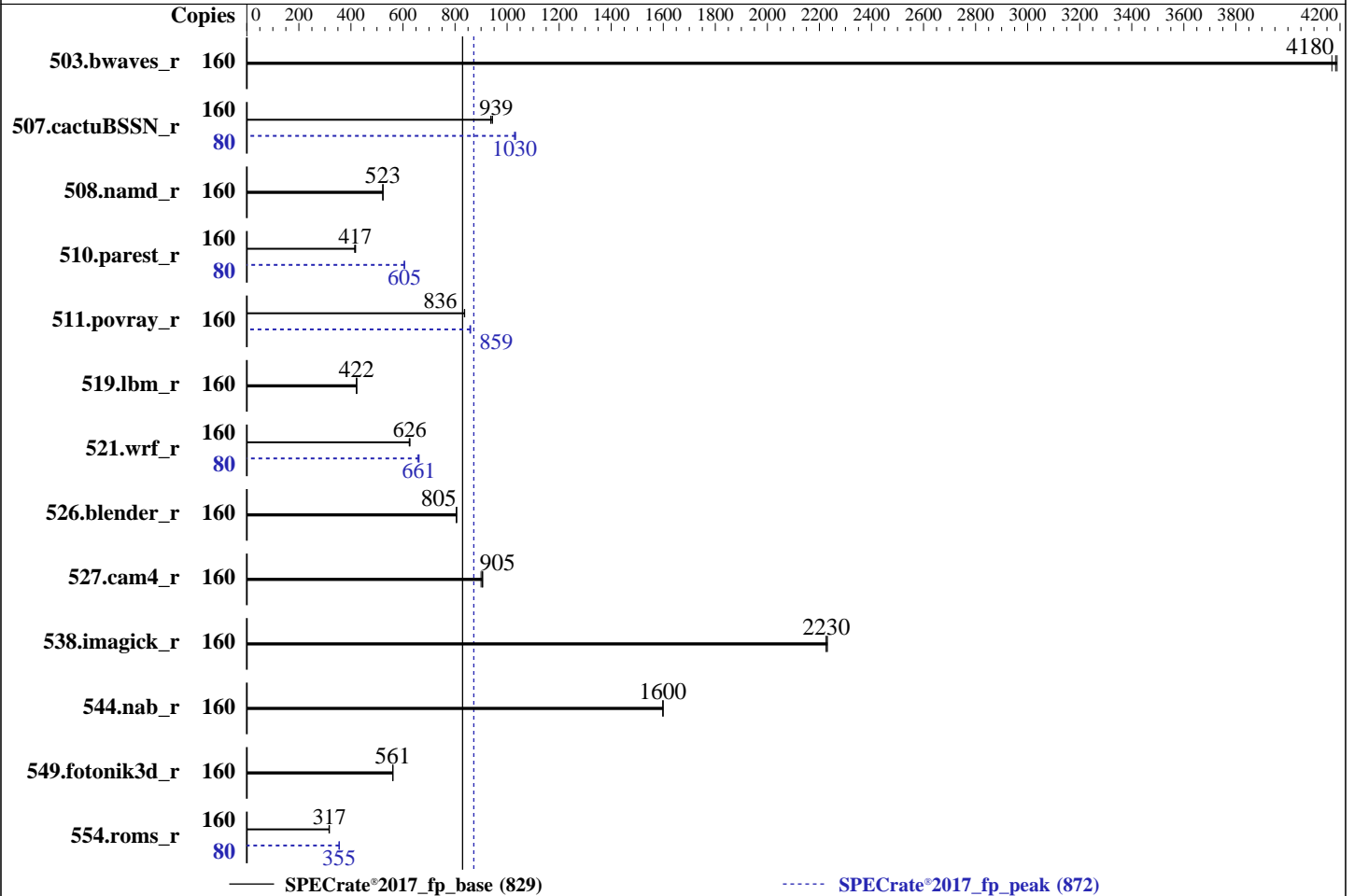
Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Jul-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022



## Hardware

CPU Name: Intel Xeon Platinum 8460Y+  
 Max MHz: 3700  
 Nominal: 2000  
 Enabled: 80 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 105 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 1 TB NVME SSD  
 Other: None

## Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.22.1.el9\_0.x86\_64  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 03.01.00 released Dec-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	160	385	4170	383	4190	<b>384</b>	<b>4180</b>	160	385	4170	383	4190	<b>384</b>	<b>4180</b>
507.cactuBSSN_r	160	215	944	<b>216</b>	<b>939</b>	216	937	80	98.4	1030	<b>98.4</b>	<b>1030</b>	98.1	1030
508.namd_r	160	291	523	291	522	<b>291</b>	<b>523</b>	160	291	523	291	522	<b>291</b>	<b>523</b>
510.parest_r	160	1011	414	<b>1004</b>	<b>417</b>	1001	418	80	345	606	346	605	<b>346</b>	<b>605</b>
511.povray_r	160	446	837	<b>447</b>	<b>836</b>	447	836	160	<b>435</b>	<b>859</b>	435	860	436	857
519.lbm_r	160	400	422	<b>400</b>	<b>422</b>	400	422	160	400	422	<b>400</b>	<b>422</b>	400	422
521.wrf_r	160	574	624	572	627	<b>573</b>	<b>626</b>	80	<b>271</b>	<b>661</b>	271	662	273	657
526.blender_r	160	<b>303</b>	<b>805</b>	303	805	302	806	160	<b>303</b>	<b>805</b>	303	805	302	806
527.cam4_r	160	<b>309</b>	<b>905</b>	311	900	309	907	160	<b>309</b>	<b>905</b>	311	900	309	907
538.imagick_r	160	178	2230	<b>179</b>	<b>2230</b>	179	2230	160	178	2230	<b>179</b>	<b>2230</b>	179	2230
544.nab_r	160	168	1600	168	1600	<b>168</b>	<b>1600</b>	160	168	1600	168	1600	<b>168</b>	<b>1600</b>
549.fotonik3d_r	160	1111	561	1112	561	<b>1111</b>	<b>561</b>	160	1111	561	1112	561	<b>1111</b>	<b>561</b>
554.roms_r	160	802	317	805	316	<b>802</b>	<b>317</b>	80	358	355	359	354	<b>358</b>	<b>355</b>

SPECrate®2017\_fp\_base = **829**

SPECrate®2017\_fp\_peak = **872**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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## General Notes (Continued)

is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
ENERGY\_PERF\_BIAS\_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
Sub NUMA Cluster (SNC) set to SNC4  
  
Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Mon Jul 3 16:14:55 2023  
  
SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 250 (250-6.el9\_0)
  12. Failed units, from systemctl list-units --state=failed
  13. Services, from systemctl list-unit-files
  14. Linux kernel boot-time arguments, from /proc/cmdline
  15. cpupower frequency-info
  16. tuned-adm active
  17. sysctl
  18. /sys/kernel/mm/transparent\_hugepage
  19. /sys/kernel/mm/transparent\_hugepage/khugepaged
  20. OS release
  21. Disk information
  22. /sys/devices/virtual/dmi/id
  23. dmidecode
  24. BIOS
- 
1. uname -a  
Linux localhost 5.14.0-70.22.1.el9\_0.x86\_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86\_64 x86\_64 x86\_64  
GNU/Linux

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## Platform Notes (Continued)

2. w

```
16:14:55 up 6:21, 1 user, load average: 105.27, 146.28, 153.97
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
root      ttyl    09:53   6:13m  0.89s  0.01s  sh
reportable-ic2023.0-lin-sapphirerapids-rate-smt-on-20221201.sh
```

3. Username

```
From environment variable $USER: root
```

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062127
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062127
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
sh reportable-ic2023.0-lin-sapphirerapids-rate-smt-on-20221201.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/temlogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Platinum 8460Y+
vendor_id       : GenuineIntel
cpu family      : 6
model           : 143
stepping        : 6
microcode       : 0x2b000130
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores       : 40
siblings        : 80
2 physical ids (chips)
160 processors (hardware threads)
```

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## Platform Notes (Continued)

physical id 0: core ids 0-39  
physical id 1: core ids 0-39  
physical id 0: apicids 0-79  
physical id 1: apicids 128-207

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 160
On-line CPU(s) list:   0-159
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) Platinum 8460Y+
BIOS Model name:       Intel(R) Xeon(R) Platinum 8460Y+
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    40
Socket(s):              2
Stepping:               6
CPU max MHz:            3700.0000
CPU min MHz:            800.0000
BogoMIPS:               4000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                        tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
                        rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                        avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                        cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni avx512_bf16
                        wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_llid arch_capabilities

L1d cache:             3.8 MiB (80 instances)
L1i cache:             2.5 MiB (80 instances)
L2 cache:              160 MiB (80 instances)
L3 cache:              210 MiB (2 instances)
NUMA node(s):          8
NUMA node0 CPU(s):    0-9,80-89
NUMA node1 CPU(s):    10-19,90-99
NUMA node2 CPU(s):    20-29,100-109
NUMA node3 CPU(s):    30-39,110-119
NUMA node4 CPU(s):    40-49,120-129
NUMA node5 CPU(s):    50-59,130-139
NUMA node6 CPU(s):    60-69,140-149
NUMA node7 CPU(s):    70-79,150-159

```

(Continued on next page)



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## Platform Notes (Continued)

Vulnerability Itlb multihit: Not affected  
 Vulnerability Lltf: Not affected  
 Vulnerability Mds: Not affected  
 Vulnerability Meltdown: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.8M	12	Data	1	64	1	64
L1i	32K	2.5M	8	Instruction	1	64	1	64
L2	2M	160M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-9,80-89
node 0 size: 64038 MB
node 0 free: 53124 MB
node 1 cpus: 10-19,90-99
node 1 size: 64507 MB
node 1 free: 55970 MB
node 2 cpus: 20-29,100-109
node 2 size: 64507 MB
node 2 free: 55963 MB
node 3 cpus: 30-39,110-119
node 3 size: 64507 MB
node 3 free: 55963 MB
node 4 cpus: 40-49,120-129
node 4 size: 64507 MB
node 4 free: 55982 MB
node 5 cpus: 50-59,130-139
node 5 size: 64507 MB
node 5 free: 55988 MB
node 6 cpus: 60-69,140-149
node 6 size: 64507 MB
node 6 free: 55693 MB
node 7 cpus: 70-79,150-159
node 7 size: 64487 MB
node 7 free: 55944 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 21 21 21 21
1:  12 10 12 12 21 21 21 21
2:  12 12 10 12 21 21 21 21
3:  12 12 12 10 21 21 21 21
4:  21 21 21 21 10 12 12 12
5:  21 21 21 21 12 10 12 12
6:  21 21 21 21 12 12 10 12
7:  21 21 21 21 12 12 12 10

```

9. /proc/meminfo

MemTotal: 527945724 kB

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## Platform Notes (Continued)

10. who -r  
run-level 3 Jul 3 09:53

11. Systemd service manager version: systemd 250 (250-6.el9\_0)  
Default Target Status  
multi-user degraded

12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* dnf-makecache.service loaded failed failed dnf makecache

13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled dbus-broker getty@ tuned udisks2 upower  
enabled-runtime systemd-remount-fs  
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd  
blk-availability canberra-system-bootup canberra-system-shutdown  
canberra-system-shutdown-reboot chronyd console-getty cpupower crond  
debug-shell firewalld irqbalance kdump kvm\_stat lvm2-monitor man-db-restart-cache-update  
mdmonitor microcode nftables nis-domainname rdisc rhsm rhsm-facts rhsmcertd rpmdb-rebuild  
rsyslog selinux-autorelabel-mark sep5 serial-getty@ sshd sshd-keygen@ sssd  
systemd-boot-check-no-failures systemd-network-generator systemd-pstore systemd-sysext  
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.22.1.el9\_0.x86\_64  
root=/dev/mapper/rhel-root  
ro  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap

15. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 3.70 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

16. tuned-adm active  
Current active profile: throughput-performance

17. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000

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## Platform Notes (Continued)

```

vm.dirty_ratio          40
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold    500
vm.min_unmapped_ratio   1
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness            10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode    0

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
20. OS release
From /etc/*-release /etc/*-version
os-release          Red Hat Enterprise Linux 9.0 (Plow)
redhat-release      Red Hat Enterprise Linux release 9.0 (Plow)
system-release      Red Hat Enterprise Linux release 9.0 (Plow)

```

```

-----
21. Disk information
SPEC is set to: /home/CPU2017
Filesystem          Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G 197G 623G 24% /home

```

```

-----
22. /sys/devices/virtual/dmi/id
Vendor:             IEIT
Product:            NF5280M7
Product Family:    Not specified
Serial:             000000000

```

```

-----
23. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
16x Samsung M321R4GA3BB6-CQKVG 32 GB 2 rank 4800

```

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## Platform Notes (Continued)

24. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 03.01.00  
BIOS Date: 12/29/2022

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.  
NF5280M7 (Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 829  
SPECrate®2017\_fp\_peak = 872

CPU2017 License: 3358

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Jul-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

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Test Date: Jul-2023

Hardware Availability: Apr-2023

Software Availability: Dec-2022

## Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
521.wrf_r: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.xml>



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**Software Availability:** Dec-2022

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