



# SPEC CPU®2017 Floating Point Speed Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380a Gen11

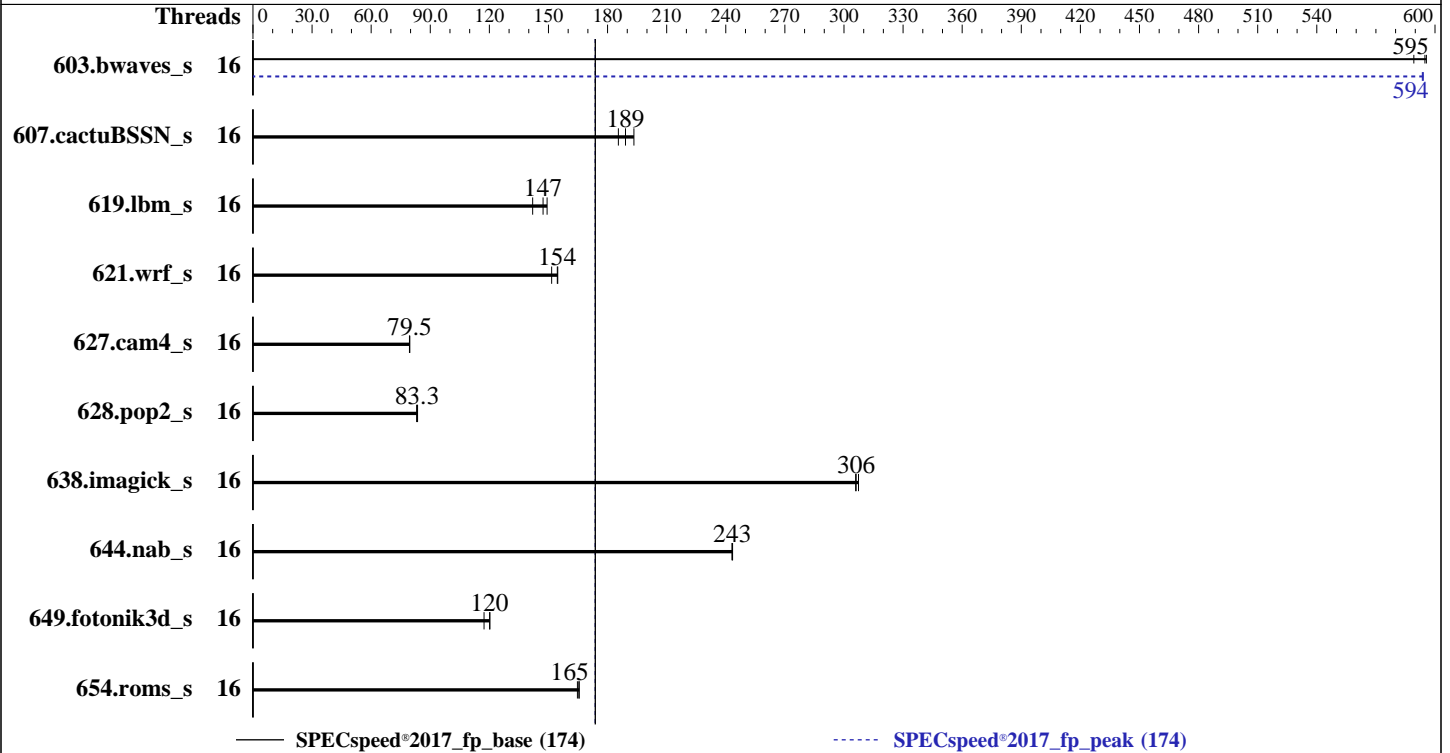
(3.70 GHz, Intel Xeon Gold 6434)

SPECspeed®2017\_fp\_base = 174

SPECspeed®2017\_fp\_peak = 174

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jul-2023  
Hardware Availability: Jun-2023  
Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 6434  
Max MHz: 4100  
Nominal: 3700  
Enabled: 16 cores, 2 chips  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 22.5 MB I+D on chip per chip  
Other: None  
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)  
Storage: 1 x 1.6 TB NVMe SSD  
Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
Kernel 5.14.0-70.13.1.el9\_0.x86\_64  
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: HPE BIOS Version v1.40 06/1/2023 released Jun-2023  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	99.1	596	<b><u>99.2</u></b>	<b><u>595</u></b>	100	589	16	<b><u>99.4</u></b>	<b><u>594</u></b>	99.4	594	99.3	594
607.cactuBSSN_s	16	86.2	193	89.9	185	<b><u>88.2</u></b>	<b><u>189</u></b>	16	86.2	193	89.9	185	<b><u>88.2</u></b>	<b><u>189</u></b>
619.lbm_s	16	<b><u>35.6</u></b>	<b><u>147</u></b>	36.9	142	35.1	149	16	<b><u>35.6</u></b>	<b><u>147</u></b>	36.9	142	35.1	149
621.wrf_s	16	<b><u>85.6</u></b>	<b><u>154</u></b>	85.5	155	87.2	152	16	<b><u>85.6</u></b>	<b><u>154</u></b>	85.5	155	87.2	152
627.cam4_s	16	<b><u>111</u></b>	<b><u>79.5</u></b>	112	79.4	111	79.6	16	<b><u>111</u></b>	<b><u>79.5</u></b>	112	79.4	111	79.6
628.pop2_s	16	<b><u>142</u></b>	<b><u>83.3</u></b>	142	83.6	143	83.1	16	<b><u>142</u></b>	<b><u>83.3</u></b>	142	83.6	143	83.1
638.imagick_s	16	46.9	307	<b><u>47.1</u></b>	<b><u>306</u></b>	47.2	306	16	46.9	307	<b><u>47.1</u></b>	<b><u>306</u></b>	47.2	306
644.nab_s	16	71.9	243	<b><u>71.8</u></b>	<b><u>243</u></b>	71.8	243	16	71.9	243	<b><u>71.8</u></b>	<b><u>243</u></b>	71.8	243
649.fotonik3d_s	16	<b><u>75.9</u></b>	<b><u>120</u></b>	77.7	117	75.8	120	16	<b><u>75.9</u></b>	<b><u>120</u></b>	77.7	117	75.8	120
654.roms_s	16	95.6	165	<b><u>95.2</u></b>	<b><u>165</u></b>	95.1	166	16	95.6	165	<b><u>95.2</u></b>	<b><u>165</u></b>	95.1	166

SPECspeed®2017\_fp\_base = 174

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
IRQ balance service was stopped using "systemctl stop irqbalance.service"
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
perf-bias for all the CPUs is set using "cpupower set -b 0"
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```



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## Platform Notes

The system ROM used for this result contains Intel microcode version 0x2b000461 for the Intel Xeon Gold 6434 Processor

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute  
Thermal Configuration set to Maximum Cooling  
Intel Hyper-Threading set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Last Level Cache (LLC) Prefetch set to Enabled  
Last Level Cache (LLC) Dead Line Allocation set to Disabled  
Enhanced Processor Performance Profile set to Aggressive  
Dead Block Predictor set to Enabled  
Workload Profile set to Custom  
Intel DMI Link Frequency set to Gen2 Speed  
Adjacent Sector Prefetch set to Disabled  
Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Sun Jul 2 02:20:01 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.e19\_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----  
1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.e19\_0.x86\_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86\_64  
x86\_64 x86\_64 GNU/Linux  
-----

-----  
2. w  
02:20:01 up 0 min, 0 users, load average: 0.11, 0.03, 0.01  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
-----

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## Platform Notes (Continued)

-----  
3. Username

From environment variable \$USER: root

-----  
4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062795
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062795
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

-----  
5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fpspeed.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak -o all --define
drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak --output_format all
--define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.004/templogs/preenv.fpspeed.004.0.log --lognum 004.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

-----  
6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6434
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000461
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 8
siblings       : 8
2 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0,2,4,6,8,10,12,14
physical id 1: apicids 128,130,132,134,136,138,140,142
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for

(Continued on next page)



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## Platform Notes (Continued)

virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:   0-15
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel(R) Corporation
Model name:            Intel(R) Xeon(R) Gold 6434
BIOS Model name:      Intel(R) Xeon(R) Gold 6434
CPU family:            6
Model:                 143
Thread(s) per core:    1
Core(s) per socket:    8
Socket(s):             2
Stepping:              8
BogoMIPS:              7400.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1
                        avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                        tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Lld cache:             768 KiB (16 instances)
Lli cache:             512 KiB (16 instances)
L2 cache:              32 MiB (16 instances)
L3 cache:              45 MiB (2 instances)
NUMA node(s):         2
NUMA node0 CPU(s):    0-3,8-11
NUMA node1 CPU(s):    4-7,12-15
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
Lld	48K	768K	12	Data	1	64	1	64
Lli	32K	512K	8	Instruction	1	64	1	64

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## Platform Notes (Continued)

L2	2M	32M	16 Unified	2	2048	1	64
L3	22.5M	45M	15 Unified	3	24576	1	64

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-3,8-11
node 0 size: 257739 MB
node 0 free: 257313 MB
node 1 cpus: 4-7,12-15
node 1 size: 257998 MB
node 1 free: 257347 MB
node distances:
node  0  1
  0: 10 20
  1: 20 10

```

### 9. /proc/meminfo

MemTotal: 528116364 kB

### 10. who -r

run-level 3 Jul 2 02:19

### 11. Systemd service manager version: systemd 250 (250-6.el9\_0)

Default Target	Status
multi-user	running

### 12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd systemd-network-generator tuned udisks2
enabled-runtime	systemd-remount-fs
disabled	blk-availability chrony-wait chronyd console-getty cpupower debug-shell kvm_stat man-db-restart-cache-update nftables powertop rdisc rhsm rhsm-facts rpmdb-rebuild serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex indirect
indirect	sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

### 13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

```

### 14. cpupower frequency-info

```

analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

```

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## Platform Notes (Continued)

-----  
15. tuned-adm active  
Current active profile: throughput-performance

-----  
16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 40  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 10  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvice [madvice] never  
enabled [always] madvice never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.0 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)  
system-release Red Hat Enterprise Linux release 9.0 (Plow)

-----  
20. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 1.4T 153G 1.3T 11% /home

-----  
21. /sys/devices/virtual/dmi/id  
Vendor: HPE

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## Platform Notes (Continued)

Product: ProLiant DL380a Gen11  
Product Family: ProLiant  
Serial: CNX22602MZ

### 22. dmidecode

Additional information from dmidecode 3.3 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

7x Hynix HMC88AEBRA168N 32 GB 2 rank 4800  
6x Hynix HMC88MEBRA113N 32 GB 2 rank 4800  
3x Hynix HMC88MEBRA115N 32 GB 2 rank 4800

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 1.40  
BIOS Date: 06/01/2023  
BIOS Revision: 1.40  
Firmware Revision: 1.30

## Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
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**Hardware Availability:** Jun-2023  
**Software Availability:** Dec-2022

## Compiler Version Notes (Continued)

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## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(3.70 GHz, Intel Xeon Gold 6434)

SPECspeed®2017\_fp\_base = 174

SPECspeed®2017\_fp\_peak = 174

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2023  
**Hardware Availability:** Jun-2023  
**Software Availability:** Dec-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte  
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:  
icx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(3.70 GHz, Intel Xeon Gold 6434)

SPECspeed®2017\_fp\_base = 174

SPECspeed®2017\_fp\_peak = 174

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2023  
**Hardware Availability:** Jun-2023  
**Software Availability:** Dec-2022

## Peak Optimization Flags (Continued)

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

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