



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SR860 V3  
(3.70 GHz, Intel Xeon Gold 6434H)

SPECrate®2017\_fp\_base = 581

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9017

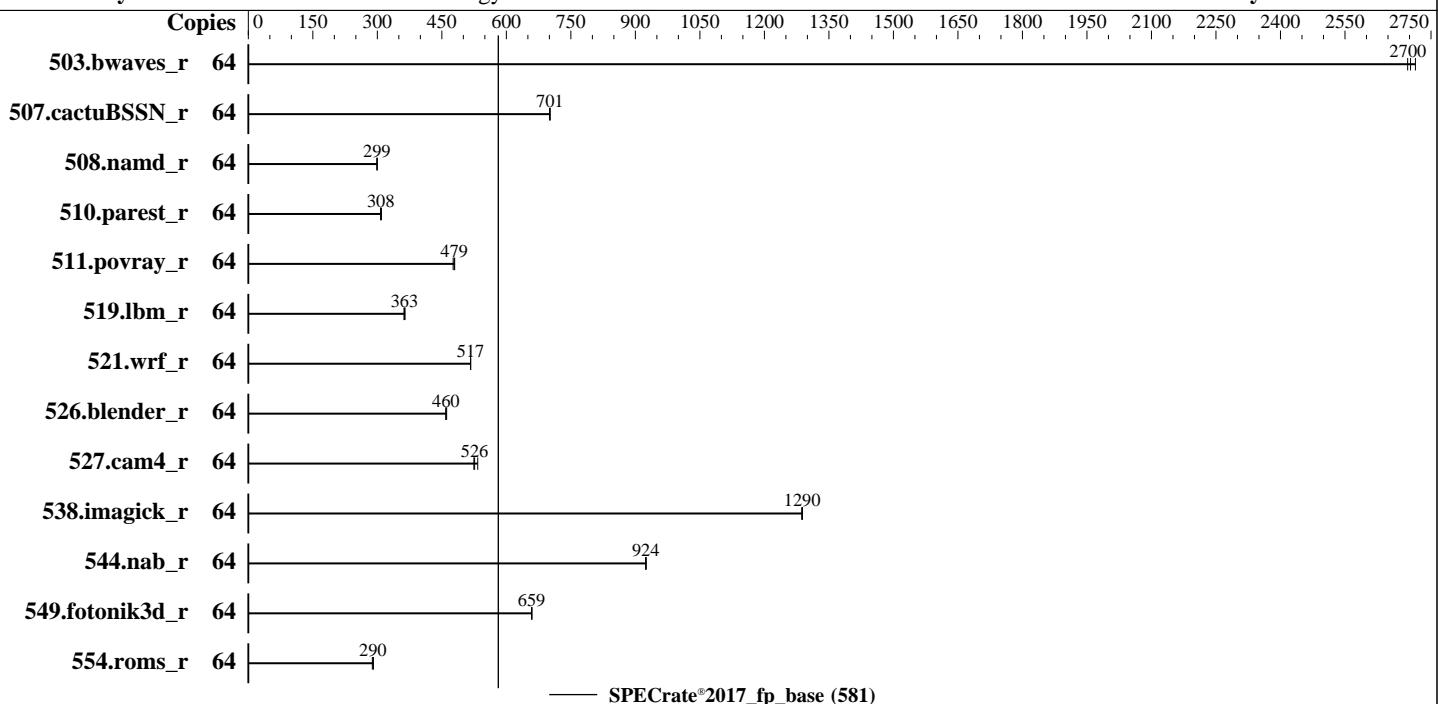
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2023

Hardware Availability: Jun-2023

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 6434H  
Max MHz: 4100  
Nominal: 3700  
Enabled: 32 cores, 4 chips, 2 threads/core  
Orderable: 2,4 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 22.5 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (32 x 32 GB 2Rx8 PC5-4800B-R)  
Storage: 1 x 480 GB SATA SSD  
Other: None

### OS:

SUSE Linux Enterprise Server 15 SP4 (x86\_64)  
Kernel 5.14.21-150400.22-default

### Compiler:

C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;

### Parallel:

No

### Firmware:

Lenovo BIOS Version RSE105E 1.10 released May-2023

### File System:

xfs

### System State:

Run level 3 (multi-user)

### Base Pointers:

64-bit

### Peak Pointers:

Not Applicable

### Other:

jemalloc memory allocator V5.0.1

### Power Management:

BIOS and OS set to prefer performance at the cost of additional power usage

### Software



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	236	2710	<b><u>237</u></b>	<b><u>2700</u></b>	238	2700							
507.cactusBSSN_r	64	115	702	<b><u>116</u></b>	<b><u>701</u></b>	116	700							
508.namd_r	64	203	299	203	299	<b><u>203</u></b>	<b><u>299</u></b>							
510.parest_r	64	541	309	<b><u>543</u></b>	<b><u>308</u></b>	544	308							
511.povray_r	64	<b><u>312</u></b>	<b><u>479</u></b>	311	480	314	476							
519.lbm_r	64	186	362	<b><u>186</u></b>	<b><u>363</u></b>	185	364							
521.wrf_r	64	277	517	277	517	<b><u>277</u></b>	<b><u>517</u></b>							
526.blender_r	64	212	459	211	461	<b><u>212</u></b>	<b><u>460</u></b>							
527.cam4_r	64	210	533	<b><u>213</u></b>	<b><u>526</u></b>	213	524							
538.imagick_r	64	124	1290	<b><u>124</u></b>	<b><u>1290</u></b>	124	1290							
544.nab_r	64	<b><u>117</u></b>	<b><u>924</u></b>	116	925	117	924							
549.fotonik3d_r	64	379	659	<b><u>379</u></b>	<b><u>659</u></b>	378	660							
554.roms_r	64	353	288	350	291	<b><u>351</u></b>	<b><u>290</u></b>							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017-1.1.9-ic2023.0/lib/intel64:/home/cpu2017-1.1.9-ic2023.0/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

LLC Prefetch set to Disabled

DCU Streamer Prefetcher set to Disabled

SNC set to SNC2

C-State set to Legacy

AMP Prefetch set to Enable

```
Sysinfo program /home/cpu2017-1.1.9-ic2023.0/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Aug 24 18:18:29 2023
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

-----  
1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. sysctl  
16. /sys/kernel/mm/transparent\_hugepage  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS  
-----

-----  
1. uname -a  
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT\_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)  
x86\_64 x86\_64 x86\_64 GNU/Linux

-----  
2. w  
18:18:29 up 1 min, 1 user, load average: 0.08, 0.05, 0.01

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## Platform Notes (Continued)

USER	TTY	FROM	LOGIN@	IDLE	JCPU	PCPU	WHAT
root	tty1	-	18:18	13.00s	0.80s	0.00s	-bash

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

core file size	(blocks, -c) unlimited
data seg size	(kbytes, -d) unlimited
scheduling priority	(-e) 0
file size	(blocks, -f) unlimited
pending signals	(-i) 4126993
max locked memory	(kbytes, -l) 64
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 4126993
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

### 5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 -c
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=32 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 --configfile
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=32 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
  rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.101/templogs/preenv.fprate.101.0.log --lognum 101.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.0
```

### 6. /proc/cpuinfo

model name	: Intel(R) Xeon(R) Gold 6434H
vendor_id	: GenuineIntel
cpu family	: 6
model	: 143
stepping	: 8
microcode	: 0x2b0001b0
bugs	: spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores	: 8
siblings	: 16
4 physical ids (chips)	
64 processors (hardware threads)	
physical id 0: core ids 0-7	
physical id 1: core ids 0-7	
physical id 2: core ids 0-7	
physical id 3: core ids 0-7	

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## Platform Notes (Continued)

```
physical id 0: apicids 0-15
physical id 1: apicids 128-143
physical id 2: apicids 256-271
physical id 3: apicids 384-399
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

```
From lscpu from util-linux 2.37.2:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Gold 6434H
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 4
Stepping: 8
BogoMIPS: 7400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf fmpf perf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced
       tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
       avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
       cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
       arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
       vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocpndq la57 rdpid
       bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
       tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 1.5 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 64 MiB (32 instances)
L3 cache: 90 MiB (4 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-3,32-35
NUMA node1 CPU(s): 4-7,36-39
NUMA node2 CPU(s): 8-11,40-43
NUMA node3 CPU(s): 12-15,44-47
NUMA node4 CPU(s): 16-19,48-51
NUMA node5 CPU(s): 20-23,52-55
NUMA node6 CPU(s): 24-27,56-59
NUMA node7 CPU(s): 28-31,60-63
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
```

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## Platform Notes (Continued)

Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	22.5M	90M	15	Unified	3	24576	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-3,32-35

node 0 size: 128683 MB

node 0 free: 127991 MB

node 1 cpus: 4-7,36-39

node 1 size: 129021 MB

node 1 free: 128845 MB

node 2 cpus: 8-11,40-43

node 2 size: 129021 MB

node 2 free: 128699 MB

node 3 cpus: 12-15,44-47

node 3 size: 128987 MB

node 3 free: 128634 MB

node 4 cpus: 16-19,48-51

node 4 size: 129021 MB

node 4 free: 128796 MB

node 5 cpus: 20-23,52-55

node 5 size: 129021 MB

node 5 free: 128778 MB

node 6 cpus: 24-27,56-59

node 6 size: 129021 MB

node 6 free: 128855 MB

node 7 cpus: 28-31,60-63

node 7 size: 128992 MB

node 7 free: 128802 MB

node distances:

node	0	1	2	3	4	5	6	7
0:	10	12	21	21	21	21	21	21
1:	12	10	21	21	21	21	21	21
2:	21	21	10	12	21	21	21	21
3:	21	21	12	10	21	21	21	21
4:	21	21	21	21	10	12	21	21
5:	21	21	21	21	12	10	21	21
6:	21	21	21	21	21	21	10	12
7:	21	21	21	21	21	21	12	10

-----  
9. /proc/meminfo

MemTotal: 1056534880 kB

-----  
10. who -r

run-level 3 Aug 24 18:17

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## Platform Notes (Continued)

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance  
issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog  
smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chrony console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd issue-add-ssh-keys kexec-load  
lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd  
serial-getty@ smartd\_generate\_opts snmpd snmptrapd systemd-boot-check-no-failures  
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd  
indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default  
root=UUID=07b494b8-a782-4eba-84f2-ef5cae789da8  
splash=silent  
mitigations=auto  
quiet  
security=apparmor

14. cpupower frequency-info  
analyzing CPU 0:  
    Unable to determine current policy  
    boost state support:  
        Supported: yes  
        Active: yes

15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 60  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

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## Platform Notes (Continued)

-----  
16. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvise [madvise] never  
enabled [always] madvise never  
hugepage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP4

-----  
19. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2023.0  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda3 xfs 445G 12G 433G 3% /

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SR860 V3  
Product Family: ThinkSystem  
Serial: None

-----  
21. dmidecode  
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
19x Samsung M321R4GA3BB0-CQKDG 32 GB 2 rank 4800  
6x Samsung M321R4GA3BB0-CQKEG 32 GB 2 rank 4800  
4x Samsung M321R4GA3BB0-CQKMG 32 GB 2 rank 4800  
3x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: RSE105E-1.10  
BIOS Date: 05/12/2023  
BIOS Revision: 1.10  
Firmware Revision: 1.10



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## Compiler Version Notes

```
=====
C           | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

```
=====
C++          | 508.namd_r(base) 510.parest_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

```
=====
C++, C       | 511.povray_r(base) 526.blender_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

```
=====
C++, C, Fortran | 507.cactuBSSN_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

```
=====
Fortran      | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----
```

```
=====
Fortran, C   | 521.wrf_r(base) 527.cam4_r(base)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
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-----
```

## Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SR860 V3  
(3.70 GHz, Intel Xeon Gold 6434H)

SPECrate®2017\_fp\_base = 581

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jun-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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Test Sponsor: Lenovo Global Technology

Hardware Availability: Jun-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -fsto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-fsto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -fsto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -fsto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-X.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-X.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

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