



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## IEIT Systems Co., Ltd. i24A7 (AMD EPYC 9654)

SPECrate®2017\_int\_base = 890

SPECrate®2017\_int\_peak = 954

CPU2017 License: 3358

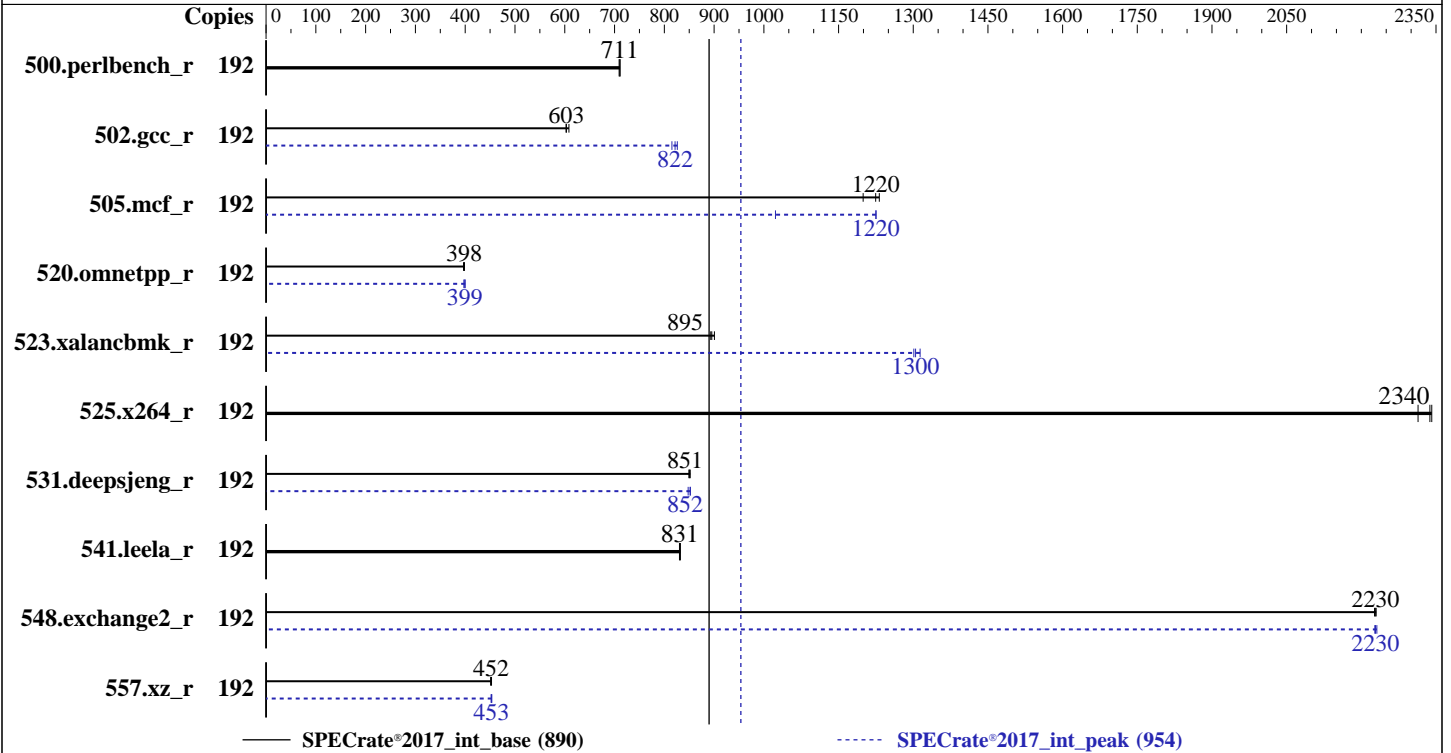
Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2023

Hardware Availability: Dec-2023

Software Availability: Dec-2022



### Hardware

CPU Name: AMD EPYC 9654  
 Max MHz: 3700  
 Nominal: 2400  
 Enabled: 96 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 1 TB NVME SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.22.1.el9\_0.x86\_64  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 03.09.01 released Sep-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base   |            |             |            |             |            |             | Peak   |            |             |            |             |            |             |
|-----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
|                 | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       |
| 500.perlbench_r | 192    | 430        | 711         | <b>430</b> | <u>711</u>  | 431        | 709         | 192    | 430        | 711         | <b>430</b> | <u>711</u>  | 431        | 709         |
| 502.gcc_r       | 192    | 447        | 608         | <b>451</b> | <u>603</u>  | 451        | 603         | 192    | 333        | 815         | 329        | 826         | <b>331</b> | <u>822</u>  |
| 505.mcf_r       | 192    | 252        | 1230        | <b>253</b> | <u>1220</u> | 259        | 1200        | 192    | 303        | 1020        | <b>253</b> | <u>1220</u> | 253        | 1230        |
| 520.omnetpp_r   | 192    | 635        | 397         | <b>633</b> | <u>398</u>  | 632        | 399         | 192    | <b>631</b> | <u>399</u>  | 634        | 397         | 630        | 400         |
| 523.xalancbmk_r | 192    | 227        | 893         | 225        | 901         | <b>226</b> | <u>895</u>  | 192    | <b>155</b> | <u>1300</u> | 154        | 1310        | 156        | 1300        |
| 525.x264_r      | 192    | 144        | 2340        | 145        | 2310        | <b>144</b> | <u>2340</u> | 192    | 144        | 2340        | 145        | 2310        | <b>144</b> | <u>2340</u> |
| 531.deepsjeng_r | 192    | <b>259</b> | <u>851</u>  | 259        | 849         | 258        | 852         | 192    | 260        | 848         | 258        | 852         | <b>258</b> | <u>852</u>  |
| 541.leela_r     | 192    | 382        | 832         | 383        | 830         | <b>382</b> | <u>831</u>  | 192    | 382        | 832         | 383        | 830         | <b>382</b> | <u>831</u>  |
| 548.exchange2_r | 192    | <b>226</b> | <u>2230</u> | 226        | 2230        | 226        | 2230        | 192    | 226        | 2230        | 225        | 2230        | <b>226</b> | <u>2230</u> |
| 557.xz_r        | 192    | 458        | 453         | <b>459</b> | <u>452</u>  | 460        | 451         | 192    | <b>458</b> | <u>453</u>  | 457        | 453         | 459        | 452         |

SPECrate®2017\_int\_base = **890**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

System date was wrongly set. The actual date is Oct-2023

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and

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## Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/CPU2017/amd_rate_aocc400_genoa_B_lib/lib:/home/CPU2017/amd_rate_aocc400_genoa_B_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

```
MALLOC_CONF = "thp:never"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

```
SVM Mode = disable  
DRAM Scrub time = disable  
NUMA nodes per socket = NPS4  
Determinism Slider = Power  
cTDP = 400  
Package Power Limit = 400
```

```
Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Wed Apr 6 20:00:39 2022
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9\_0)

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## Platform Notes (Continued)

- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. systemctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
-----
1. uname -a
Linux localhost.localdomain 5.14.0-70.22.1.el9_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
-----
```

```
-----
2. w
20:00:39 up 0 min,  1 user,  load average: 1.32, 0.42, 0.14
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
root      tty1    20:00   15.00s  1.37s  0.17s  /bin/bash ./amd_rate_aocc400_genoa_B1.sh
-----
```

```
-----
3. Username
From environment variable $USER:  root
-----
```

```
-----
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size                (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                    (blocks, -f) unlimited
pending signals              (-i) 1546215
max locked memory            (kbytes, -l) 2097152
max memory size              (kbytes, -m) unlimited
open files                   (-n) 1024
pipe size                    (512 bytes, -p) 8
POSIX message queues         (bytes, -q) 819200
real-time priority           (-r) 0
stack size                   (kbytes, -s) unlimited
cpu time                     (seconds, -t) unlimited
max user processes           (-u) 1546215
virtual memory                (kbytes, -v) unlimited
file locks                   (-x) unlimited
-----
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 27
login -- root
-bash
python3 ./run_amd_rate_aocc400_genoa_B1.py
/bin/bash ./amd_rate_aocc400_genoa_B1.sh
runcpu --config amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower --runmode
rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.010/templogs/preenv.intrate.010.0.log --lognum 010.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
-----
```

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### Platform Notes (Continued)

\$SPEC = /home/CPU2017

```

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9654 96-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa10113e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 96
siblings      : 192
1 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-95
physical id 0: apicids 0-191

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 192
On-line CPU(s) list:   0-191
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9654 96-Core Processor
BIOS Model name:       AMD EPYC 9654 96-Core Processor
CPU family:             25
Model:                  17
Thread(s) per core:    2
Core(s) per socket:    96
Socket(s):              1
Stepping:               1
Frequency boost:        enabled
CPU max MHz:            3707.8120
CPU min MHz:            1500.0000
BogoMIPS:               4792.73
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
                        pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                        abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
                        perfctr_core perfctr_nb bpeext perfctr_llc mwaitx cpb cat_l3 cdp_l3
                        invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
                        avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
                        svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                        pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg

```

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### Platform Notes (Continued)

```

Virtualization: avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_llid
AMD-V
L1d cache: 3 MiB (96 instances)
L1i cache: 3 MiB (96 instances)
L2 cache: 96 MiB (96 instances)
L3 cache: 384 MiB (12 instances)
NUMA node(s): 12
NUMA node0 CPU(s): 0-7,96-103
NUMA node1 CPU(s): 8-15,104-111
NUMA node2 CPU(s): 16-23,112-119
NUMA node3 CPU(s): 24-31,120-127
NUMA node4 CPU(s): 32-39,128-135
NUMA node5 CPU(s): 40-47,136-143
NUMA node6 CPU(s): 48-55,144-151
NUMA node7 CPU(s): 56-63,152-159
NUMA node8 CPU(s): 64-71,160-167
NUMA node9 CPU(s): 72-79,168-175
NUMA node10 CPU(s): 80-87,176-183
NUMA node11 CPU(s): 88-95,184-191
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 32K 3M 8 Data 1 64 1 64
L1i 32K 3M 8 Instruction 1 64 1 64
L2 1M 96M 8 Unified 2 2048 1 64
L3 32M 384M 16 Unified 3 32768 1 64

```

```

-----
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 12 nodes (0-11)
node 0 cpus: 0-7,96-103
node 0 size: 32046 MB
node 0 free: 31087 MB
node 1 cpus: 8-15,104-111
node 1 size: 32246 MB
node 1 free: 31862 MB
node 2 cpus: 16-23,112-119
node 2 size: 32246 MB
node 2 free: 31909 MB
node 3 cpus: 24-31,120-127
node 3 size: 32246 MB
node 3 free: 31647 MB
node 4 cpus: 32-39,128-135
node 4 size: 32210 MB
node 4 free: 31709 MB
node 5 cpus: 40-47,136-143
node 5 size: 32246 MB
node 5 free: 31784 MB
node 6 cpus: 48-55,144-151

```

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## Platform Notes (Continued)

```

node 6 size: 32246 MB
node 6 free: 31899 MB
node 7 cpus: 56-63,152-159
node 7 size: 32246 MB
node 7 free: 31918 MB
node 8 cpus: 64-71,160-167
node 8 size: 32247 MB
node 8 free: 31917 MB
node 9 cpus: 72-79,168-175
node 9 size: 32247 MB
node 9 free: 31917 MB
node 10 cpus: 80-87,176-183
node 10 size: 32247 MB
node 10 free: 31891 MB
node 11 cpus: 88-95,184-191
node 11 size: 32178 MB
node 11 free: 31839 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11
0: 10 11 11 12 12 12 12 12 12 12 12 12
1: 11 10 11 12 12 12 12 12 12 12 12 12
2: 11 11 10 12 12 12 12 12 12 12 12 12
3: 12 12 12 10 11 11 12 12 12 12 12 12
4: 12 12 12 11 10 11 12 12 12 12 12 12
5: 12 12 12 12 11 11 10 12 12 12 12 12
6: 12 12 12 12 12 12 10 11 11 12 12 12
7: 12 12 12 12 12 12 11 10 11 12 12 12
8: 12 12 12 12 12 12 11 11 10 12 12 12
9: 12 12 12 12 12 12 12 12 12 10 11 11
10: 12 12 12 12 12 12 12 12 12 11 10 11
11: 12 12 12 12 12 12 12 12 12 11 11 10

```

```

9. /proc/meminfo
MemTotal:      395938348 kB

```

```

10. who -r
run-level 3 Apr 6 20:00

```

```

11. Systemd service manager version: systemd 250 (250-6.el9_0)
Default Target Status
multi-user      running

```

```

12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
               dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
               nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
               systemd-network-generator udisks2 upower
enabled-runtime systemd-remount-fs
disabled       blk-availability canberra-system-bootup canberra-system-shutdown
               canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell kvm_stat
               man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmdm-rebuild serial-getty@
               sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect       sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

```

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## Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.22.1.el9\_0.x86\_64  
root=/dev/mapper/rhel-root  
ro  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap

-----  
14. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 1.50 GHz and 2.40 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 2400MHz

-----  
15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 1  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 1

-----  
16. /sys/kernel/mm/transparent\_hugepage  
defrag [always] defer defer+madvise madvise never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swapped 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

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## Platform Notes (Continued)

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.0 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)  
system-release Red Hat Enterprise Linux release 9.0 (Plow)  
-----

19. Disk information  
SPEC is set to: /home/CPU2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 819G 13G 806G 2% /home  
-----

20. /sys/devices/virtual/dmi/id  
Product: NS3260A7  
Product Family: Not specified  
-----

21. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
12x Micron MTC20F2085S1RC48BA1 32 GB 2 rank 4800  
-----

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 03.09.01  
BIOS Date: 09/23/2023  
-----

## Compiler Version Notes

=====  
C | 502.gcc\_r(peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 502.gcc\_r(peak)  
-----

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# SPEC CPU®2017 Integer Rate Result

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**IEIT Systems Co., Ltd.**  
**i24A7 (AMD EPYC 9654)**

**SPECrate®2017\_int\_base = 890**

**SPECrate®2017\_int\_peak = 954**

**CPU2017 License:** 3358  
**Test Sponsor:** IEIT Systems Co., Ltd.  
**Tested by:** IEIT Systems Co., Ltd.

**Test Date:** Oct-2023  
**Hardware Availability:** Dec-2023  
**Software Availability:** Dec-2022

## Compiler Version Notes (Continued)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

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CPU2017 License: 3358  
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Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2023  
Hardware Availability: Dec-2023  
Software Availability: Dec-2022

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc

C++ benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3

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Test Date: Oct-2023

Hardware Availability: Dec-2023

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



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**Test Date:** Oct-2023  
**Hardware Availability:** Dec-2023  
**Software Availability:** Dec-2022

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

```
502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

```
505.mcf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc
```

525.x264\_r: basepeak = yes

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

```
520.omnetpp_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
```

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## Peak Optimization Flags (Continued)

520.omnetpp\_r (continued):

```
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

```
523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdalloc-ext
```

```
531.deepsjeng_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

541.leela\_r: basepeak = yes

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

## Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

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## Peak Other Flags (Continued)

C++ benchmarks (except as noted below):

-Wno-unused-command-line-argument

523.xalancbmk\_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v118/aocc4/b1/rate/amd\_rate\_aocc400\_genoa\_B\_lib/lib32

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V3.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V3.1.xml>

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