



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECspeed®2017_int_base = 14.3

NF5466M7 (Intel Xeon Silver 4510)

SPECspeed®2017_int_peak = 14.6

CPU2017 License: 3358

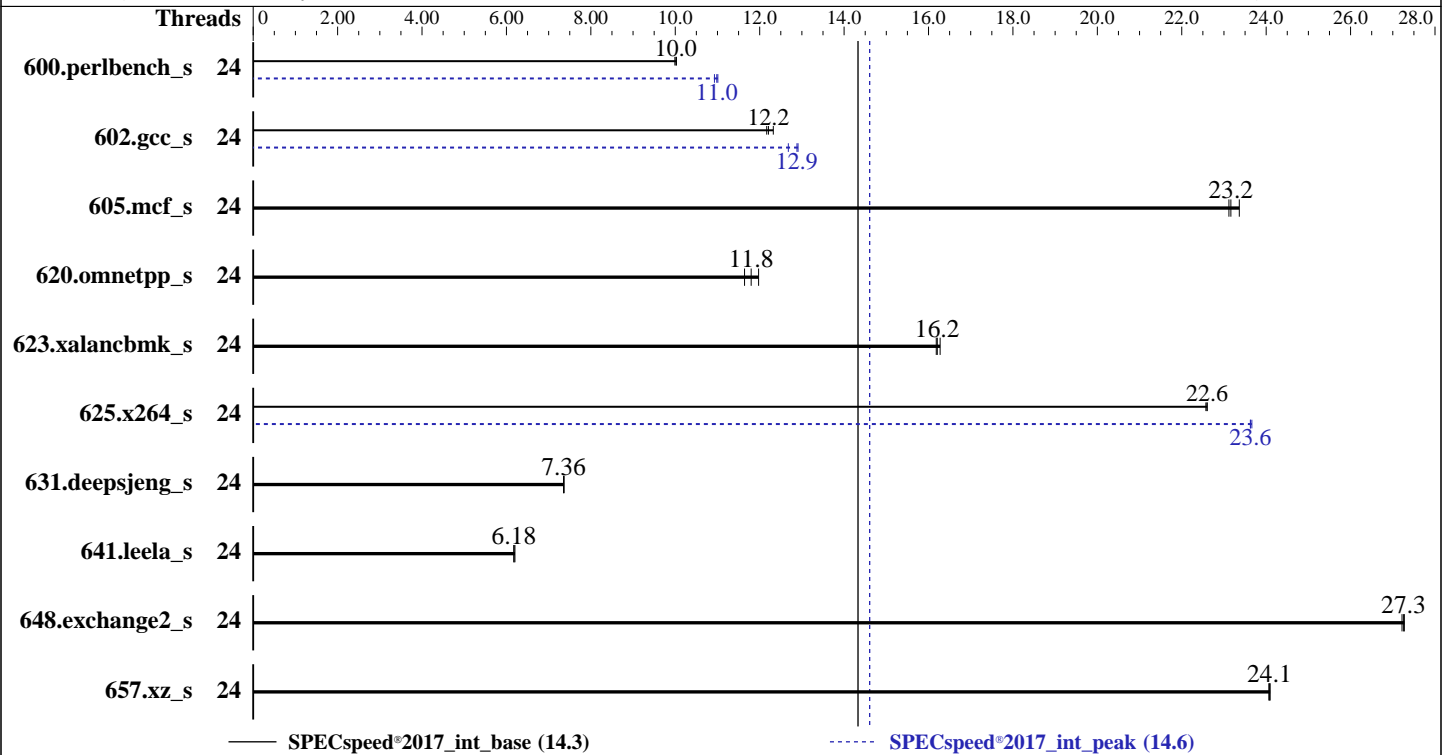
Test Date: Mar-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Dec-2023

Tested by: IEIT Systems Co., Ltd.

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Silver 4510
 Max MHz: 4100
 Nominal: 2400
 Enabled: 24 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 30 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)
 Storage: 1 x 3.84 TB NVME SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
 5.14.21-150500.53-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Version 05.13.00 released Nov-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	24	178	9.99	<u>177</u>	10.0	177	10.0	24	<u>162</u>	11.0	162	10.9	161	11.0
602.gcc_s	24	326	12.2	327	12.2	323	12.3	24	309	12.9	308	12.9	314	12.7
605.mcf_s	24	204	23.1	202	23.4	204	23.2	24	204	23.1	202	23.4	204	23.2
620.omnetpp_s	24	138	11.8	140	11.6	136	12.0	24	138	11.8	140	11.6	136	12.0
623.xalancbmk_s	24	87.4	16.2	87.1	16.3	87.6	16.2	24	87.4	16.2	87.1	16.3	87.6	16.2
625.x264_s	24	78.0	22.6	78.2	22.6	78.1	22.6	24	74.7	23.6	74.6	23.7	74.7	23.6
631.deepsjeng_s	24	195	7.36	195	7.36	195	7.36	24	195	7.36	195	7.36	195	7.36
641.leela_s	24	275	6.20	276	6.18	276	6.18	24	275	6.20	276	6.18	276	6.18
648.exchange2_s	24	108	27.2	108	27.3	108	27.3	24	108	27.2	108	27.3	108	27.3
657.xz_s	24	257	24.1	257	24.1	257	24.1	24	257	24.1	257	24.1	257	24.1

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Intel Hyper Threading Technology set to Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat Mar 2 07:14:18 2024

SUT (System Under Test) info as seen by some common utilities.

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10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
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17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
07:14:18 up 1 min, 1 user, load average: 0.58, 0.37, 0.14
USER  TTY  FROM          LOGIN@  IDLE   JCPU   PCPU WHAT
root  tty1  -             07:13   10.00s 0.97s  0.00s -bash
```

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Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```

core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 4124657
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 4124657
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=24 --tune base,peak -o all --define
  intspeedaffinity --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=24 --tune base,peak --output_format all
  --define intspeedaffinity --define drop_caches --nopower --runmode speed --tune base:peak --size refspped
  intspeed --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.006/templogs/preenv.intspeed.006.0.log
  --lognum 006.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) SILVER 4510
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000461
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 12
siblings       : 12
2 physical ids (chips)
24 processors (hardware threads)
physical id 0: core ids 0-11
physical id 1: core ids 0-11
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22
physical id 1: apicids 64,66,68,70,72,74,76,78,80,82,84,86
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

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Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        52 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               24
On-line CPU(s) list:  0-23
Vendor ID:            GenuineIntel
Model name:           INTEL(R) XEON(R) SILVER 4510
CPU family:           6
Model:                143
Thread(s) per core:   1
Core(s) per socket:  12
Socket(s):            2
Stepping:             8
CPU max MHz:          4100.0000
CPU min MHz:          800.0000
BogoMIPS:             4800.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                    x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                    abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                    intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                    tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
                    avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd
                    sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
                    cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni
                    avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
                    hwp_pkg_req avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                    vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                    bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                    tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities

L1d cache:            1.1 MiB (24 instances)
L1i cache:            768 KiB (24 instances)
L2 cache:             48 MiB (24 instances)
L3 cache:             60 MiB (2 instances)
NUMA node(s):        2
NUMA node0 CPU(s):   0-11
NUMA node1 CPU(s):   12-23
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:   Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                    sequence
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE

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Platform Notes (Continued)

L1d	48K	1.1M	12 Data	1	64	1	64
L1i	32K	768K	8 Instruction	1	64	1	64
L2	2M	48M	16 Unified	2	2048	1	64
L3	30M	60M	15 Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-11
node 0 size: 515553 MB
node 0 free: 509167 MB
node 1 cpus: 12-23
node 1 size: 515634 MB
node 1 free: 513933 MB
node distances:
node  0  1
  0: 10 21
  1: 21 10

```

9. /proc/meminfo

MemTotal: 1055936720 kB

10. who -r

run-level 3 Mar 2 07:13

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled ModemManager apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings
kdump kdump-early nvme-fc-boot-connections postfix purge-kernels rollback sshd
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
wpa_supplicant
enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online boot-sysctl
ca-certificates chrony-wait chronyd console-getty debug-shell dnsmasq ebttables
exchange-bmc-os-info grub2-once haveged haveged-switch-root ipmievd issue-add-ssh-keys
kexec-load lunmask nfs nfs-blkmap nvme-fc-autoconnect rpcbind rpmconfigcheck serial-getty@
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd wpa_supplicant@
indirect wickedd

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=a59515e6-248a-4127-a0b3-55e3eb914592
splash=silent
resume=/dev/disk/by-uuid/6c324f52-0c62-433d-ac4d-77e32d920d61
mitigations=auto
quiet
security=apparmor
crashkernel=451M,high
crashkernel=72M,low

```

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Platform Notes (Continued)

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 4.10 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness   20
vm.dirty_background_bytes     0
vm.dirty_background_ratio     10
vm.dirty_bytes                0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages   0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs   2.5T   70G  2.5T   3% /home

```

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Platform Notes (Continued)

```

-----
20. /sys/devices/virtual/dmi/id
   Vendor:          IEIT SYSTEMS
   Product:         NF5466-M7-A0-R0-00
   Product Family:  Not specified
   Serial:          000000000

```

```

-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Samsung M321R8GA0PB0-CWMMXH 64 GB 2 rank 5600, configured at 4400

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:      American Megatrends International, LLC.
   BIOS Version:     05.13.00
   BIOS Date:        11/30/2023

```

Compiler Version Notes

```

=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
      | 657.xz_s(base, peak)

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```

```

=====
C++   | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
      | 641.leela_s(base, peak)

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```

```

=====
Fortran | 648.exchange2_s(base, peak)

```

```

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

```

Base Compiler Invocation

C benchmarks:
icx

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```



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Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 14.3

NF5466M7 (Intel Xeon Silver 4510)

SPECspeed®2017_int_peak = 14.6

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Hardware Availability: Dec-2023

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.0.xml>

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