



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R7525 (AMD EPYC 7513 32-Core Processor)

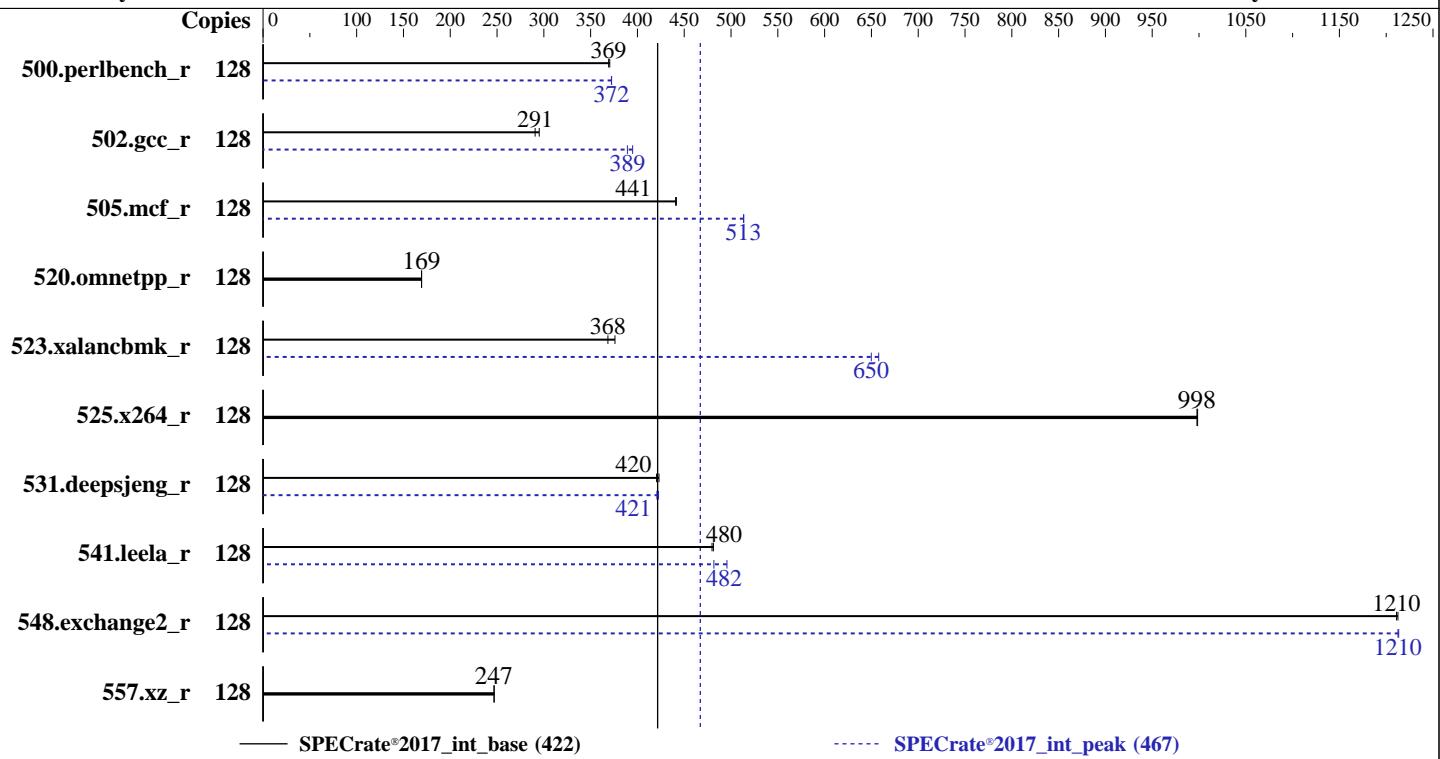
CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017_int_base = 422

SPECrate®2017_int_peak = 467



Hardware

CPU Name: AMD EPYC 7513
Max MHz: 3650
Nominal: 2600
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 512 KB I+D on chip per core
L3: 128 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 80 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: Ubuntu 22.04.4 LTS
Compiler: 5.15.0-101-generic
Parallel: C/C++/Fortran: Version 3.2.0 of AOCC
Firmware: No
File System: Version 2.8.4 released Jun-2022
System State: tmpfs
Base Pointers: Run level 5 (graphical multi-user)
Peak Pointers: 64-bit
Other: 32/64-bit
Power Management: jemalloc: jemalloc memory allocator library v5.1.0
BIOS and OS set to prefer performance at the cost of additional power usage.



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Test Date: Apr-2024

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Hardware Availability: Jun-2021

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Software Availability: Mar-2024

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	550	370	552	369			128	547	372	547	372				
502.gcc_r	128	614	295	623	291			128	466	389	459	395				
505.mcf_r	128	468	442	469	441			128	403	514	403	513				
520.omnetpp_r	128	991	169	992	169			128	991	169	992	169				
523.xalancbmk_r	128	367	368	359	376			128	208	650	205	658				
525.x264_r	128	224	999	225	998			128	224	999	225	998				
531.deepsjeng_r	128	347	423	349	420			128	347	423	349	421				
541.leela_r	128	440	481	442	480			128	428	496	440	482				
548.exchange2_r	128	277	1210	277	1210			128	277	1210	276	1210				
557.xz_r	128	560	247	560	247			128	560	247	560	247				

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.9-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib;/mnt/ramdisk/cpu2017-1.1.9-
    cc320-A1/amd_rate_aocc320_milanx_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk_r peak run:

```
MALLOC_CONF = "thp:never"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```
    DRAM Refresh Delay : Performance
    Memory Interleaving : Disabled
    DIMM Self Healing on
Uncorrectable Memory Error : Disabled
```

Virtualization Technology : Disabled

NUMA Nodes per Socket : 4

L3 cache as NUMA Domain : Enabled

System Profile : Custom

CPU Power Management : Maximum Performance

Memory Patrol Scrub : Disabled

PCI ASPM L1 Link

Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on spa-amd Tue Apr 9 15:32:17 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a

2. w

(Continued on next page)



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Platform Notes (Continued)

```
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
-----
-----
1. uname -a
Linux spa-amd 5.15.0-101-generic #111-Ubuntu SMP Tue Mar 5 20:16:58 UTC 2024 x86_64 x86_64 x86_64 GNU/Linux
-----
2. w
15:32:17 up 10 min, 1 user, load average: 0.13, 0.03, 0.01
USER      TTY      FROM          LOGIN@    IDLE      JCPU      PCPU WHAT
root      ttys1          -           15:31    33.00s   1.66s   0.36s /bin/bash ./amd_rate_aocc320_milanx_A1.sh
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 2097152
process            4126153
nofiles             1024
vmmemory(kbytes)   unlimited
locks               unlimited
rtprio              0
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
```

(Continued on next page)



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Platform Notes (Continued)

```
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
python3 ./run_amd_rate_aocc320_milanx_A1.py
/bin/bash ./amd_rate_aocc320_milanx_A1.sh
runcpu --config amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt intrate
runcpu --configfile amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 7513 32-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 1
stepping        : 1
microcode       : 0xa0011d1
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size        : 2560 4K pages
cpu cores      : 32
siblings        : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 64-127
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu
```

```
From lscpu from util-linux 2.37.2:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          48 bits physical, 48 bits virtual
Byte Order:              Little Endian
CPU(s):                 128
On-line CPU(s) list:    0-127
Vendor ID:               AuthenticAMD
Model name:              AMD EPYC 7513 32-Core Processor
CPU family:              25
Model:                  1
Thread(s) per core:     2
Core(s) per socket:     32
Socket(s):              2
Stepping:                1
BogoMIPS:                5189.78
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                           clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                           lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf
                           rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic
                           movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
```

(Continued on next page)



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Platform Notes (Continued)

```
cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13
cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
fsgsbase bmil avx2 smep bmi2 invpcid cqm rdt_a rdseed adx snap
clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc
cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero iperf xsaveerptr
rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
v_vmsave_vmlload vgif v_spec_ctrl umip pku ospke vaes vpclmulqdq rdpid
overflow_recov succor smca
```

Virtualization:

L1d cache:	AMD-V
L1i cache:	2 MiB (64 instances)
L2 cache:	2 MiB (64 instances)
L3 cache:	32 MiB (64 instances)

NUMA node(s):

NUMA node0 CPU(s):	8
NUMA node1 CPU(s):	0-7,64-71
NUMA node2 CPU(s):	8-15,72-79
NUMA node3 CPU(s):	16-23,80-87
NUMA node4 CPU(s):	24-31,88-95
NUMA node5 CPU(s):	32-39,96-103
NUMA node6 CPU(s):	40-47,104-111
NUMA node7 CPU(s):	48-55,112-119
NUMA node8 CPU(s):	56-63,120-127

Vulnerability Gather data sampling:

Vulnerability Itlb multihit:

Vulnerability Llft:

Vulnerability Mds:

Vulnerability Meltdown:

Vulnerability Mmio stale data:

Vulnerability Retbleed:

Vulnerability Spec rstack overflow:

Vulnerability Spec store bypass:

Vulnerability Spectre v1:

Vulnerability Spectre v2:

Vulnerability Srbds:

Vulnerability Tsx async abort:

Mitigation: safe RET

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

Mitigation: usercopy/swapgs barriers and __user pointer sanitization

Mitigation: Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	2M	8	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	512K	32M	8	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-7,64-71

node 0 size: 128580 MB

node 0 free: 128041 MB

node 1 cpus: 8-15,72-79

node 1 size: 129019 MB

node 1 free: 128603 MB

node 2 cpus: 16-23,80-87

node 2 size: 129019 MB

node 2 free: 128581 MB

node 3 cpus: 24-31,88-95

(Continued on next page)



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Platform Notes (Continued)

```
node 3 size: 129007 MB
node 3 free: 128556 MB
node 4 cpus: 32-39,96-103
node 4 size: 129019 MB
node 4 free: 125138 MB
node 5 cpus: 40-47,104-111
node 5 size: 128972 MB
node 5 free: 128531 MB
node 6 cpus: 48-55,112-119
node 6 size: 129019 MB
node 6 free: 128606 MB
node 7 cpus: 56-63,120-127
node 7 size: 129013 MB
node 7 free: 128618 MB
node distances:
node   0   1   2   3   4   5   6   7
 0: 10  12  12  12  32  32  32  32
 1: 12  10  12  12  32  32  32  32
 2: 12  12  10  12  32  32  32  32
 3: 12  12  12  10  32  32  32  32
 4: 32  32  32  32  10  12  12  12
 5: 32  32  32  32  12  10  12  12
 6: 32  32  32  32  12  12  10  12
 7: 32  32  32  32  12  12  12  10
```

9. /proc/meminfo
MemTotal: 1056411092 kB

10. who -r
run-level 5 Apr 9 15:21

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
Default Target Status
graphical running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor blk-availability cloud-config cloud-final cloud-init
 cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
 grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor
 lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog
 secureboot-db setvtrgb ssh systemd-networkd systemd-pstore systemd-resolved
 systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgaauth
 systemplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
 console-getty debug-shell iscsid nftables rsync serial-getty@
 systemd-boot-check-no-failures systemd-network-generator systemd-sysext
 systemd-time-wait-sync upower
generated apport
indirect uuidd
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
 systemd-networkd-wait-online x11-common

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-5.15.0-101-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv

(Continued on next page)



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Platform Notes (Continued)

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```
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2600MHz
```

```
15. tuned-adm active
Current active profile: latency-performance
```

```
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       3
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```

```
17. /sys/kernel/mm/transparent_hugepage
defrag           always defer defer+madvise [madvise] never
enabled          always [madvise] never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag               1
max_ptes_none        511
max_ptes_shared      256
max_ptes_swap        64
pages_to_scan        4096
scan_sleep_millisecs 10000
```

```
19. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.4 LTS
```

(Continued on next page)



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Platform Notes (Continued)

20. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 80G 3.4G 77G 5% /mnt/ramdisk

21. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge R7525
Product Family: PowerEdge
Serial: GK2D853

22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 80AD869D80AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200
16x Not Specified Not Specified

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 2.8.4
BIOS Date: 06/23/2022
BIOS Revision: 2.8

Compiler Version Notes

=====

C | 502.gcc_r(peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

=====

C | 502.gcc_r(peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu

(Continued on next page)



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Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

C++ | 523.xalancbmk_r(peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base,
| peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

C++ | 523.xalancbmk_r(peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base,
| peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

Fortran | 548.exchange2_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin



SPEC CPU®2017 Integer Rate Result

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Dell Inc.

PowerEdge R7525 (AMD EPYC 7513 32-Core Processor)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017_int_base = 422

SPECrate®2017_int_peak = 467

Test Date: Apr-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-fsto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang

C++ benchmarks:

-m64 -std=c++98 -fsto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM  
-ffast-math -mllvm -enable-partial-unswitch  
-mllvm -unroll-threshold=100 -finline-aggressive  
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000  
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch  
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false  
-mllvm -enable-loop-fusion -z muldefs -fvirtual-function-elimination  
-fvisibility=hidden -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4  
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split  
-flto -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM  
-ffast-math -z muldefs -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang
```

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang



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Test Date: Apr-2024

Hardware Availability: Jun-2021

Software Availability: Mar-2024

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flio
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=false
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

```
502.gcc_r: -m32 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flio
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -fgnu89-inline
-ljemalloc
```

```
505.mcf_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flio
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
```

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Hardware Availability: Jun-2021

Software Availability: Mar-2024

Peak Optimization Flags (Continued)

505.mcf_r (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

```
523.xalancbmk_r: -m32 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-ljemalloc
```

```
531.deepsjeng_r: -m64 -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -ljemalloc
```

541.leela_r: Same as 531.deepsjeng_r

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Software Availability: Mar-2024

Peak Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4  
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split  
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIB -ffast-math -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang
```

Peak Other Flags

C benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
502.gcc_r: -L/usr/lib -Wno-unused-command-line-argument  
-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32
```

C++ benchmarks (except as noted below):

```
-Wno-unused-command-line-argument
```

```
523.xalancbmk_r: -L/usr/lib -Wno-unused-command-line-argument  
-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2024-04-09 11:32:17-0400.

Report generated on 2024-05-07 22:17:23 by CPU2017 PDF formatter v6716.

Originally published on 2024-05-07.