



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 106

## PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

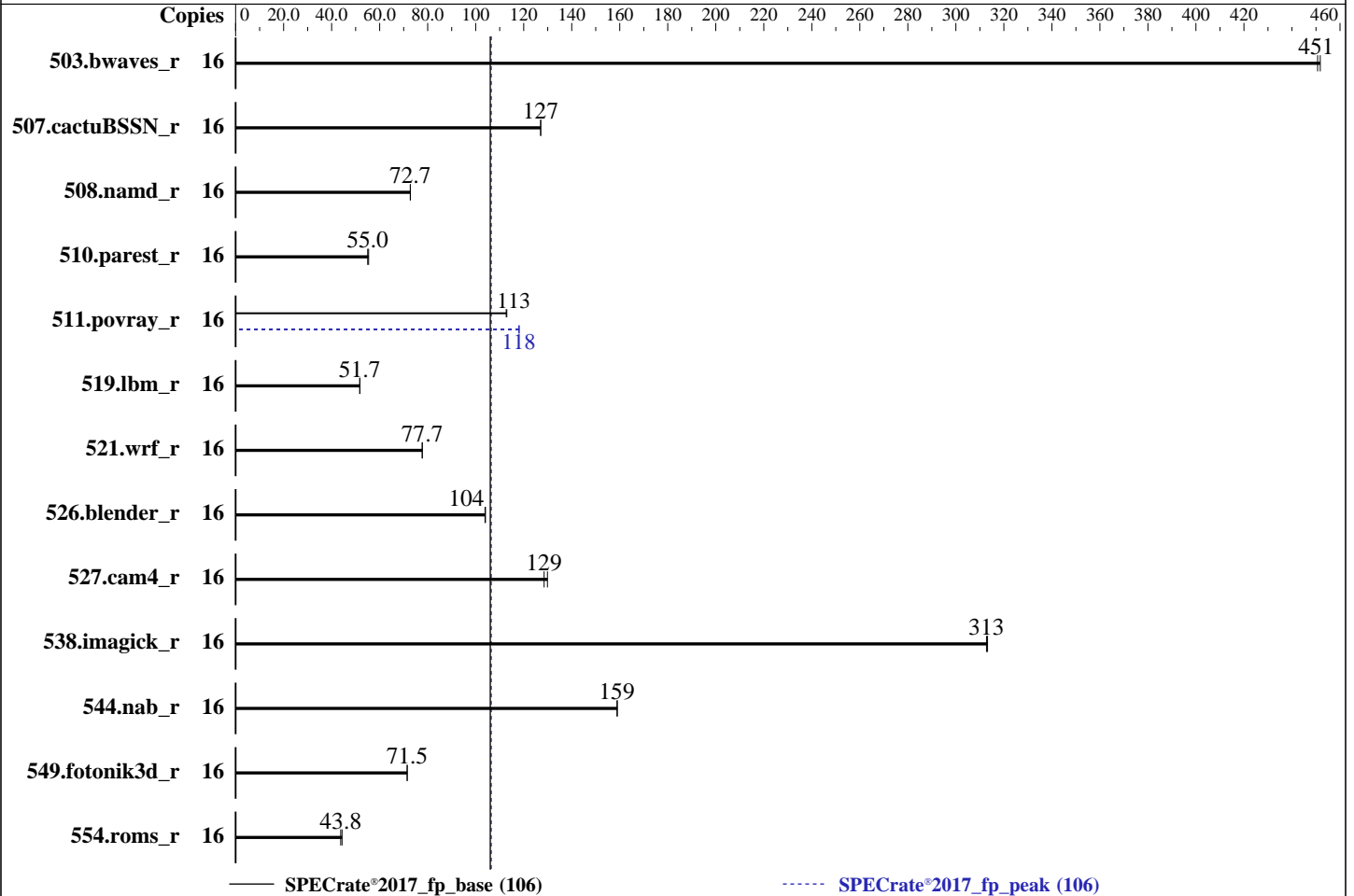
Test Date: Mar-2024

Test Sponsor: Dell Inc.

Hardware Availability: May-2024

Tested by: Dell Inc.

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon E-2488  
 Max MHz: 5600  
 Nominal: 3200  
 Enabled: 8 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 24 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)  
 Storage: 40 GB on tmpfs  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++  
 Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran  
 Compiler for Linux;  
 Parallel: No  
 Firmware: Version 1.2.2 released Mar-2024  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance  
 at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2024  
Hardware Availability: May-2024  
Software Availability: Dec-2023

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	16	355	452	<b>356</b>	<b>451</b>			16	355	452	<b>356</b>	<b>451</b>		
507.cactuBSSN_r	16	<b>159</b>	<b>127</b>	159	127			16	<b>159</b>	<b>127</b>	159	127		
508.namd_r	16	209	72.8	<b>209</b>	<b>72.7</b>			16	209	72.8	<b>209</b>	<b>72.7</b>		
510.parest_r	16	756	55.4	<b>761</b>	<b>55.0</b>			16	756	55.4	<b>761</b>	<b>55.0</b>		
511.povray_r	16	331	113	<b>331</b>	<b>113</b>			16	<b>317</b>	<b>118</b>	316	118		
519.lbm_r	16	326	51.7	<b>326</b>	<b>51.7</b>			16	326	51.7	<b>326</b>	<b>51.7</b>		
521.wrf_r	16	461	77.7	<b>461</b>	<b>77.7</b>			16	461	77.7	<b>461</b>	<b>77.7</b>		
526.blender_r	16	<b>235</b>	<b>104</b>	234	104			16	<b>235</b>	<b>104</b>	234	104		
527.cam4_r	16	215	130	<b>218</b>	<b>129</b>			16	215	130	<b>218</b>	<b>129</b>		
538.imagick_r	16	<b>127</b>	<b>313</b>	127	313			16	<b>127</b>	<b>313</b>	127	313		
544.nab_r	16	<b>170</b>	<b>159</b>	169	159			16	<b>170</b>	<b>159</b>	169	159		
549.fotonik3d_r	16	873	71.5	<b>873</b>	<b>71.5</b>			16	873	71.5	<b>873</b>	<b>71.5</b>		
554.roms_r	16	<b>581</b>	<b>43.8</b>	574	44.3			16	<b>581</b>	<b>43.8</b>	574	44.3		

SPECrate®2017\_fp\_base = 106

SPECrate®2017\_fp\_peak = 106

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

## General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

DIMM Self Healing on  
Uncorrectable Memory Error : Disabled

Virtualization Technology : Disabled  
LLC Prefetch : Disabled  
Dead Line LLC Alloc : Disabled

System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Fri Mar 29 17:07:43 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2024  
Hardware Availability: May-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

```

-----
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043/lp)
x86_64 x86_64 x86_64 GNU/Linux

-----
2. w
17:07:43 up 3:57, 1 user, load average: 13.36, 15.41, 15.75
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root     tty1      -             13:16       3:13m      1.21s  0.00s  /bin/bash ./dell-run-speccpu.sh rate
--define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
html,pdf,txt --define DL-LQC=1

-----
3. Username
From environment variable $USER: root

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 254884
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes      (-u) 254884
virtual memory           (kbytes, -v) unlimited
file locks               (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
--define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
--define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 -c
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=8 --define physicallogical
--define no-numa --tune base,peak -o all --define drop_caches --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
html,pdf,txt --define DL-LQC=1 fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 --configfile
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=8 --define physicallogical
--define no-numa --tune base,peak --output_format all --define drop_caches --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
html,pdf,txt --define DL-LQC=1 --nopower --runmode rate --tune base:peak --size refrate fprate --nopreenv

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2024  
Hardware Availability: May-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

```
--note-preenv --logfile $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
```

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) E E-2488
vendor_id       : GenuineIntel
cpu family      : 6
model           : 183
stepping        : 1
microcode       : 0x121
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 8
siblings        : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 0: apicids 0-15
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      42 bits physical, 48 bits virtual
Byte Order:         Little Endian
CPU(s):             16
On-line CPU(s) list: 0-15
Vendor ID:          GenuineIntel
Model name:         Intel(R) Xeon(R) E E-2488
CPU family:         6
Model:              183
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s):          1
Stepping:           1
BogoMIPS:           6374.40
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                   ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic
                   movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                   3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp
                   ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed
                   adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                   split_lock_detect avx_vnni dtherm ida arat pln pts hfi umip pku ospke
                   waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear
                   serialize_pconfig arch_lbr flush_l1d arch_capabilities

L1d cache:         384 KiB (8 instances)
L1i cache:         256 KiB (8 instances)
L2 cache:          16 MiB (8 instances)
L3 cache:          24 MiB (1 instance)
NUMA node(s):      1
NUMA node0 CPU(s): 0-15
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Date: Mar-2024

Test Sponsor: Dell Inc.

Hardware Availability: May-2024

Tested by: Dell Inc.

Software Availability: Dec-2023

## Platform Notes (Continued)

Vulnerability Itlb multihit: Not affected  
 Vulnerability Lltf: Not affected  
 Vulnerability Mds: Not affected  
 Vulnerability Meltdown: Not affected  
 Vulnerability Mmio stale data: Not affected  
 Vulnerability Retbleed: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	384K	12	Data	1	64	1	64
L1i	32K	256K	8	Instruction	1	64	1	64
L2	2M	16M	16	Unified	2	2048	1	64
L3	24M	24M	12	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-15
node 0 size: 63755 MB
node 0 free: 54619 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

MemTotal: 65285796 kB

10. who -r

run-level 3 Mar 29 13:10

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```
Default Target Status
multi-user      running
```

12. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings kdump
kdump-early klog lvm2-monitor nscd nvme-fc-boot-connections postfix purge-kernels rollback
rsyslog sapconf smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
wickedd-dhcp6 wickedd-nanny wpa_supplicant xrdp
enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofsd
autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
chrony-wait chronyd console-getty cups cups-browsed debug-shell dmraid-activation dnsmasq
eatables exchange-bmc-os-info firewallld gpm grub2-once haveged haveged-switch-root
hwloc-dump-hwdata ipmi ipmievd irqbindall issue-add-ssh-keys kexec-load ksm kvm_stat
lunmask man-db-create multipathd nfs nfs-blkmap nmb nvme-autoconnect openvpn@
ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon saptone serial-getty@
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```

indirect
set_kthread_prio smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd svnservice
sysstat systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
wpa_supplicant@ xrdp-sesman
pcscd saned@ uidd wickedd

```

### 13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=/dev/mapper/system-root
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=308M,high
crashkernel=72M,low

```

### 14. cpupower frequency-info

```

analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

```

### 15. sysctl

```

kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 314572800
vm.dirty_background_ratio 0
vm.dirty_bytes 629145600
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 0
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

```

### 16. /sys/kernel/mm/transparent\_hugepage

```

defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```

alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2024  
Hardware Availability: May-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

```
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

-----  
18. OS release  
From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP5  
-----

-----  
19. Disk information  
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 40G 4.1G 36G 11% /mnt/ramdisk  
-----

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge R260  
Product Family: PowerEdge  
Serial: 1234567  
-----

-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
2x 00AD00001201 HMC888AEBEA081N 32 GB 2 rank 4800, configured at 4400  
-----

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.  
BIOS Version: 1.2.2  
BIOS Date: 03/21/2024  
BIOS Revision: 1.2  
-----

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
-----

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Mar-2024  
**Hardware Availability:** May-2024  
**Software Availability:** Dec-2023

## Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using both C and C++:  
icpx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2024  
Hardware Availability: May-2024  
Software Availability: Dec-2023

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

### Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

## Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 106

PowerEdge R260 (Intel Xeon E-2488)

SPECrate®2017\_fp\_peak = 106

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-03-29 13:07:43-0400.

Report generated on 2024-06-03 08:34:33 by CPU2017 PDF formatter v6716.

Originally published on 2024-06-01.