



SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 263

H3C UniServer R4700 G6 (Intel Xeon Gold 5415+)

SPECrate®2017_fp_peak = 267

CPU2017 License: 9066

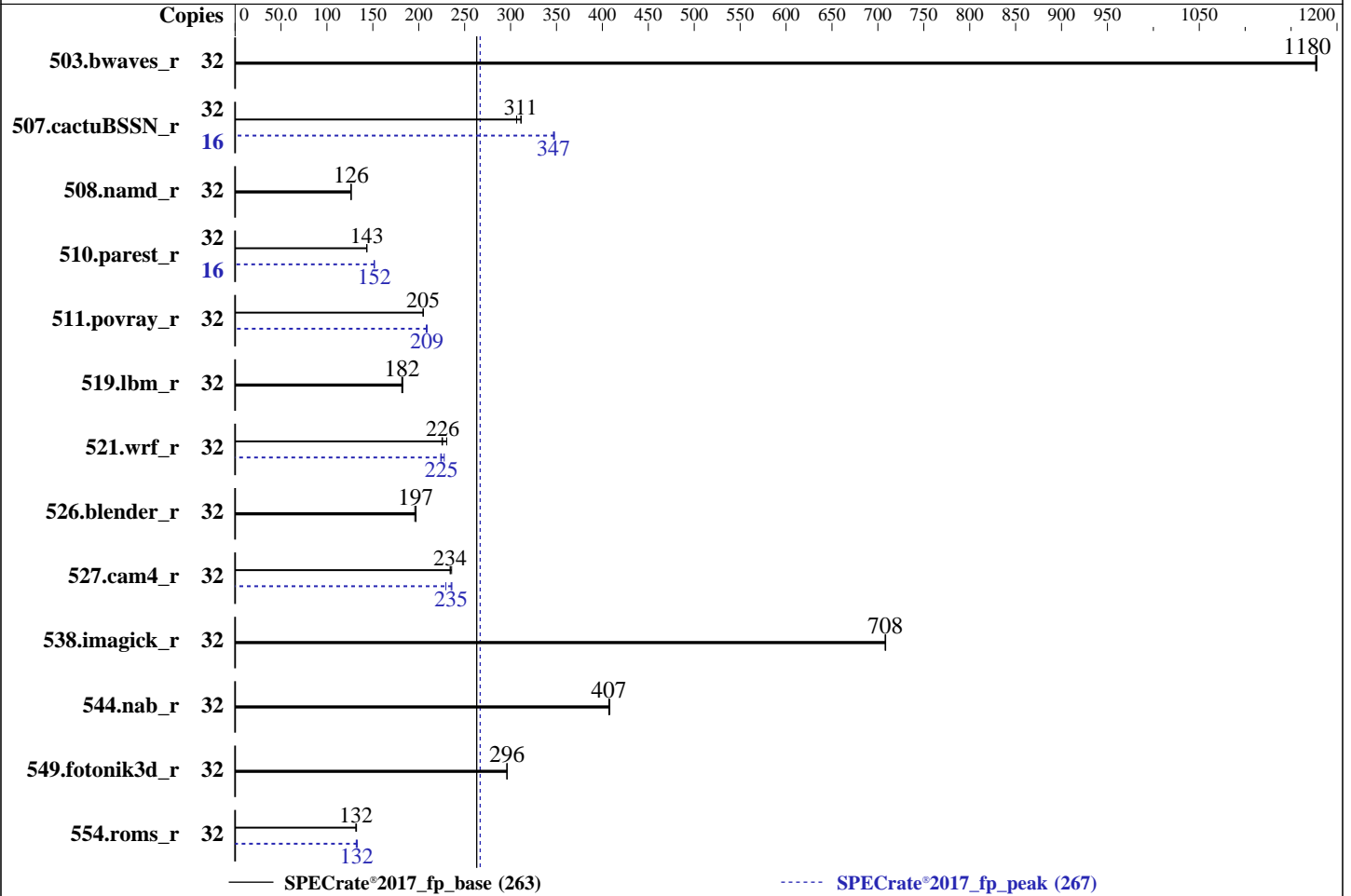
Test Date: Apr-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2023

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024



Hardware

CPU Name: Intel Xeon Gold 5415+
 Max MHz: 4100
 Nominal: 2900
 Enabled: 16 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 22.5 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx8 PC5-4800B-R, running at 4400)
 Storage: 1 x 1.92 TB SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 5.31 released Dec-2023 BIOS
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	<u>273</u>	<u>1180</u>	273	1180	272	1180	32	<u>273</u>	<u>1180</u>	273	1180	272	1180
507.cactuBSSN_r	32	130	312	<u>130</u>	<u>311</u>	132	307	16	58.4	347	<u>58.4</u>	<u>347</u>	58.2	348
508.namd_r	32	241	126	<u>241</u>	<u>126</u>	241	126	32	241	126	<u>241</u>	<u>126</u>	241	126
510.parest_r	32	<u>584</u>	<u>143</u>	584	143	583	144	16	<u>276</u>	<u>152</u>	276	152	276	151
511.povray_r	32	365	205	364	205	<u>365</u>	<u>205</u>	32	357	209	<u>358</u>	<u>209</u>	359	208
519.lbm_r	32	186	182	<u>185</u>	<u>182</u>	185	183	32	186	182	<u>185</u>	<u>182</u>	185	183
521.wrf_r	32	311	230	318	225	<u>317</u>	<u>226</u>	32	315	228	320	224	<u>319</u>	<u>225</u>
526.blender_r	32	249	196	247	197	<u>248</u>	<u>197</u>	32	249	196	247	197	<u>248</u>	<u>197</u>
527.cam4_r	32	237	236	239	234	<u>239</u>	<u>234</u>	32	244	230	237	236	<u>238</u>	<u>235</u>
538.imagick_r	32	112	708	112	709	<u>112</u>	<u>708</u>	32	112	708	112	709	<u>112</u>	<u>708</u>
544.nab_r	32	<u>132</u>	<u>407</u>	132	407	132	408	32	<u>132</u>	<u>407</u>	132	407	132	408
549.fotonik3d_r	32	421	296	<u>421</u>	<u>296</u>	421	296	32	421	296	<u>421</u>	<u>296</u>	421	296
554.roms_r	32	387	132	<u>386</u>	<u>132</u>	385	132	32	385	132	382	133	<u>384</u>	<u>132</u>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Set SNC to Enable SNC2 (2-clusters)

Set Power Performance Tuning to BIOS Controls EFB

Set ENERGY_PERF_BIAS_CFG mode to Performance

Sysinfo program /home/speccpu/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Mon Apr 22 22:34:53 2024

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023
(b630043) x86_64 x86_64 x86_64 GNU/Linux

2. w
22:34:53 up 8:51, 2 users, load average: 19.72, 28.83, 30.71
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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Platform Notes (Continued)

```

root      tty1      -                13:44    5:05m 0.95s 0.01s /bin/sh
./reportable-ic2024.0.2-lin-sapphirerapids-rate-smt-on-20231213.sh
root      pts/0      172.16.53.190   13:44    5:03m 40.91s 40.85s top

```

3. Username

From environment variable \$USER: root

4. ulimit -a

```

core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4124985
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4124985
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/sh ./reportable-ic2024.0.2-lin-sapphirerapids-rate-smt-on-20231213.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.038/templogs/preenv.fprate.038.0.log --lognum 038.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/speccpu

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Gold 5415+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 7
microcode      : 0x2b0004d0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 8
siblings       : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15

```

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Platform Notes (Continued)

physical id 1: apicids 128-143

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                52 bits physical, 57 bits virtual
Byte Order:                   Little Endian
CPU(s):                       32
On-line CPU(s) list:         0-31
Vendor ID:                    GenuineIntel
Model name:                   Intel(R) Xeon(R) Gold 5415+
CPU family:                   6
Model:                        143
Thread(s) per core:          2
Core(s) per socket:          8
Socket(s):                    2
Stepping:                     7
CPU max MHz:                  4100.0000
CPU min MHz:                  800.0000
BogoMIPS:                     5800.00
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                                tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                                avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                                ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                                amx_tile flush_l1d arch_capabilities

Virtualization:              VT-x
L1d cache:                   768 KiB (16 instances)
L1i cache:                   512 KiB (16 instances)
L2 cache:                     32 MiB (16 instances)
L3 cache:                     45 MiB (2 instances)
NUMA node(s):                4
NUMA node0 CPU(s):           0-3,16-19
NUMA node1 CPU(s):           4-7,20-23
NUMA node2 CPU(s):           8-11,24-27
NUMA node3 CPU(s):           12-15,28-31
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:      Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:      Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp

```

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Platform Notes (Continued)

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-3,16-19
node 0 size: 257552 MB
node 0 free: 249332 MB
node 1 cpus: 4-7,20-23
node 1 size: 258045 MB
node 1 free: 252629 MB
node 2 cpus: 8-11,24-27
node 2 size: 258011 MB
node 2 free: 252612 MB
node 3 cpus: 12-15,28-31
node 3 size: 257660 MB
node 3 free: 252180 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10
```

9. /proc/meminfo

MemTotal: 1056020476 kB

10. who -r

run-level 3 Apr 22 13:44

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```
Default Target Status
multi-user degraded
```

12. Failed units, from systemctl list-units --state=failed

```
UNIT                                LOAD ACTIVE SUB DESCRIPTION
* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online
```

13. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online apparmor
auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early
postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
```

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Platform Notes (Continued)

```

enabled-runtime    wickedd-dhcp6 wickedd-nanny wpa_supplicant
disabled          systemd-remount-fs
                  boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell dnsmasq ebttables
                  grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs
                  nfs-blkmap rpcbind rpmconfigcheck serial-getty@ systemd-boot-check-no-failures
                  systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect          wpa_supplicant@
                  wickedd

```

14. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=8941a345-b7dd-4a3e-98f3-0af9eb97f32e
splash=silent
resume=/dev/disk/by-uuid/06a94d0a-0f9e-4e93-848f-045322d8f16d
mitigations=auto
quiet
security=apparmor
crashkernel=373M,high
crashkernel=72M,low
transparent_hugepage=always

```

15. cpupower frequency-info

```

analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 4.10 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
boost state support:
  Supported: yes
  Active: yes

```

16. tuned-adm active

```

It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

```

17. sysctl

```

kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode          0

```

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Platform Notes (Continued)

```
18. /sys/kernel/mm/transparent_hugepage
    defrag          always defer defer+madvice [madvice] never
    enabled         [always] madvice never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force
```

```
-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                  1
    max_ptes_none           511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs    10000
```

```
-----
20. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP5
```

```
-----
21. Disk information
    SPEC is set to: /home/speccpu
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda3       xfs   1.5T  50G  1.5T   4% /home
```

```
-----
22. /sys/devices/virtual/dmi/id
    Vendor:          H3C
    Product:         RS33M2C9S
    Product Family:  Rack
    Serial:          N/A
```

```
-----
23. dmidecode
    Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
    1x Samsung M321R8GA0BB0-CQKDG 64 GB 2 rank 4800, configured at 4400
    1x Samsung M321R8GA0BB0-CQKEG 64 GB 2 rank 4800, configured at 4400
    14x Samsung M321R8GA0BB0-CQKMG 64 GB 2 rank 4800, configured at 4400
```

```
-----
24. BIOS
    (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:          American Megatrends International, LLC.
    BIOS Version:         6.00.34
    BIOS Date:            12/19/2023
    BIOS Revision:        5.31
```

Compiler Version Notes

```
-----
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----
```

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
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=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
=====

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=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
=====

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Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 263

H3C UniServer R4700 G6 (Intel Xeon Gold 5415+)

SPECrate®2017_fp_peak = 267

CPU2017 License: 9066

Test Date: Apr-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2023

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

507.cactuBSSN_r: -DSPEC_LP64

508.namd_r: -DSPEC_LP64

510.parest_r: -DSPEC_LP64

511.povray_r: -DSPEC_LP64

519.lbm_r: -DSPEC_LP64

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc

-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc

-L/usr/local/jemalloc64-5.0.1/lib

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



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Software Availability: Apr-2024

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)

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Software Availability: Apr-2024

Peak Optimization Flags (Continued)

511.povray_r (continued):

```
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevD.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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